



# SPEC<sup>®</sup> CFP2006 Result

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Dell Inc.

SPECfp<sup>®</sup>2006 = 52.4

PowerEdge T420 (Intel Xeon E5-2407, 2.20 GHz)

SPECfp\_base2006 = 50.4

CPU2006 license: 55

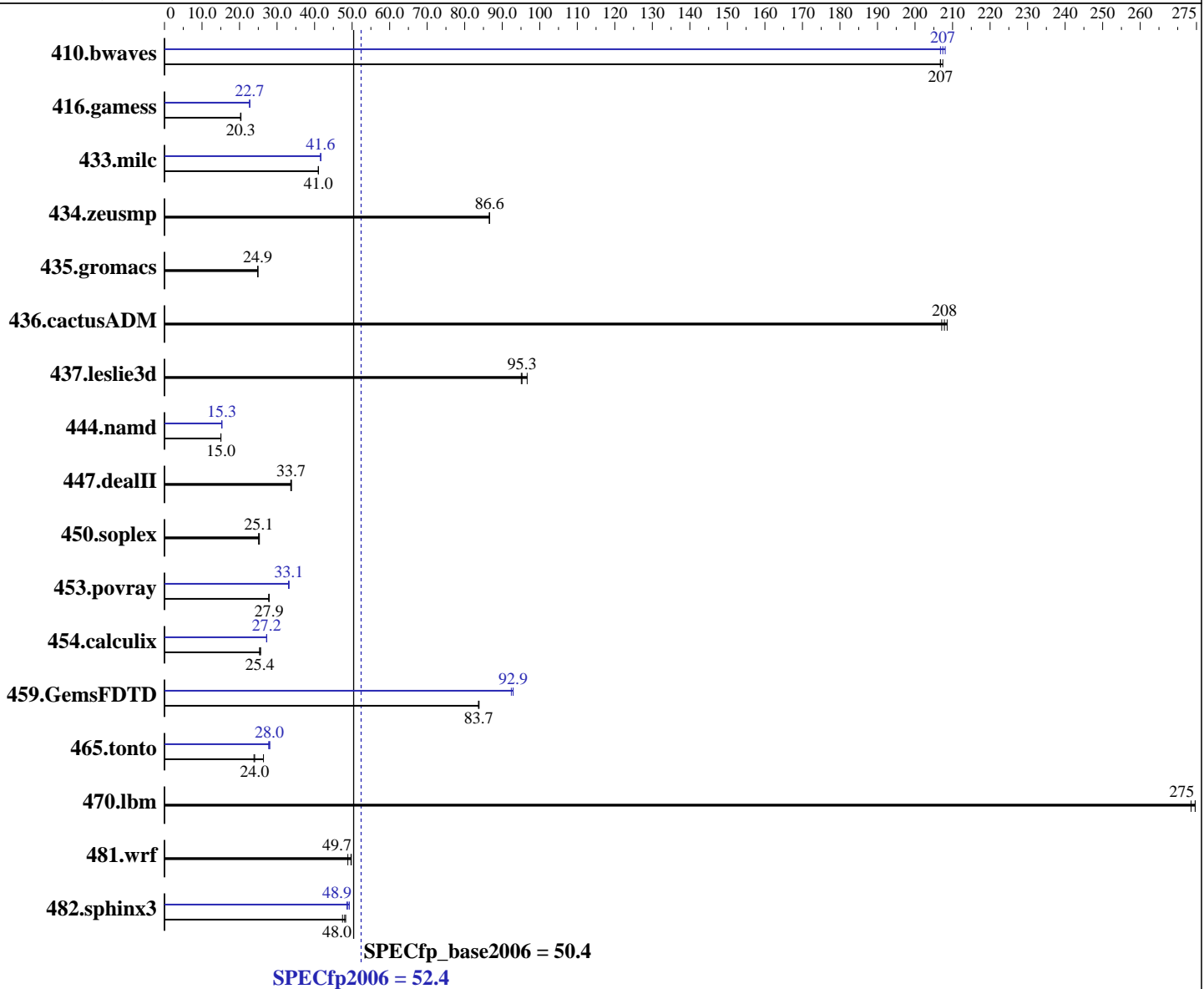
Test date: Jun-2012

Test sponsor: Dell Inc.

Hardware Availability: Jun-2012

Tested by: Dell Inc.

Software Availability: Feb-2012



### Hardware

CPU Name: Intel Xeon E5-2407  
 CPU Characteristics:  
 CPU MHz: 2200  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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### Software

Operating System: SUSE Linux Enterprise Server 11 SP2(x86\_64)  
 3.0.13-0.27-default  
 Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux;  
 Fortran: Version 12.1.0.225 of Intel Fortran Studio XE for Linux  
 Auto Parallel: Yes  
 File System: ext3  
 System State: Run level 3 (multi-user)

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L3 Cache: 10 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 96 GB (12 x 8 GB 2Rx4 PC3-12800R-11, ECC, running at 1067 MHz)  
 Disk Subsystem: 1 x 300 GB 15000 RPM SAS  
 Other Hardware: None

Base Pointers: 64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: None

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	<u>65.7</u>	<u>207</u>	65.5	207	65.7	207	<u>65.5</u>	<u>207</u>	65.3	208	65.7	207
416.gamess	<u>963</u>	<u>20.3</u>	966	20.3	960	20.4	<u>863</u>	<u>22.7</u>	863	22.7	862	22.7
433.milc	224	41.0	224	41.0	<u>224</u>	<u>41.0</u>	221	41.6	<u>221</u>	<u>41.6</u>	221	41.6
434.zeusmp	105	86.6	105	86.6	<u>105</u>	<u>86.6</u>	105	86.6	105	86.6	<u>105</u>	<u>86.6</u>
435.gromacs	287	24.9	<u>287</u>	<u>24.9</u>	287	24.8	287	24.9	<u>287</u>	<u>24.9</u>	287	24.8
436.cactusADM	57.7	207	57.3	209	<u>57.5</u>	<u>208</u>	57.7	207	57.3	209	<u>57.5</u>	<u>208</u>
437.leslie3d	98.9	95.1	97.3	96.6	<u>98.7</u>	<u>95.3</u>	98.9	95.1	97.3	96.6	<u>98.7</u>	<u>95.3</u>
444.namd	533	15.0	<u>533</u>	<u>15.0</u>	534	15.0	525	15.3	525	15.3	<u>525</u>	<u>15.3</u>
447.dealII	<u>339</u>	<u>33.7</u>	338	33.8	340	33.7	<u>339</u>	<u>33.7</u>	338	33.8	340	33.7
450.soplex	<u>332</u>	<u>25.1</u>	332	25.1	330	25.3	<u>332</u>	<u>25.1</u>	332	25.1	330	25.3
453.povray	192	27.8	191	27.9	<u>191</u>	<u>27.9</u>	160	33.2	<u>160</u>	<u>33.1</u>	161	33.0
454.calculix	326	25.3	<u>325</u>	<u>25.4</u>	322	25.6	<u>303</u>	<u>27.2</u>	303	27.2	303	27.3
459.GemsFDTD	<u>127</u>	<u>83.7</u>	127	83.8	127	83.7	115	92.4	<u>114</u>	<u>92.9</u>	114	92.9
465.tonto	<u>409</u>	<u>24.0</u>	373	26.4	413	23.8	<u>352</u>	<u>28.0</u>	354	27.8	351	28.1
470.lbm	50.2	274	50.0	275	<u>50.0</u>	<u>275</u>	50.2	274	50.0	275	<u>50.0</u>	<u>275</u>
481.wrf	<u>225</u>	<u>49.7</u>	229	48.9	224	49.8	<u>225</u>	<u>49.7</u>	229	48.9	224	49.8
482.sphinx3	404	48.3	<u>406</u>	<u>48.0</u>	411	47.4	401	48.6	<u>399</u>	<u>48.9</u>	396	49.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

CPU Power Management set to Maximum Performance  
 Memory Frequency set to Maximum Performance  
 Turbo Boost set to Enabled  
 C States/C1E set to Enabled  
 Sysinfo program /root/CPU2006-1.2/config/sysinfo.rev6800  
 \$Rev: 6800 \$ \$Date:: 2011-10-11 #\$ 6f2ebdff5032aaa42e583f96b07f99d3  
 running on linux-316h Fri Jun 1 18:57:33 2012

This section contains SUT (System Under Test) info as seen by

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## Platform Notes (Continued)

some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```

From /proc/cpuinfo
  model name : Intel(R) Xeon(R) CPU E5-2407 0 @ 2.20GHz
  2 "physical id"s (chips)
  8 "processors"
  cores, siblings (Caution: counting these is hw and system dependent. The
  following excerpts from /proc/cpuinfo might not be reliable. Use with
  caution.)
    cpu cores : 4
    siblings  : 4
    physical 0: cores 0 1 2 3
    physical 1: cores 0 1 2 3
  cache size : 10240 KB

```

```

From /proc/meminfo
MemTotal:      98994188 kB
HugePages_Total:    0
Hugepagesize:    2048 kB

```

```

/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 11 (x86_64)

```

```

From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 11 (x86_64)
VERSION = 11
PATCHLEVEL = 2

```

```

uname -a:
Linux linux-316h 3.0.13-0.27-default #1 SMP Wed Feb 15 13:33:49 UTC 2012
(d73692b) x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 Jun 1 09:55 last=S

```

SPEC is set to: /root/CPU2006-1.2
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda2       ext3  271G  123G  135G  48% /

```

Additional information from dmidecode:

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

```

KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/root/CPU2006-1.2/libs/32:/root/CPU2006-1.2/libs/64"
OMP_NUM_THREADS = "8"

```

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## General Notes (Continued)

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RHEL5.5  
 Transparent Huge Pages disabled with:  
 echo never > /sys/kernel/mm/transparent\_hugepage/enabled  
 Filesystem page cache cleared with:  
 echo 1> /proc/sys/vm/drop\_caches

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
 416.gamess: -DSPEC\_CPU\_LP64  
 433.milc: -DSPEC\_CPU\_LP64  
 434.zeusmp: -DSPEC\_CPU\_LP64  
 435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
 436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
 437.leslie3d: -DSPEC\_CPU\_LP64  
 444.namd: -DSPEC\_CPU\_LP64  
 447.dealII: -DSPEC\_CPU\_LP64  
 450.soplex: -DSPEC\_CPU\_LP64  
 453.povray: -DSPEC\_CPU\_LP64  
 454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
 459.GemsFDTD: -DSPEC\_CPU\_LP64  
 465.tonto: -DSPEC\_CPU\_LP64  
 470.lbm: -DSPEC\_CPU\_LP64  
 481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
 482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch  
-ansi-alias

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## Base Optimization Flags (Continued)

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias

Fortran benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch  
-ansi-alias

## Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32  
-ansi-alias

470.lbm: basepeak = yes

482.sphinx3: -xAVX -ipo -O3 -no-prec-div -unroll2 -ansi-alias  
-parallel

C++ benchmarks:

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## Peak Optimization Flags (Continued)

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias  
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

### Fortran benchmarks:

410.bwaves: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -parallel  
-static

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc  
-opt-malloc-options=3 -auto -unroll4

### Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revA.20120410.00.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revA.20120410.00.xml>



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