



# SPEC<sup>®</sup> CFP2006 Result

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Dell Inc.

SPECfp<sup>®</sup>\_rate2006 = 726

PowerEdge M820 (Intel Xeon E5-4617, 2.90 GHz)

SPECfp\_rate\_base2006 = 708

CPU2006 license: 55

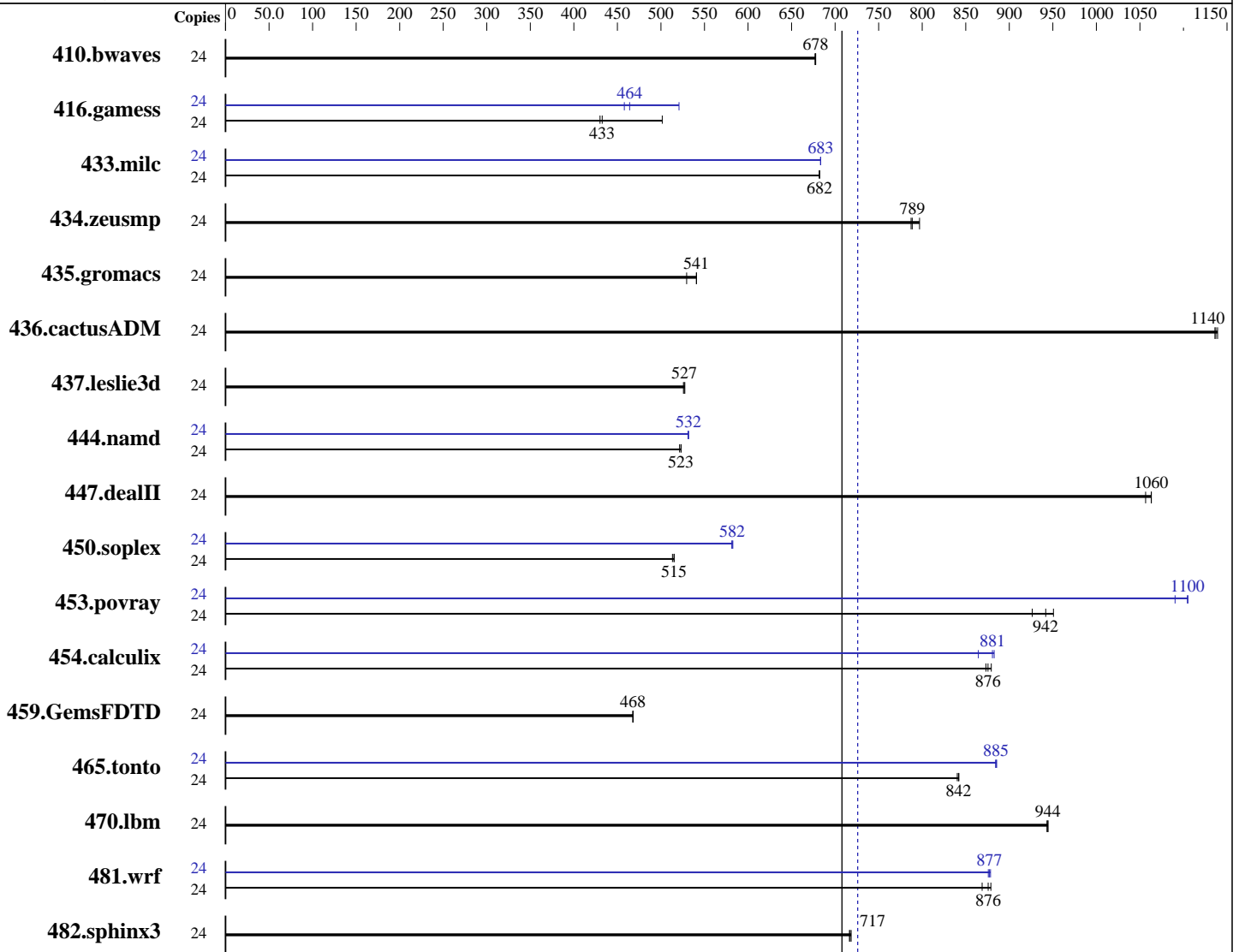
Test date: May-2012

Test sponsor: Dell Inc.

Hardware Availability: Aug-2012

Tested by: Dell Inc.

Software Availability: Feb-2012



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### Hardware

CPU Name: Intel Xeon E5-4617  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.40 GHz  
 CPU MHz: 2900  
 FPU: Integrated  
 CPU(s) enabled: 24 cores, 4 chips, 6 cores/chip  
 CPU(s) orderable: 2,4 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

### Software

Operating System: SUSE Linux Enterprise Server 11 SP2 (x86\_64) 3.0.13-0.27-default  
 Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux;  
 Fortran: Version 12.1.0.225 of Intel Fortran Studio XE for Linux  
 Auto Parallel: No  
 File System: ext3  
 System State: Run level 3 (multi-user)

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L3 Cache: 15 MB I+D on chip per chip  
Other Cache: None  
Memory: 256 GB (32 x 8 GB 2Rx4 PC3-12800R-11, ECC, running at 1333 MHz)  
Disk Subsystem: 1 x 900 GB 10000 RPM SAS  
Other Hardware: None

Base Pointers: 32/64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	24	<b>481</b>	<b>678</b>	482	677	481	678	24	<b>481</b>	<b>678</b>	482	677	481	678
416.gamess	24	937	502	1093	430	<b>1086</b>	<b>433</b>	24	<b>1013</b>	<b>464</b>	902	521	1026	458
433.milc	24	323	682	323	682	<b>323</b>	<b>682</b>	24	322	683	322	683	<b>322</b>	<b>683</b>
434.zeusmp	24	274	797	277	787	<b>277</b>	<b>789</b>	24	274	797	277	787	<b>277</b>	<b>789</b>
435.gromacs	24	<b>317</b>	<b>541</b>	317	541	324	529	24	<b>317</b>	<b>541</b>	317	541	324	529
436.cactusADM	24	<b>252</b>	<b>1140</b>	252	1140	252	1140	24	<b>252</b>	<b>1140</b>	252	1140	252	1140
437.leslie3d	24	428	527	429	526	<b>428</b>	<b>527</b>	24	428	527	429	526	<b>428</b>	<b>527</b>
444.namd	24	368	523	<b>368</b>	<b>523</b>	369	521	24	362	531	<b>362</b>	<b>532</b>	362	532
447.dealII	24	260	1060	258	1060	<b>258</b>	<b>1060</b>	24	260	1060	258	1060	<b>258</b>	<b>1060</b>
450.soplex	24	390	513	389	515	<b>389</b>	<b>515</b>	24	<b>344</b>	<b>582</b>	344	583	344	581
453.povray	24	138	926	<b>136</b>	<b>942</b>	134	951	24	116	1110	117	1090	<b>116</b>	<b>1100</b>
454.calculix	24	<b>226</b>	<b>876</b>	225	879	227	873	24	224	883	229	864	<b>225</b>	<b>881</b>
459.GemsFDTD	24	544	468	<b>544</b>	<b>468</b>	544	468	24	544	468	<b>544</b>	<b>468</b>	544	468
465.tonto	24	281	840	<b>281</b>	<b>842</b>	280	842	24	267	886	<b>267</b>	<b>885</b>	267	884
470.lbm	24	349	944	<b>349</b>	<b>944</b>	350	943	24	349	944	<b>349</b>	<b>944</b>	350	943
481.wrf	24	<b>306</b>	<b>876</b>	309	869	305	879	24	305	878	306	876	<b>306</b>	<b>877</b>
482.sphinx3	24	653	716	651	718	<b>652</b>	<b>717</b>	24	653	716	651	718	<b>652</b>	<b>717</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"



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## Platform Notes

CPU Power Management set to Maximum Performance  
 Memory Frequency set to Maximum Performance  
 Turbo Boost set to Enabled  
 C States/C1E set to Enabled  
 Sysinfo program /root/CPU2006-1.2/config/sysinfo.rev6800  
 \$Rev: 6800 \$ \$Date:: 2011-10-11 #\$ 6f2ebdff5032aaa42e583f96b07f99d3  
 running on linux-b611 Tue May 22 23:02:37 2012

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name      : Intel(R) Xeon(R) CPU E5-4617 0 @ 2.90GHz
 4 "physical id"s (chips)
 24 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  cpu cores    : 6
  siblings     : 6
  physical 0: cores 0 1 2 3 4 5
  physical 1: cores 0 1 2 3 4 5
  physical 2: cores 0 1 2 3 4 5
  physical 3: cores 0 1 2 3 4 5
cache size     : 15360 KB
```

```
From /proc/meminfo
MemTotal:      264468688 kB
HugePages_Total: 0
Hugepagesize:  2048 kB
```

```
/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 11 (x86_64)
```

```
From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 11 (x86_64)
VERSION = 11
PATCHLEVEL = 2
```

```
uname -a:
Linux linux-b611 3.0.13-0.27-default #1 SMP Wed Feb 15 13:33:49 UTC 2012
(d73692b) x86_64 x86_64 x86_64 GNU/Linux
```

run-level 3 May 22 16:02 last=S

```
SPEC is set to: /root/CPU2006-1.2
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sdal       ext3  911G   96G  769G  12% /
```

Additional information from dmidecode:  
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## Platform Notes (Continued)

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

LD\_LIBRARY\_PATH = "/root/CPU2006-1.2/libs/32:/root/CPU2006-1.2/libs/64"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RHEL5.5

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent\_hugepage/enabled

Filesystem page cache cleared with:

echo 1> /proc/sys/vm/drop\_caches

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
 416.gamess: -DSPEC\_CPU\_LP64  
 433.milc: -DSPEC\_CPU\_LP64  
 434.zeusmp: -DSPEC\_CPU\_LP64  
 435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
 436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
 437.leslie3d: -DSPEC\_CPU\_LP64  
 444.namd: -DSPEC\_CPU\_LP64  
 447.dealII: -DSPEC\_CPU\_LP64  
 450.soplex: -DSPEC\_CPU\_LP64  
 453.povray: -DSPEC\_CPU\_LP64  
 454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
 459.GemsFDTD: -DSPEC\_CPU\_LP64

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## Base Portability Flags (Continued)

465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3

Fortran benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch

Benchmarks using both Fortran and C:

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3

## Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64

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## Peak Portability Flags (Continued)

```

434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

## Peak Optimization Flags

C benchmarks:

```

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32
-opt-mem-layout-trans=3

```

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

```

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
-auto-ilp32

```

447.dealII: basepeak = yes

```

450.soplex: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3

```

```

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

```

Fortran benchmarks:

410.bwaves: basepeak = yes

```

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep- -static

```

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## Peak Optimization Flags (Continued)

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto  
-inline-alloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -static -auto-ilp32  
-opt-mem-layout-trans=3

481.wrf: Same as 454.calculix

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revA.20120410.00.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revA.20120410.00.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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