



# SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## IBM Corporation

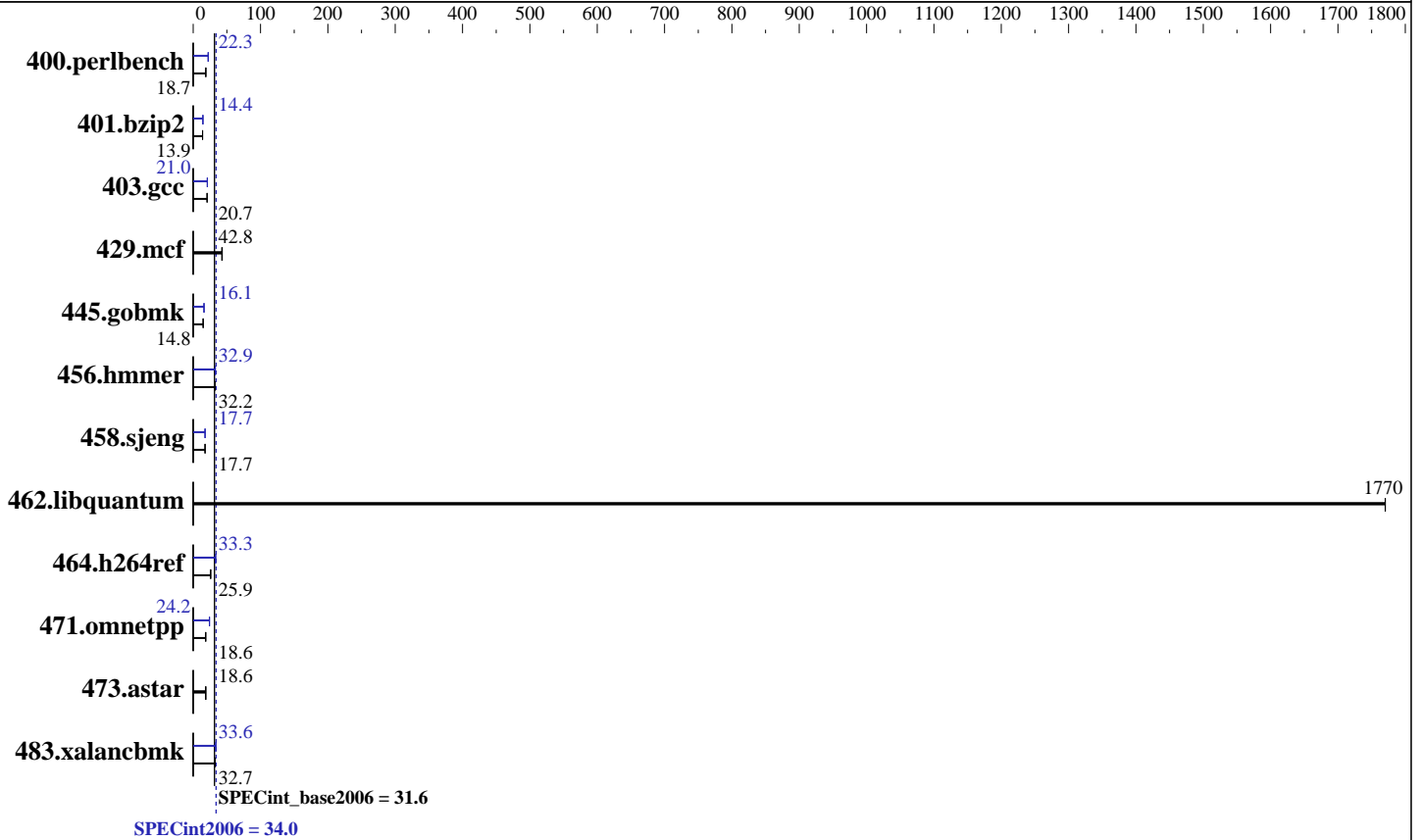
IBM Flex System x240  
(Intel Xeon E5-2648L, 1.80 GHz)

**SPECint®2006 = 34.0**

**SPECint\_base2006 = 31.6**

**CPU2006 license:** 11  
**Test sponsor:** IBM Corporation  
**Tested by:** IBM Corporation

**Test date:** May-2012  
**Hardware Availability:** May-2012  
**Software Availability:** Dec-2011



### Hardware

**CPU Name:** Intel Xeon E5-2648L  
**CPU Characteristics:** Intel Turbo Boost Technology up to 2.10 GHz  
**CPU MHz:** 1800  
**FPU:** Integrated  
**CPU(s) enabled:** 16 cores, 2 chips, 8 cores/chip, 2 threads/core  
**CPU(s) orderable:** 1,2 chips  
**Primary Cache:** 32 KB I + 32 KB D on chip per core  
**Secondary Cache:** 256 KB I+D on chip per core  
**L3 Cache:** 20 MB I+D on chip per chip  
**Other Cache:** None  
**Memory:** 128 GB (16 x 8 GB 2Rx4 PC3-12800R-11, ECC)  
**Disk Subsystem:** 1 x 300 GB SAS, 10000 RPM  
**Other Hardware:** None

### Software

**Operating System:** Red Hat Enterprise Linux Server release 6.2 (Santiago)  
 2.6.32-220.el6.x86\_64  
**Compiler:** C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux  
**Auto Parallel:** Yes  
**File System:** ext4  
**System State:** Run level 3 (multi-user)  
**Base Pointers:** 32/64-bit  
**Peak Pointers:** 32/64-bit  
**Other Software:** Microquill SmartHeap V9.01



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## IBM Corporation

IBM Flex System x240  
(Intel Xeon E5-2648L, 1.80 GHz)

SPECint2006 = 34.0

SPECint\_base2006 = 31.6

CPU2006 license: 11  
Test sponsor: IBM Corporation  
Tested by: IBM Corporation

Test date: May-2012  
Hardware Availability: May-2012  
Software Availability: Dec-2011

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	522	18.7	521	18.7	<u>522</u>	<u>18.7</u>	<u>438</u>	<u>22.3</u>	438	22.3	438	22.3
401.bzip2	692	13.9	<u>692</u>	<u>13.9</u>	694	13.9	669	14.4	670	14.4	<u>670</u>	<u>14.4</u>
403.gcc	389	20.7	<u>389</u>	<u>20.7</u>	389	20.7	384	21.0	384	21.0	<u>384</u>	<u>21.0</u>
429.mcf	<u>213</u>	<u>42.8</u>	211	43.2	214	42.5	<u>213</u>	<u>42.8</u>	211	43.2	214	42.5
445.gobmk	707	14.8	707	14.8	<u>707</u>	<u>14.8</u>	<u>652</u>	<u>16.1</u>	652	16.1	653	16.1
456.hammer	<u>290</u>	<u>32.2</u>	289	32.2	290	32.2	284	32.9	284	32.9	<u>284</u>	<u>32.9</u>
458.sjeng	686	17.7	685	17.7	<u>685</u>	<u>17.7</u>	684	17.7	686	17.6	<u>685</u>	<u>17.7</u>
462.libquantum	<u>11.7</u>	<u>1770</u>	11.7	1770	11.7	1770	<u>11.7</u>	<u>1770</u>	11.7	1770	11.7	1770
464.h264ref	852	26.0	<u>854</u>	<u>25.9</u>	855	25.9	668	33.1	664	33.3	<u>664</u>	<u>33.3</u>
471.omnetpp	337	18.5	<u>337</u>	<u>18.6</u>	336	18.6	258	24.2	259	24.1	<u>259</u>	<u>24.2</u>
473.astar	378	18.6	<u>378</u>	<u>18.6</u>	375	18.7	378	18.6	<u>378</u>	<u>18.6</u>	375	18.7
483.xalancbmk	<u>211</u>	<u>32.7</u>	213	32.3	211	32.8	206	33.5	<u>205</u>	<u>33.6</u>	205	33.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

Operating Mode set to Maximum Performance in BIOS  
Sysinfo program /cpu2006.1.2/config/sysinfo.rev6800  
\$Rev: 6800 \$ \$Date:: 2011-10-11 #\$ 6f2ebdff5032aaa42e583f96b07f99d3  
running on blacktip-pete Wed May 23 14:00:06 2012

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2648L 0 @ 1.80GHz
 2 "physical id"s (chips)
 32 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
cpu cores : 8
siblings : 16
physical 0: cores 0 1 2 3 4 5 6 7
physical 1: cores 0 1 2 3 4 5 6 7
cache size : 20480 KB
```

From /proc/meminfo

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

**IBM Corporation**

IBM Flex System x240  
(Intel Xeon E5-2648L, 1.80 GHz)

**SPECint2006 = 34.0**

**SPECint\_base2006 = 31.6**

**CPU2006 license:** 11  
**Test sponsor:** IBM Corporation  
**Tested by:** IBM Corporation

**Test date:** May-2012  
**Hardware Availability:** May-2012  
**Software Availability:** Dec-2011

## Platform Notes (Continued)

MemTotal: 132136204 kB  
HugePages\_Total: 0  
Hugepagesize: 2048 kB

/usr/bin/lsc\_release -d  
Red Hat Enterprise Linux Server release 6.2 (Santiago)

From /etc/\*release\* /etc/\*version\*  
redhat-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)  
system-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)  
system-release-cpe: cpe:/o:redhat:enterprise\_linux:6server:ga:server

uname -a:  
Linux blacktip-pete 2.6.32-220.el6.x86\_64 #1 SMP Wed Nov 9 08:03:13 EST 2011  
x86\_64 x86\_64 x86\_64 GNU/Linux

run-level 3 May 21 15:22

SPEC is set to: /cpu2006.1.2  
Filesystem Type Size Used Avail Use% Mounted on  
/dev/mapper/vg\_blacktippete-lv\_root  
ext4 265G 6.0G 246G 3% /

Additional information from dmidecode:  
Memory:  
4x Hynix HMT31GR7CFR4C-PB 8 GB 1600 MHz 2 rank  
12x Samsung M393B1K70DH0-CK0 8 GB 1600 MHz 2 rank

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:  
KMP\_AFFINITY = "granularity=fine,scatter"  
LD\_LIBRARY\_PATH = "/cpu2006.1.2/libs/32:/cpu2006.1.2/libs/64"  
OMP\_NUM\_THREADS = "16"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB  
memory using RHEL5.5  
Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled

## Base Compiler Invocation

C benchmarks:  
icc -m64

C++ benchmarks:  
icpc -m64



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

**IBM Corporation**

IBM Flex System x240  
(Intel Xeon E5-2648L, 1.80 GHz)

**SPECint2006 = 34.0**

**SPECint\_base2006 = 31.6**

**CPU2006 license:** 11

**Test sponsor:** IBM Corporation

**Tested by:** IBM Corporation

**Test date:** May-2012

**Hardware Availability:** May-2012

**Software Availability:** Dec-2011

## Base Portability Flags

```

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

```

## Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-Wl,-z,muldefs -L/smartheap -lsmartheap64

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

400.perlbench: icc -m32

445.gobmk: icc -m32

464.h264ref: icc -m32

C++ benchmarks (except as noted below):

icpc -m32

473.astar: icpc -m64



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

**IBM Corporation**

IBM Flex System x240  
(Intel Xeon E5-2648L, 1.80 GHz)

**SPECint2006 = 34.0**

**SPECint\_base2006 = 31.6**

**CPU2006 license:** 11

**Test sponsor:** IBM Corporation

**Tested by:** IBM Corporation

**Test date:** May-2012

**Hardware Availability:** May-2012

**Software Availability:** Dec-2011

## Peak Portability Flags

```

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
456.hmmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

```

## Peak Optimization Flags

C benchmarks:

```

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
              -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
              -opt-prefetch -ansi-alias

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
           -O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32
           -opt-prefetch -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -inline-alloc
         -opt-malloc-options=3 -auto-ilp32

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
          -ansi-alias

456.hmmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
           -ansi-alias

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
           -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
           -unroll4

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
            -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
            -unroll2 -ansi-alias

```

C++ benchmarks:

```

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
            -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
            -opt-ra-region-strategy=block -ansi-alias
            -Wl,-z,muldefs -L/smartheap -lsmartheap

```

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

**IBM Corporation**

IBM Flex System x240  
(Intel Xeon E5-2648L, 1.80 GHz)

**SPECint2006 = 34.0**

**SPECint\_base2006 = 31.6**

**CPU2006 license:** 11  
**Test sponsor:** IBM Corporation  
**Tested by:** IBM Corporation

**Test date:** May-2012  
**Hardware Availability:** May-2012  
**Software Availability:** Dec-2011

## Peak Optimization Flags (Continued)

473.astar: basepeak = yes

483.xalancbmk: -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias  
-Wl,-z,muldefs -L/smartheap -lsmartheap

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>  
<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-SNB-C.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>  
<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-SNB-C.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.2.  
Report generated on Thu Jul 24 08:36:45 2014 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 19 June 2012.