



# SPEC® CINT2006 Result

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## Fujitsu

PRIMERGY TX100 S3p, Intel Xeon E3-1230 v2, 3.30 GHz

SPECint®2006 = 54.2

SPECint\_base2006 = 51.0

CPU2006 license: 19

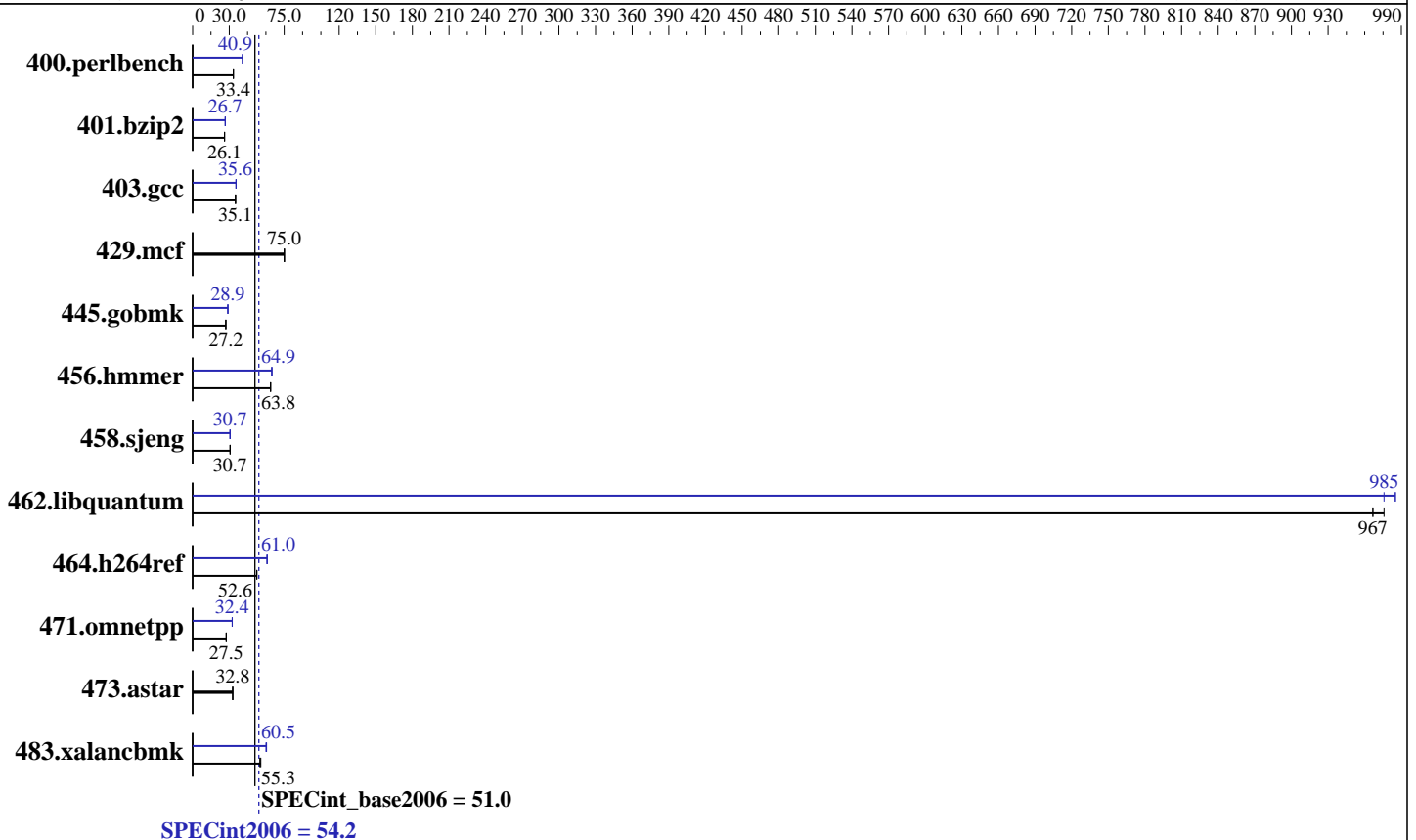
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: May-2012

Hardware Availability: May-2012

Software Availability: Feb-2012



### Hardware

CPU Name: Intel Xeon E3-1230 v2  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.7 GHz  
 CPU MHz: 3300  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 8 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 16 GB (2 x 8 GB 2Rx8 PC3-12800E-11, ECC)  
 Disk Subsystem: 1 x SATA, 500 GB, 7200 RPM  
 Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server release 6.2 (Santiago)  
 2.6.32-220.el6.x86\_64  
 Compiler: C/C++: Version 12.1.0.293 of Intel C++ Studio XE for Linux  
 Auto Parallel: Yes  
 File System: ext4  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V10.0



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	293	33.3	<b><u>293</u></b>	<b><u>33.4</u></b>	292	33.4	239	41.0	<b><u>239</u></b>	<b><u>40.9</u></b>	239	40.8
401.bzip2	370	26.1	369	26.1	<b><u>369</u></b>	<b><u>26.1</u></b>	361	26.7	361	26.7	<b><u>361</u></b>	<b><u>26.7</u></b>
403.gcc	229	35.2	230	35.0	<b><u>229</u></b>	<b><u>35.1</u></b>	<b><u>226</u></b>	<b><u>35.6</u></b>	226	35.6	227	35.5
429.mcf	121	75.3	122	75.0	<b><u>122</u></b>	<b><u>75.0</u></b>	121	75.3	122	75.0	<b><u>122</u></b>	<b><u>75.0</u></b>
445.gobmk	386	27.2	387	27.1	<b><u>386</u></b>	<b><u>27.2</u></b>	364	28.8	363	28.9	<b><u>363</u></b>	<b><u>28.9</u></b>
456.hammer	146	63.8	146	63.8	<b><u>146</u></b>	<b><u>63.8</u></b>	144	64.9	144	64.8	<b><u>144</u></b>	<b><u>64.9</u></b>
458.sjeng	394	30.7	<b><u>394</u></b>	<b><u>30.7</u></b>	394	30.7	395	30.7	395	30.7	<b><u>395</u></b>	<b><u>30.7</u></b>
462.libquantum	<b><u>21.4</u></b>	<b><u>967</u></b>	21.2	976	21.4	967	<b><u>21.0</u></b>	<b><u>985</u></b>	21.2	976	21.0	985
464.h264ref	422	52.5	421	52.6	<b><u>421</u></b>	<b><u>52.6</u></b>	<b><u>363</u></b>	<b><u>61.0</u></b>	363	61.0	363	61.0
471.omnetpp	227	27.5	<b><u>227</u></b>	<b><u>27.5</u></b>	227	27.6	193	32.4	<b><u>193</u></b>	<b><u>32.4</u></b>	193	32.5
473.astar	214	32.8	214	32.9	<b><u>214</u></b>	<b><u>32.8</u></b>	214	32.8	214	32.9	<b><u>214</u></b>	<b><u>32.8</u></b>
483.xalancbmk	125	55.1	<b><u>125</u></b>	<b><u>55.3</u></b>	124	55.8	<b><u>114</u></b>	<b><u>60.5</u></b>	115	60.1	114	60.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS configuration:  
Intel HT Technology = Disable

## General Notes

Environment variables set by runspec before the start of the run:  
KMP\_AFFINITY = "granularity=fine,scatter"  
LD\_LIBRARY\_PATH = "/SPECcpu2006/libs/32:/SPECcpu2006/libs/64"  
OMP\_NUM\_THREADS = "4"

Binaries compiled on a system with 1x E3-1270V2 CPU + 32 GB memory using RHEL6.2  
Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled  
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## Base Compiler Invocation

C benchmarks:  
icc -m64

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## Base Compiler Invocation (Continued)

C++ benchmarks:  
icpc -m64

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64  
403.gcc: -DSPEC\_CPU\_LP64  
429.mcf: -DSPEC\_CPU\_LP64  
445.gobmk: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
464.h264ref: -DSPEC\_CPU\_LP64  
471.omnetpp: -DSPEC\_CPU\_LP64  
473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-Wl,-z,muldefs -L/opt/SmartHeap/lib64 -lsmartheap64

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc -m64

400.perlbench: icc -m32

445.gobmk: icc -m32

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## Peak Compiler Invocation (Continued)

464.h264ref: `icc -m32`

C++ benchmarks (except as noted below):

`icpc -m32`

473.astar: `icpc -m64`

## Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`  
401.bzip2: `-DSPEC_CPU_LP64`  
403.gcc: `-DSPEC_CPU_LP64`  
429.mcf: `-DSPEC_CPU_LP64`  
456.hmmer: `-DSPEC_CPU_LP64`  
458.sjeng: `-DSPEC_CPU_LP64`  
462.libquantum: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`  
473.astar: `-DSPEC_CPU_LP64`  
483.xalancbmk: `-DSPEC_CPU_LINUX`

## Peak Optimization Flags

C benchmarks:

400.perlbench: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch -ansi-alias`

401.bzip2: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32 -opt-prefetch -ansi-alias`

403.gcc: `-xAVX -ipo -O3 -no-prec-div -inline-calloc -opt-malloc-options=3 -auto-ilp32`

429.mcf: `basepeak = yes`

445.gobmk: `-xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -ansi-alias`

456.hmmer: `-xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32 -ansi-alias`

458.sjeng: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4`

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## Peak Optimization Flags (Continued)

462.libquantum: -xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch  
-auto-p32

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-ra-region-strategy=block -ansi-alias  
-Wl,-z,muldefs -L/opt/SmartHeap/lib -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias  
-Wl,-z,muldefs -L/opt/SmartHeap/lib -lsmartheap

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>  
<http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20120320.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>  
<http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20120320.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.2.  
Report generated on Thu Jul 24 05:20:15 2014 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 5 June 2012.