



SPEC® CINT2006 Result

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Supermicro

SPECint®2006 = 57.3

SuperServer 5037A-i (X9SRA, Intel Xeon E5-1660)

SPECint_base2006 = 54.2

CPU2006 license: 001176

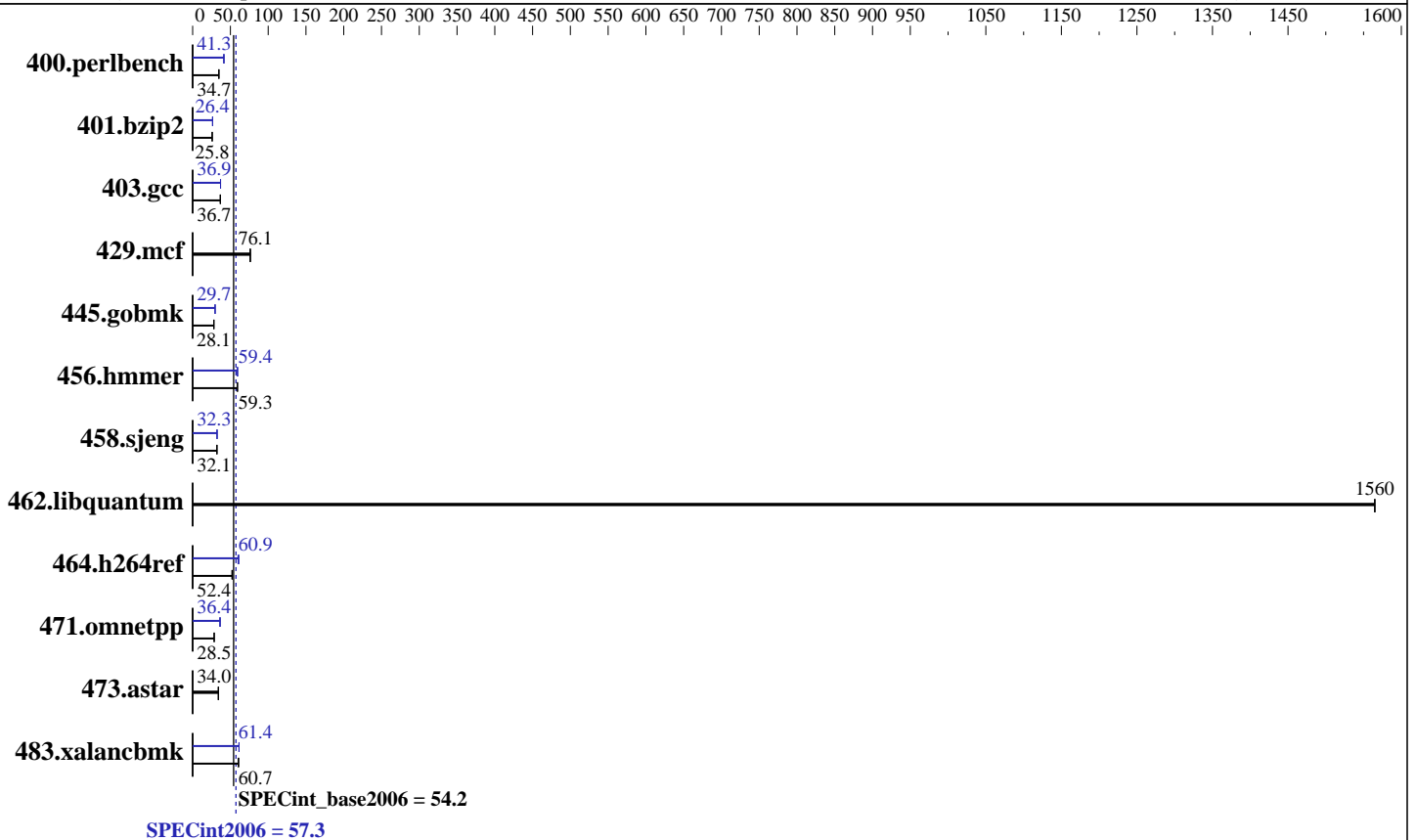
Test date: May-2012

Test sponsor: Supermicro

Hardware Availability: Mar-2012

Tested by: Supermicro

Software Availability: Dec-2011



Hardware

CPU Name: Intel Xeon E5-1660
 CPU Characteristics: Intel Turbo Boost Technology up to 3.90 GHz
 CPU MHz: 3300
 FPU: Integrated
 CPU(s) enabled: 6 cores, 1 chip, 6 cores/chip, 2 threads/core
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 15 MB I+D on chip per chip
 Other Cache: None
 Memory: 64 GB (8 x 8 GB 2Rx4 PC3-12800R-11, ECC)
 Disk Subsystem: 1 x 300 GB SATA II, 10000 RPM
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server Release 6.2, Kernel 2.6.32-220.el6.x86_64
 Compiler: C/C++; Version 12.1.0.225 of Intel C++ Studio XE for Linux
 Auto Parallel: Yes
 File System: ext4
 System State: Run level 3 (multi-user)
 Base Pointers: 32/64-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V9.01



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Results Table

| Benchmark | Base | | | | | | Peak | | | | | |
|----------------|------------|-------------|-------------|-------------|------------|-------------|------------|-------------|-------------|-------------|------------|-------------|
| | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 282 | 34.7 | 281 | 34.8 | 282 | 34.6 | 237 | 41.3 | 236 | 41.3 | 235 | 41.5 |
| 401.bzip2 | 373 | 25.8 | 373 | 25.8 | 372 | 25.9 | 366 | 26.4 | 366 | 26.4 | 364 | 26.5 |
| 403.gcc | 220 | 36.7 | 219 | 36.7 | 220 | 36.6 | 218 | 36.9 | 218 | 36.9 | 218 | 37.0 |
| 429.mcf | 119 | 76.7 | 120 | 76.1 | 120 | 76.1 | 119 | 76.7 | 120 | 76.1 | 120 | 76.1 |
| 445.gobmk | 374 | 28.1 | 373 | 28.1 | 373 | 28.1 | 354 | 29.7 | 353 | 29.7 | 353 | 29.7 |
| 456.hammer | 157 | 59.3 | 157 | 59.2 | 157 | 59.3 | 158 | 59.2 | 157 | 59.4 | 157 | 59.5 |
| 458.sjeng | 377 | 32.1 | 377 | 32.1 | 377 | 32.1 | 375 | 32.3 | 374 | 32.3 | 375 | 32.3 |
| 462.libquantum | 13.2 | 1560 | 13.2 | 1560 | 13.2 | 1570 | 13.2 | 1560 | 13.2 | 1560 | 13.2 | 1570 |
| 464.h264ref | 422 | 52.5 | 422 | 52.4 | 426 | 51.9 | 363 | 61.0 | 363 | 60.9 | 368 | 60.1 |
| 471.omnetpp | 219 | 28.5 | 219 | 28.5 | 219 | 28.5 | 172 | 36.4 | 172 | 36.4 | 174 | 36.0 |
| 473.astar | 206 | 34.0 | 207 | 33.9 | 205 | 34.3 | 206 | 34.0 | 207 | 33.9 | 205 | 34.3 |
| 483.xalancbmk | 113 | 60.9 | 114 | 60.7 | 114 | 60.3 | 112 | 61.3 | 112 | 61.4 | 112 | 61.4 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

```
Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/usr/cpu2006/libs/32:/usr/cpu2006/libs/64"
OMP_NUM_THREADS = "6"
```

```
Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
memory using RHEL5.5
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
```

Base Compiler Invocation

```
C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64
```



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Base Portability Flags

```

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

```

Base Optimization Flags

C benchmarks:

-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -Wl,-z,muldefs
-L/smartheap -lsmartheap64

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

400.perlbench: icc -m32

445.gobmk: icc -m32

464.h264ref: icc -m32

C++ benchmarks (except as noted below):

icpc -m32

473.astar: icpc -m64



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Peak Portability Flags

```

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

```

Peak Optimization Flags

C benchmarks:

```

400.perlbench: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
               -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch
               -ansi-alias

401.bzip2: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div -prof-use(pass 2) -auto-ilp32 -opt-prefetch
           -ansi-alias

403.gcc: -xAVX -ipo -O3 -no-prec-div -inline-calloc
         -opt-malloc-options=3 -auto-ilp32

429.mcf: basepeak = yes

445.gobmk: -xAVX(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
           -ansi-alias

456.hmmer: -xAVX -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
           -ansi-alias

458.sjeng: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div(pass 2) -prof-use(pass 2) -unroll4

462.libquantum: basepeak = yes

464.h264ref: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
             -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
             -ansi-alias

```

C++ benchmarks:

```

471.omnetpp: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
             -no-prec-div(pass 2) -prof-use(pass 2)
             -opt-ra-region-strategy=block -ansi-alias
             -Wl,-z,muldefs -L/smartheap -lsmartheap

```

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Peak Optimization Flags (Continued)

473.astar: basepeak = yes

483.xalancbmk: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias
-Wl,-z,muldefs -L/smartheap -lsmartheap

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>

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For other inquiries, please contact webmaster@spec.org.

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