SPEC® CINT2006 Result
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Supermicro

SuperServer 7047R-TXRF (X9DRX+-F, Intel Xeon E5-2667)

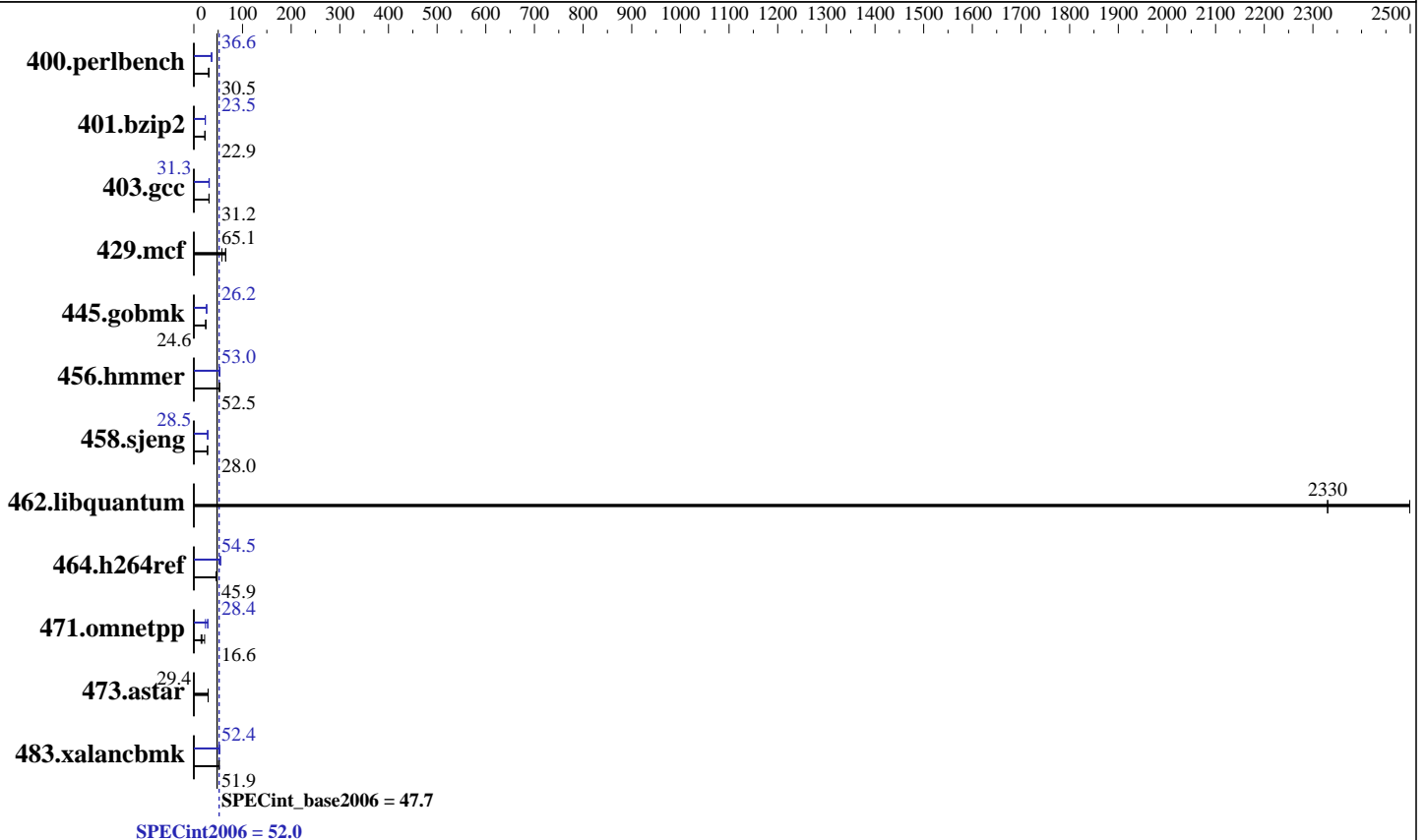SPECint®2006 = 52.0

SPECint_base2006 = 47.7

CPU2006 license: 001176
Test sponsor: Supermicro
Tested by: Supermicro

Test date: Apr-2012
Hardware Availability: Mar-2012
Software Availability: Dec-2011

400.perlbench 36.6 / 30.5
401.bzip2 23.5 / 22.9
403.gcc 31.3 / 31.2
429.mcf 65.1
445.gobmk 26.2 / 24.6
456.hmmer 53.0 / 52.5
458.sjeng 28.5 / 28.0
462.libquantum 2330
464.h264ref 54.5 / 45.9
471.omnetpp 28.4 / 16.6
473.astar 29.4
483.xalancbmk 52.4 / 51.9

SPECint_base2006 = 47.7
SPECint2006 = 52.0

Hardware

CPU Name: Intel Xeon E5-2667
CPU Characteristics: Intel Turbo Boost Technology up to 3.50 GHz
CPU MHz: 2900
FPU: Integrated
CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core
L3 Cache: 15 MB I+D on chip per chip
Other Cache: None
Memory: 256 GB (16 x 16 GB 1Rx4 PC3-12800R-11, ECC)
Disk Subsystem: 1 x 2 TB SATA II, 7200 RPM
Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server Release 6.2, Kernel 2.6.32-220.el6.x86_64
Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux
Auto Parallel: Yes
File System: ext3
System State: Run level 3 (multi-user)
Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: Microquill SmartHeap V9.01

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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	316	30.9	320	30.5	**320**	**30.5**	266	36.7	**267**	**36.6**	271	36.1
401.bzip2	424	22.8	**422**	**22.9**	421	22.9	411	23.5	413	23.4	**411**	**23.5**
403.gcc	258	31.2	**258**	**31.2**	258	31.3	**257**	**31.3**	259	31.0	253	31.8
429.mcf	158	57.6	139	65.6	**140**	**65.1**	158	57.6	139	65.6	**140**	**65.1**
445.gobmk	429	24.5	**427**	**24.6**	427	24.6	401	26.1	**400**	**26.2**	400	26.2
456.hmmer	**178**	**52.5**	178	52.5	178	52.5	**176**	**53.0**	176	53.1	177	52.8
458.sjeng	432	28.0	431	28.0	**432**	**28.0**	**424**	**28.5**	424	28.6	430	28.1
462.libquantum	8.29	2500	8.89	2330	**8.89**	**2330**	8.29	2500	8.89	2330	**8.89**	**2330**
464.h264ref	**482**	**45.9**	483	45.9	478	46.3	**406**	**54.5**	407	54.4	405	54.6
471.omnetpp	**376**	**16.6**	283	22.1	421	14.9	218	28.7	263	23.8	**220**	**28.4**
473.astar	**239**	**29.4**	239	29.4	241	29.2	**239**	**29.4**	239	29.4	241	29.2
483.xalancbmk	**133**	**51.9**	133	52.1	135	51.0	136	50.9	**132**	**52.4**	130	53.1

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/usr/cpu2006/libs/32:/usr/cpu2006/libs/64"
OMP_NUM_THREADS = "12"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
memory using RHEL5.5
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled

Base Compiler Invocation

C benchmarks:
    icc -m64

C++ benchmarks:
    icpc -m64

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Base Portability Flags

```
   400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
      401.bzip2: -DSPEC_CPU_LP64
        403.gcc: -DSPEC_CPU_LP64
        429.mcf: -DSPEC_CPU_LP64
     445.gobmk: -DSPEC_CPU_LP64
     456.hmmer: -DSPEC_CPU_LP64
      458.sjeng: -DSPEC_CPU_LP64
  462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
     464.h264ref: -DSPEC_CPU_LP64
    471.omnetpp: -DSPEC_CPU_LP64
       473.astar: -DSPEC_CPU_LP64
  483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:

```
    -xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32
```

C++ benchmarks:

```
    -xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -Wl,-z,muldefs
    -L/smartheap -lsmartheap64
```

Base Other Flags

C benchmarks:

```
        403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks (except as noted below):
```
    icc   -m64
```

```
    400.perlbench: icc -m32
```

```
      445.gobmk: icc -m32
```

```
     464.h264ref: icc -m32
```

C++ benchmarks (except as noted below):
```
    icpc -m32
```

```
       473.astar: icpc -m64
```

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Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
   401.bzip2: -DSPEC_CPU_LP64
     403.gcc: -DSPEC_CPU_LP64
     429.mcf: -DSPEC_CPU_LP64
  456.hmmer: -DSPEC_CPU_LP64
   458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
    473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
400.perlbench: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
               -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch
               -ansi-alias

   401.bzip2: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
               -no-prec-div -prof-use(pass 2) -auto-ilp32 -opt-prefetch
               -ansi-alias

     403.gcc: -xAVX -ipo -O3 -no-prec-div -inline-calloc
               -opt-malloc-options=3 -auto-ilp32

     429.mcf: basepeak = yes

   445.gobmk: -xAVX(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
               -ansi-alias

   456.hmmer: -xAVX -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
               -ansi-alias

   458.sjeng: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
               -no-prec-div(pass 2) -prof-use(pass 2) -unroll4

462.libquantum: basepeak = yes

  464.h264ref: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
               -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
               -ansi-alias
```

C++ benchmarks:

```
   471.omnetpp: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
               -no-prec-div(pass 2) -prof-use(pass 2)
               -opt-ra-region-strategy=block              -ansi-alias
               -Wl,-z,muldefs -L/smartheap -lsmartheap
```

Continued on next page

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Test sponsor:	Supermicro	**Hardware Availability:**	Mar-2012
Tested by:	Supermicro	**Software Availability:**	Dec-2011

Peak Optimization Flags (Continued)

473.astar: basepeak = yes

483.xalancbmk: `-xAVX -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias -Wl,-z,muldefs -L/smartheap -lsmartheap`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml

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For other inquiries, please contact webmaster@spec.org.

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