



SPEC[®] CFP2006 Result

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Fujitsu

SPECfp[®]_rate2006 = 213

CELSIUS M720 (Intel Xeon E5-1650)

SPECfp_rate_base2006 = 208

CPU2006 license: 19

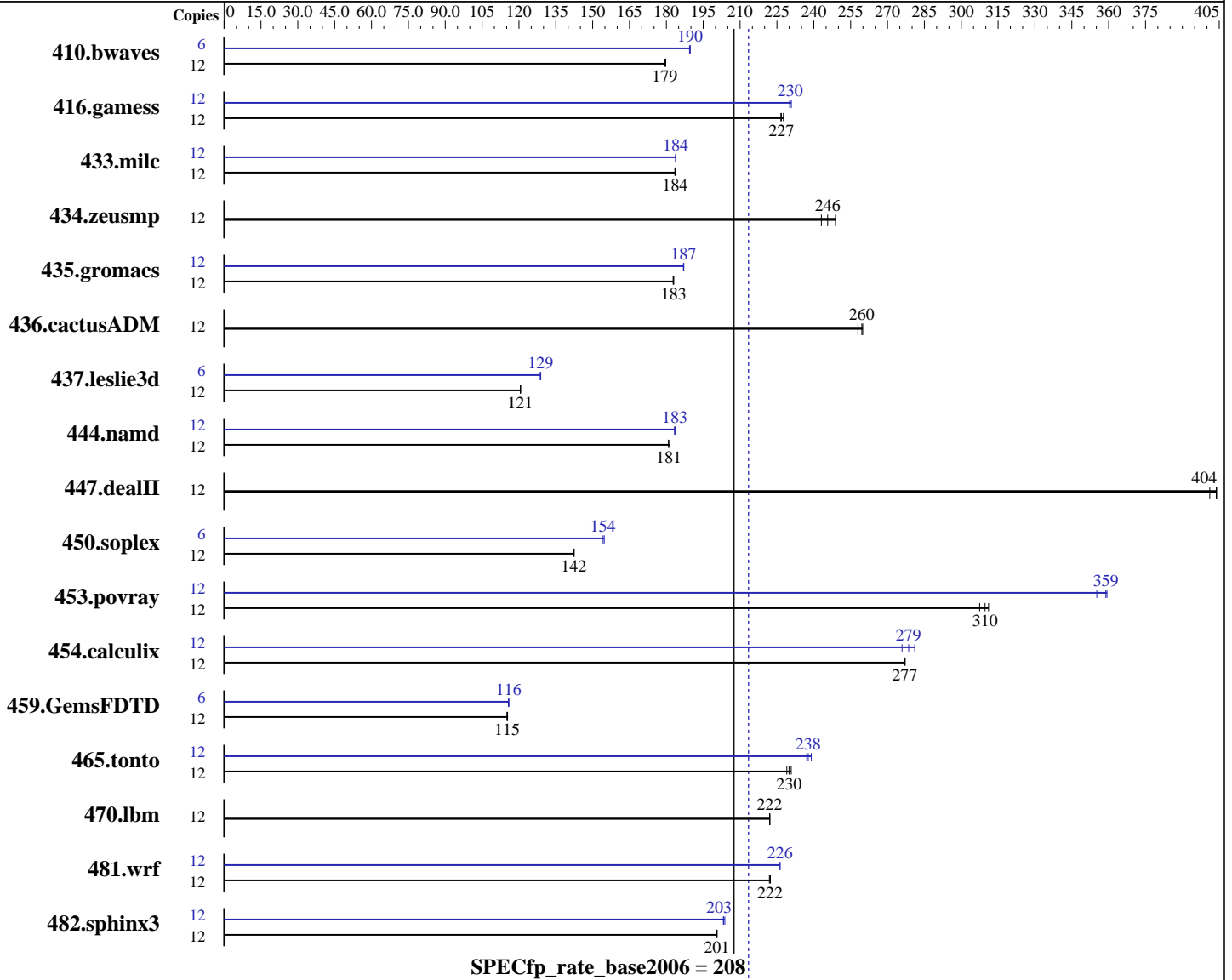
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Mar-2012

Hardware Availability: Mar-2012

Software Availability: Dec-2011



Hardware

CPU Name: Intel Xeon E5-1650
 CPU Characteristics: Intel Turbo Boost Technology up to 3.80 GHz
 CPU MHz: 3200
 FPU: Integrated
 CPU(s) enabled: 6 cores, 1 chip, 6 cores/chip, 2 threads/core
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: Red Hat Enterprise Linux Server release 6.1, 2.6.32-131.0.15.el6.x86_64
 Compiler: C/C++: Version 12.1.2.273 of Intel C++ Studio XE for Linux;
 Fortran: Version 12.1.2.273 of Intel Fortran Studio XE for Linux
 Auto Parallel: No
 File System: ReiserFS
 System State: Run level 3 (multi - user)

Continued on next page



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L3 Cache: 12 MB I+D on chip per chip
Other Cache: None
Memory: 32 GB (8 x 4 GB 2Rx8 PC3-12800E-11, ECC)
Disk Subsystem: 1 x SATA III, 500 GB, 7200 rpm
Other Hardware: None

Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	12	907	180	<u>909</u>	<u>179</u>	910	179	6	430	189	430	190	<u>430</u>	<u>190</u>
416.gamess	12	<u>1036</u>	<u>227</u>	1032	228	1037	227	12	1018	231	1021	230	<u>1020</u>	<u>230</u>
433.milc	12	<u>600</u>	<u>184</u>	600	184	600	184	12	599	184	599	184	<u>599</u>	<u>184</u>
434.zeusmp	12	<u>445</u>	<u>246</u>	449	243	439	249	12	<u>445</u>	<u>246</u>	449	243	439	249
435.gromacs	12	<u>468</u>	<u>183</u>	468	183	469	183	12	<u>458</u>	<u>187</u>	458	187	458	187
436.cactusADM	12	<u>553</u>	<u>260</u>	552	260	556	258	12	<u>553</u>	<u>260</u>	552	260	556	258
437.leslie3d	12	936	121	935	121	<u>935</u>	<u>121</u>	6	<u>438</u>	<u>129</u>	438	129	438	129
444.namd	12	<u>532</u>	<u>181</u>	530	181	532	181	12	<u>525</u>	<u>183</u>	525	183	525	183
447.dealII	12	<u>340</u>	<u>404</u>	340	404	342	401	12	<u>340</u>	<u>404</u>	340	404	342	401
450.soplex	12	702	142	<u>703</u>	<u>142</u>	704	142	6	<u>324</u>	<u>154</u>	323	155	325	154
453.povray	12	205	311	208	307	<u>206</u>	<u>310</u>	12	178	359	180	355	<u>178</u>	<u>359</u>
454.calculix	12	<u>357</u>	<u>277</u>	358	277	357	277	12	352	281	<u>355</u>	<u>279</u>	359	276
459.GemsFDTD	12	<u>1105</u>	<u>115</u>	1106	115	1104	115	6	549	116	<u>550</u>	<u>116</u>	550	116
465.tonto	12	516	229	512	231	<u>514</u>	<u>230</u>	12	498	237	<u>497</u>	<u>238</u>	494	239
470.lbm	12	742	222	<u>743</u>	<u>222</u>	743	222	12	742	222	<u>743</u>	<u>222</u>	743	222
481.wrf	12	604	222	<u>603</u>	<u>222</u>	603	222	12	594	226	592	226	<u>592</u>	<u>226</u>
482.sphinx3	12	1166	201	<u>1166</u>	<u>201</u>	1166	201	12	1151	203	<u>1150</u>	<u>203</u>	1148	204

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used to generate taskset commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS settings:
Frequency Floor Override = Enabled



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General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/work/cpu2006/libs/32:/work/cpu2006/libs/64"

Binaries compiled on a system with
Red Hat Enterprise Linux Server release 6.1 (Santiago)
Added glibc-static-2.12-1.25.el6.x86_64.rpm
to enable static linking

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

Continued on next page



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Base Optimization Flags (Continued)

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

Fortran benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch

Benchmarks using both Fortran and C:

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
465.tonto: -DSPEC_CPU_LP64

Continued on next page



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Peak Portability Flags (Continued)

470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32
-opt-mem-layout-trans=3

470.lbm: basepeak = yes

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -opt-mem-layout-trans=3
-unroll2

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -static

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: -xAVX -ipo -O3 -no-prec-div -static -opt-prefetch

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto
-inline-calloc -opt-malloc-options=3

Continued on next page



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Peak Optimization Flags (Continued)

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -static
-auto-p32 -ansi-alias -opt-mem-layout-trans=3

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -static -auto-ilp32
-opt-mem-layout-trans=3

481.wrf: Same as 454.calculix

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>
<http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20120313.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>
<http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20120313.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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