SPEC® CINT2006 Result

Bull SAS

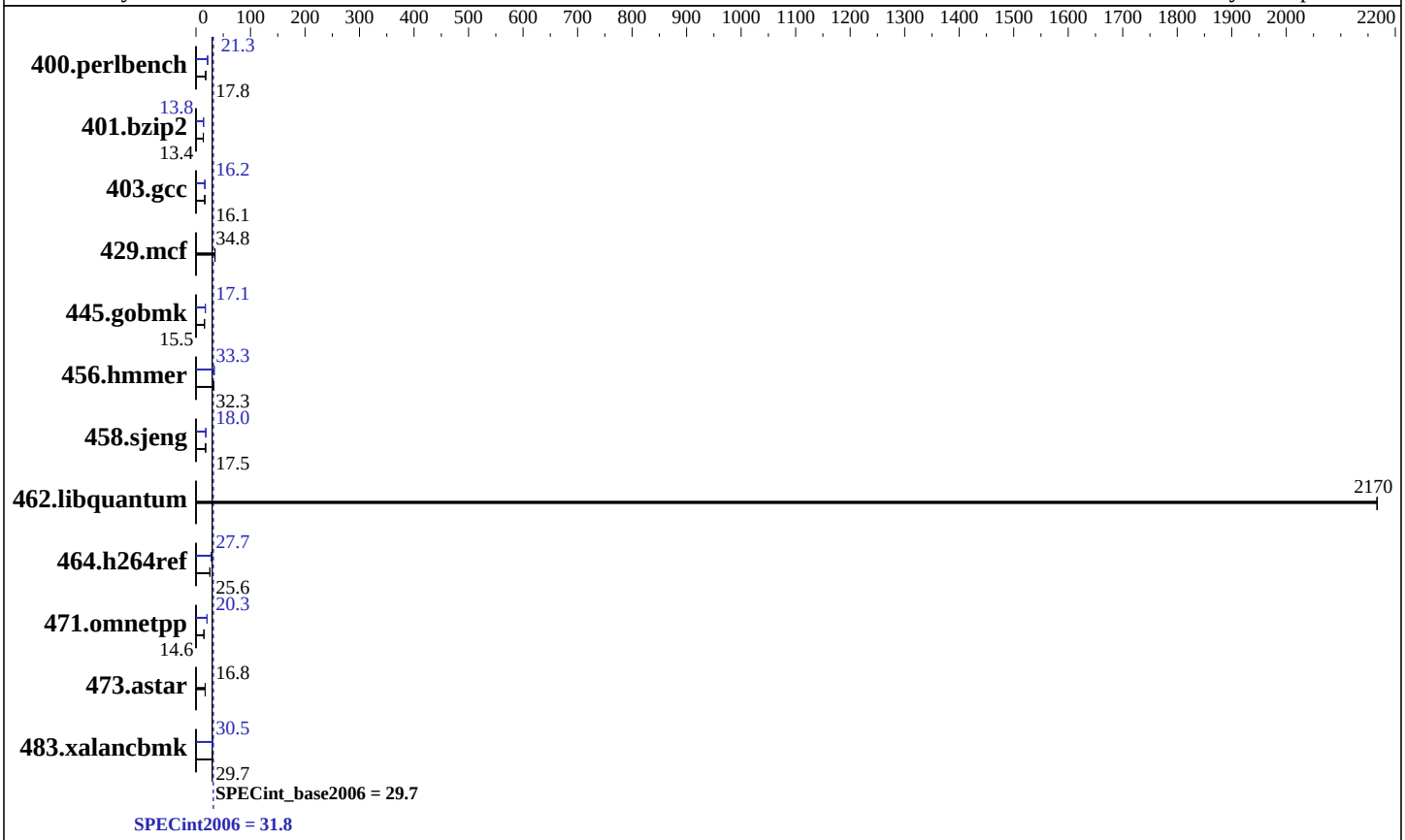
BL465 (Intel Xeon E7-4820, 2.00 GHz)

SPECint®2006 = 31.8

SPECint_base2006 = 29.7

CPU2006 license: 20
Test sponsor: Bull SAS
Tested by: Bull SAS

Test date: Dec-2011
Hardware Availability: Apr-2011
Software Availability: Sep-2011



Hardware

CPU Name:	Intel Xeon E7-4820
CPU Characteristics:	Intel Turbo Boost Technology up to 2.27 GHz
CPU MHz:	2000
FPU:	Integrated
CPU(s) enabled:	32 cores, 4 chips, 8 cores/chip, 2 threads/core
CPU(s) orderable:	2,4 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	18 MB I+D on chip per chip
Other Cache:	None
Memory:	256 GB (32 x 8 GB 2Rx4 PC3L-10600R-9 VLP, ECC)
Disk Subsystem:	4 x 50 GB SSD, RAID 0
Other Hardware:	None

Software

Operating System:	Red Hat Enterprise Linux Server release 6.1 (Santiago) 2.6.32-131.0.15.el6.x86_64
Compiler:	C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux
Auto Parallel:	Yes
File System:	ext4
System State:	Run level 3 (multi-user)
Base Pointers:	32/64-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V9.01

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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	**550**	**17.8**	550	17.8	551	17.7	458	21.3	465	21.0	**459**	**21.3**
401.bzip2	**718**	**13.4**	718	13.4	719	13.4	698	13.8	708	13.6	**699**	**13.8**
403.gcc	499	16.1	501	16.1	**500**	**16.1**	495	16.3	496	16.2	**496**	**16.2**
429.mcf	261	34.9	263	34.6	**262**	**34.8**	261	34.9	263	34.6	**262**	**34.8**
445.gobmk	**678**	**15.5**	677	15.5	679	15.4	614	17.1	**614**	**17.1**	613	17.1
456.hmmer	287	32.5	288	32.3	**288**	**32.3**	280	33.3	280	33.3	**280**	**33.3**
458.sjeng	685	17.7	**690**	**17.5**	691	17.5	**673**	**18.0**	673	18.0	672	18.0
462.libquantum	**9.56**	**2170**	9.56	2170	9.56	2170	**9.56**	**2170**	9.56	2170	9.56	2170
464.h264ref	**865**	**25.6**	867	25.5	864	25.6	798	27.7	**798**	**27.7**	798	27.7
471.omnetpp	428	14.6	424	14.7	**428**	**14.6**	308	20.3	309	20.3	**308**	**20.3**
473.astar	417	16.8	415	16.9	**417**	**16.8**	417	16.8	415	16.9	**417**	**16.8**
483.xalancbmk	**232**	**29.7**	231	29.8	233	29.6	225	30.7	227	30.4	**226**	**30.5**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

Platform Notes

```
BIOS Setting:
Turbo Boost Power Optimization set to Traditional
 Sysinfo program /spec/cpu2006.1.2/config/sysinfo.rev6800
 $Rev: 6800 $ $Date:: 2011-10-11 #$ 6f2ebdff5032aaa42e583f96b07f99d3
 running on localhost.localdomain Mon Dec 19 11:39:15 2011

 This section contains SUT (System Under Test) info as seen by
 some common utilities.  To remove or add to this section, see:
   http://www.spec.org/cpu2006/Docs/config.html#sysinfo

 From /proc/cpuinfo
    model name : Intel(R) Xeon(R) CPU E7- 4820 @ 2.00GHz
       4 "physical id"s (chips)
       64 "processors"
    cores, siblings (Caution: counting these is hw and system dependent.  The
    following excerpts from /proc/cpuinfo might not be reliable.  Use with
    caution.)
       cpu cores : 8
       siblings  : 16
       physical 0: cores 0 1 2 8 17 18 24 25
       physical 1: cores 0 2 8 9 16 17 18 25
       physical 2: cores 0 2 8 9 16 17 18 25
       physical 3: cores 1 2 8 9 16 17 18 24
```

Continued on next page

Platform Notes (Continued)

```
      cache size : 18432 KB

   From /proc/meminfo
      MemTotal:       264656344 kB
      HugePages_Total:       0
      Hugepagesize:       2048 kB

   /usr/bin/lsb_release -d
      Red Hat Enterprise Linux Server release 6.1 (Santiago)

   From /etc/*release* /etc/*version*
      redhat-release: Red Hat Enterprise Linux Server release 6.1 (Santiago)
      system-release: Red Hat Enterprise Linux Server release 6.1 (Santiago)
      system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

   uname -a:
      Linux localhost.localdomain 2.6.32-131.0.15.el6.x86_64 #1 SMP Tue May 10
      15:42:40 EDT 2011 x86_64 x86_64 x86_64 GNU/Linux

   run-level 3 Dec 19 11:36

   SPEC is set to: /spec/cpu2006.1.2
      Filesystem     Type    Size  Used Avail Use% Mounted on
      /dev/mapper/VolGroup-lv_root
                     ext4    178G  128G   41G  77% /

   Additional information from dmidecode:
     Memory:
      32x Samsung M392B1K70CM0-YH9 8 GB 1333 MHz 2 rank

   (End of data from sysinfo program)
```

General Notes

```
 Environment variables set by runspec before the start of the run:
 KMP_AFFINITY = "granularity=fine,scatter"
 LD_LIBRARY_PATH = "/spec/cpu2006.1.2/libs/32:/spec/cpu2006.1.2/libs/64"
 OMP_NUM_THREADS = "32"

  Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
  memory using RHEL5.5
  Transparent Huge Pages enabled with:
  echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
```

Base Compiler Invocation

C benchmarks:

```
     icc  -m64
```

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Base Compiler Invocation (Continued)

C++ benchmarks:
icpc -m64

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-Wl,-z,muldefs -L/smartheap -lsmartheap64

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m64

400.perlbench: icc -m32

445.gobmk: icc -m32

Continued on next page

Peak Compiler Invocation (Continued)

464.h264ref: icc -m32

C++ benchmarks (except as noted below):
icpc -m32

473.astar: icpc -m64

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-prefetch -ansi-alias

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32
-opt-prefetch -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -inline-calloc
-opt-malloc-options=3 -auto-ilp32

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
-ansi-alias

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll4

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Peak Optimization Flags (Continued)

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -opt-ra-region-strategy=block -ansi-alias -Wl,-z,muldefs -L/smartheap -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias -Wl,-z,muldefs -L/smartheap -lsmartheap

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html
http://www.spec.org/cpu2006/flags/Bull-Platform-Settings-V1.2-revA.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml
http://www.spec.org/cpu2006/flags/Bull-Platform-Settings-V1.2-revA.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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