SPEC® CINT2006 Result

Hewlett-Packard Company

ProLiant DL380 G7
(3.06 GHz, Intel Xeon X5667)

SPECint®_rate2006 =   324

SPECint_rate_base2006 = 306
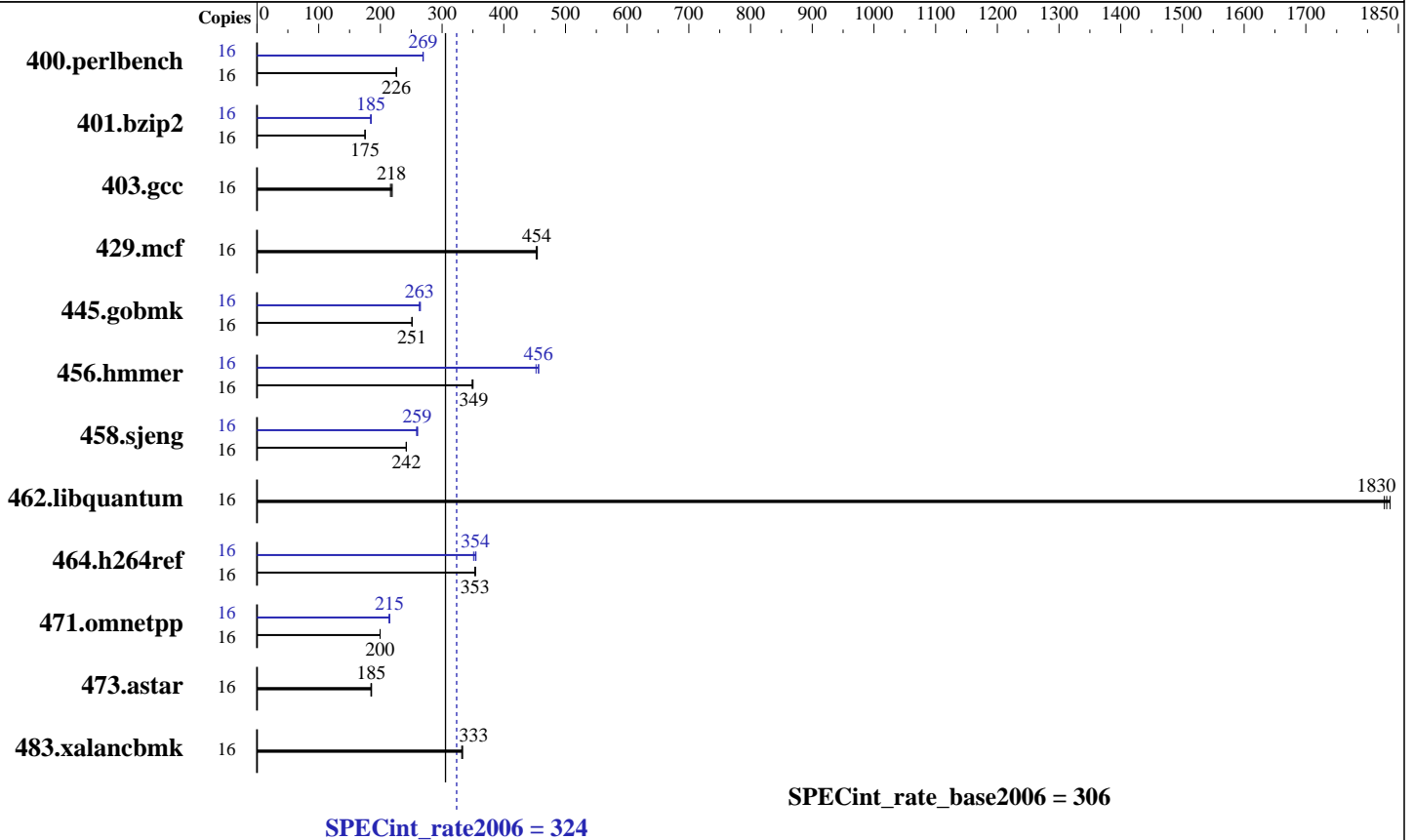
CPU2006 license: 3

Test date: Oct-2011

Test sponsor: Hewlett-Packard Company

Hardware Availability: Mar-2010

Tested by: Hewlett-Packard Company

Software Availability: Sep-2011

| Copies | | 0 | 100 | 200 | 300 | 400 | 500 | 600 | 700 | 800 | 900 | 1000 | 1100 | 1200 | 1300 | 1400 | 1500 | 1600 | 1700 | 1850 |

400.perlbench — 16 — 269; 16 — 226

401.bzip2 — 16 — 185; 16 — 175

403.gcc — 16 — 218

429.mcf — 16 — 454

445.gobmk — 16 — 263; 16 — 251

456.hmmer — 16 — 456; 16 — 349

458.sjeng — 16 — 259; 16 — 242

462.libquantum — 16 — 1830

464.h264ref — 16 — 354; 16 — 353

471.omnetpp — 16 — 215; 16 — 200

473.astar — 16 — 185

483.xalancbmk — 16 — 333

SPECint_rate_base2006 = 306

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Hardware

CPU Name:	Intel Xeon X5667
CPU Characteristics:	Intel Turbo Boost Technology up to 3.46 GHz
CPU MHz:	3067
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	12 MB I+D on chip per chip
Other Cache:	None
Memory:	48 GB (12 x 4 GB 2Rx4 PC3-10600R-9, ECC)
Disk Subsystem:	1 x 146 GB 15 K SAS
Other Hardware:	None

Software

Operating System:	Red Hat Enterprise Linux Server release 6.1, Kernel 2.6.32-131.0.15.el6.x86_64
Compiler:	C/C++: Version 12.1.0.225 of Intel Compiler XE Build 20110803
Auto Parallel:	No
File System:	ext3
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V9.01

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	**692**	**226**	691	226	693	226	16	**580**	**269**	581	269	580	269
401.bzip2	16	**881**	**175**	881	175	882	175	16	**835**	**185**	838	184	835	185
403.gcc	16	595	216	**591**	**218**	588	219	16	595	216	**591**	**218**	588	219
429.mcf	16	322	452	**322**	**454**	321	454	16	322	452	**322**	**454**	321	454
445.gobmk	16	667	252	**668**	**251**	669	251	16	637	263	**637**	**263**	634	265
456.hmmer	16	**428**	**349**	428	349	426	350	16	330	452	**327**	**456**	327	456
458.sjeng	16	**800**	**242**	801	242	800	242	16	**746**	**259**	743	260	748	259
462.libquantum	16	181	1830	180	1840	**181**	**1830**	16	181	1830	180	1840	**181**	**1830**
464.h264ref	16	1002	353	**1002**	**353**	1000	354	16	998	355	1009	351	**1000**	**354**
471.omnetpp	16	**501**	**200**	501	200	501	199	16	**466**	**215**	467	214	465	215
473.astar	16	608	185	**606**	**185**	605	186	16	608	185	**606**	**185**	605	186
483.xalancbmk	16	332	333	**332**	**333**	333	332	16	332	333	**332**	**333**	333	332

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
    The config file option 'submit' was used.
    numactl was used to bind copies to the cores
```

Operating System Notes

```
    Stack size set to unlimited using "ulimit -s unlimited"
    Transparent Huge Pages enabled with:
    echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
    Filesystem page cache cleared with:
    echo 1>         /proc/sys/vm/drop_caches
    runspec command invoked through numactl i.e.:
    numactl --interleave=all runspec <etc>
```

Platform Notes

```
     BIOS configuration:
      HP Power Profile set to Maximum Performance
      Thermal Configuration set to Increased Cooling
      Data Reuse set to Disabled
```

General Notes

```
    Environment variables set by runspec before the start of the run:
    LD_LIBRARY_PATH = "/cpu2006/smartheap:/cpu2006/ic12.1-libs/ia32/:/cpu2006/ic12.1-libs/intel64"
```

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Base Compiler Invocation

C benchmarks:
    icc -m32

C++ benchmarks:
    icpc -m32

Base Portability Flags

    400.perlbench: -DSPEC_CPU_LINUX_IA32
    462.libquantum: -DSPEC_CPU_LINUX
    483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
    -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3

C++ benchmarks:
    -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
    -Wl,-z,muldefs -L/smartheap -lsmartheap

Base Other Flags

C benchmarks:

    403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
    icc -m32

    400.perlbench: icc -m64

    401.bzip2: icc -m64

    456.hmmer: icc -m64

    458.sjeng: icc -m64

C++ benchmarks:
    icpc -m32

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Peak Portability Flags

    400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
        401.bzip2: -DSPEC_CPU_LP64
       456.hmmer: -DSPEC_CPU_LP64
        458.sjeng: -DSPEC_CPU_LP64
   462.libquantum: -DSPEC_CPU_LINUX
   483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

    400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                -auto-ilp32

       401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                -opt-prefetch -auto-ilp32 -ansi-alias

         403.gcc: basepeak = yes

         429.mcf: basepeak = yes

       445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
                -ansi-alias -opt-mem-layout-trans=3

       456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

        458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                -unroll4 -auto-ilp32

   462.libquantum: basepeak = yes

      464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                -unroll2 -ansi-alias

C++ benchmarks:

     471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
                -L/smartheap -lsmartheap

        473.astar: basepeak = yes

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Peak Optimization Flags (Continued)

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/HP-Intel-Linux-Settings-flags.20111122.html
http://www.spec.org/cpu2006/flags/Intel-ic12.1-linux64.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/HP-Intel-Linux-Settings-flags.20111122.xml
http://www.spec.org/cpu2006/flags/Intel-ic12.1-linux64.xml

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Tested with SPEC CPU2006 v1.1.
Report generated on Thu Jul 24 01:02:55 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 12 December 2011.