



# SPEC® CINT2006 Result

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Itaotec

SPECint®\_rate2006 = 398

Servidor Itaotec MX223+ (Intel Xeon X5675)

SPECint\_rate\_base2006 = 374

CPU2006 license: 9001

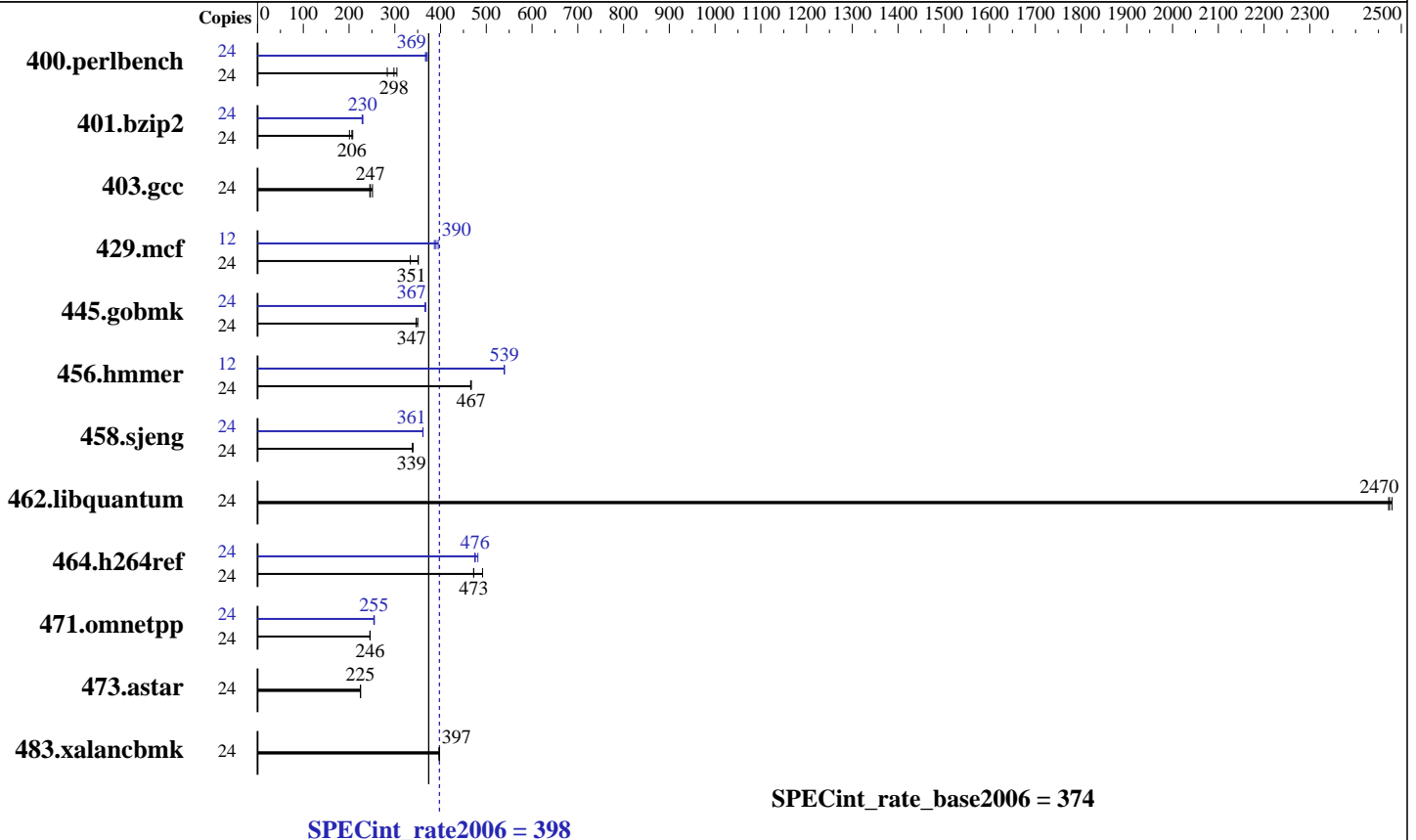
Test date: Oct-2011

Test sponsor: Itaotec

Hardware Availability: Jul-2011

Tested by: Itaotec

Software Availability: Aug-2011



## Hardware

CPU Name: Intel Xeon X5675  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.46 GHz  
 CPU MHz: 3067  
 FPU: Integrated  
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 12 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 48 GB (12 x 4 GB 2Rx4 PC3-10600R-9, ECC)  
 Disk Subsystem: 1 x 500 GB SAS, 15000 RPM  
 Other Hardware: None

## Software

Operating System: SUSE Linux Enterprise Server 11 SP1 (x86\_64), Kernel 2.6.32.12-0.7-default  
 Compiler: C/C++: Version 12.1.0 of Intel Compiler Build 20110811  
 Auto Parallel: No  
 File System: ext3  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1



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## Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio		
400.perlbench	24	770	305	827	284	<b>787</b>	<b>298</b>	24	<b>636</b>	<b>369</b>	633	371	639	367		
401.bzip2	24	<b>1124</b>	<b>206</b>	1153	201	1113	208	24	1008	230	<b>1009</b>	<b>230</b>	1013	229		
403.gcc	24	<b>784</b>	<b>247</b>	769	251	787	245	24	<b>784</b>	<b>247</b>	769	251	787	245		
429.mcf	24	623	351	655	334	<b>623</b>	<b>351</b>	12	283	387	<b>281</b>	<b>390</b>	277	395		
445.gobmk	24	719	350	726	347	<b>726</b>	<b>347</b>	24	<b>687</b>	<b>367</b>	687	366	685	367		
456.hammer	24	479	467	<b>480</b>	<b>467</b>	481	466	12	208	539	<b>208</b>	<b>539</b>	208	539		
458.sjeng	24	857	339	855	340	<b>856</b>	<b>339</b>	24	804	361	803	362	<b>803</b>	<b>361</b>		
462.libquantum	24	<b>201</b>	<b>2470</b>	201	2480	201	2470	24	<b>201</b>	<b>2470</b>	201	2480	201	2470		
464.h264ref	24	1125	472	1080	492	<b>1124</b>	<b>473</b>	24	1103	482	<b>1116</b>	<b>476</b>	1118	475		
471.omnetpp	24	609	246	611	246	<b>610</b>	<b>246</b>	24	590	254	<b>589</b>	<b>255</b>	588	255		
473.astar	24	<b>749</b>	<b>225</b>	749	225	749	225	24	<b>749</b>	<b>225</b>	749	225	749	225		
483.xalancbmk	24	<b>417</b>	<b>397</b>	417	397	416	398	24	<b>417</b>	<b>397</b>	417	397	416	398		

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.  
Large pages were not enabled for this run

## Platform Notes

Data Reuse disabled in BIOS.

## General Notes

This result was measured on the Servidor Itaotec MX224.  
The Servidor Itaotec MX203+, Servidor Itaotec MX223+ and the Servidor Itaotec MX224 are electronically equivalent.

## Base Compiler Invocation

C benchmarks:  
icc -m32

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## Base Compiler Invocation (Continued)

C++ benchmarks:  
icpc -m32

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

C++ benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/home/rcaneca/sh/SmartHeap\_8.1/lib -lsmartheap  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:  
icpc -m32



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## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-prefetch -auto-ilp32 -ansi-alias  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

403.gcc: basepeak = yes

429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -auto-ilp32

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
-ansi-alias -auto-ilp32

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll4 -auto-ilp32  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/home/rcaneca/sh/SmartHeap\_8.1/lib -lsmarheap

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## Peak Optimization Flags (Continued)

473.astar: basepeak = yes  
483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-linux64.html>  
<http://www.spec.org/cpu2006/flags/Itautec-Intel-Linux64-Platform.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-linux64.xml>  
<http://www.spec.org/cpu2006/flags/Itautec-Intel-Linux64-Platform.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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