



SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Intel Corporation

SPECint®2006 = 33.1

Intel DH57JG Motherboard (Intel Core i5-661)

SPECint_base2006 = 31.5

CPU2006 license: 13

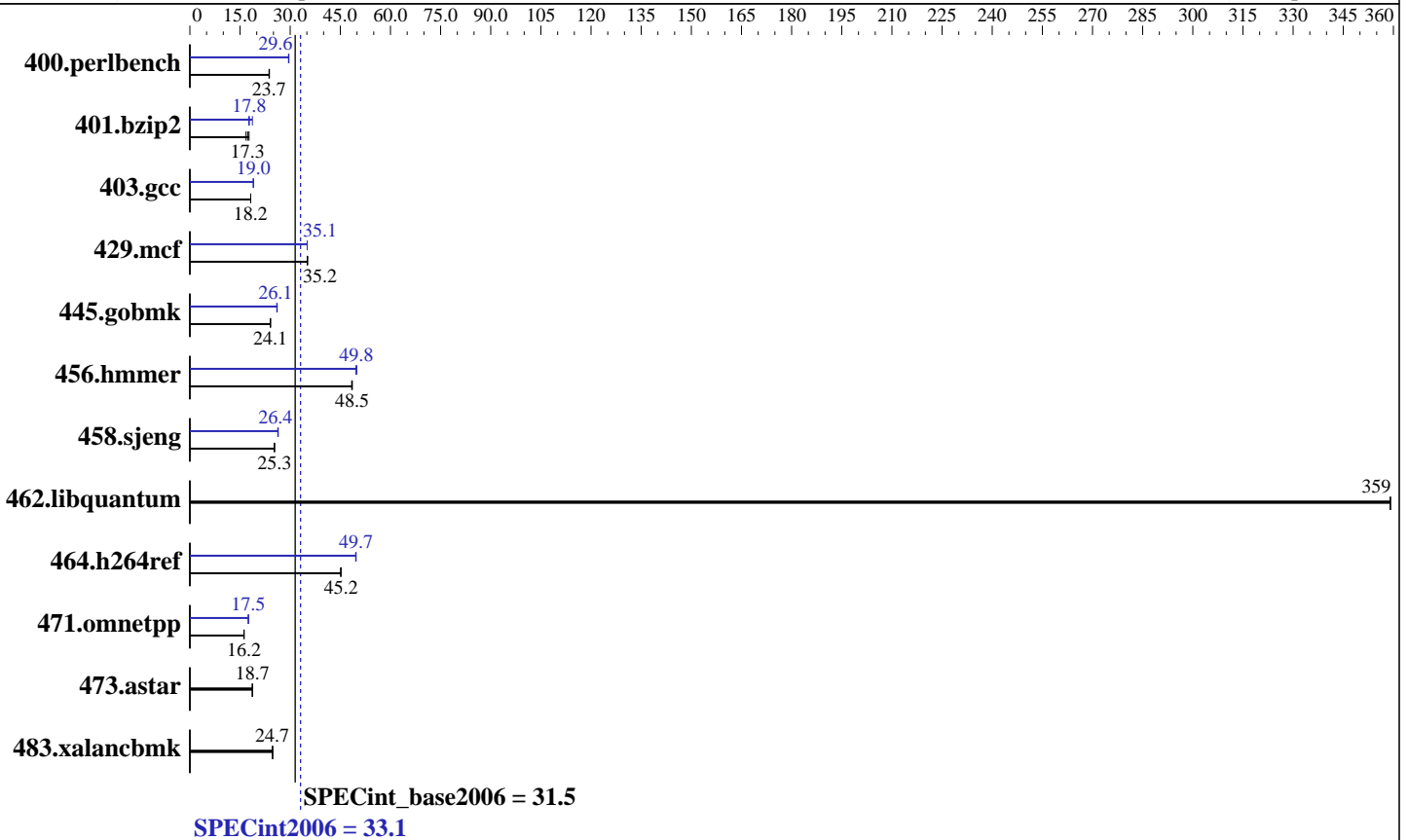
Test date: Jul-2011

Test sponsor: Intel Corporation

Hardware Availability: Jan-2010

Tested by: Intel Corporation

Software Availability: Apr-2011



Hardware

CPU Name: Intel Core i5-661
 CPU Characteristics: Intel Turbo Boost Technology up to 3.60 GHz
 CPU MHz: 3333
 FPU: Integrated
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip, 2 threads/core
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 4 MB I+D on chip per chip
 Other Cache: None
 Memory: 4 GB (2 x 2 GB 2Rx8 PC3-10600U-9)
 Disk Subsystem: Seagate 1 TB SATA, 7200 RPM
 Other Hardware: None

Software

Operating System: Windows 7 Ultimate (64-bit)
 Compiler: Intel C++ Compiler XE for Intel64
 Version 12.0.3.176 Build 20110309
 Microsoft Visual Studio 2008 Professional SP1
 (for libraries)
 Auto Parallel: Yes
 File System: NTFS
 System State: Default
 Base Pointers: 32/64-bit
 Peak Pointers: 32/64-bit
 Other Software: SmartHeap Library Version 9.01 from
<http://www.microquill.com/>



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Intel Corporation

SPECint2006 = 33.1

Intel DH57JG Motherboard (Intel Core i5-661)

SPECint_base2006 = 31.5

CPU2006 license: 13

Test date: Jul-2011

Test sponsor: Intel Corporation

Hardware Availability: Jan-2010

Tested by: Intel Corporation

Software Availability: Apr-2011

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	411	23.8	411	23.7	411	23.7	331	29.5	331	29.6	331	29.6
401.bzip2	558	17.3	546	17.7	573	16.8	550	17.6	541	17.8	515	18.7
403.gcc	443	18.2	443	18.2	443	18.2	423	19.0	424	19.0	423	19.0
429.mcf	260	35.1	259	35.2	259	35.2	260	35.1	260	35.1	260	35.1
445.gobmk	436	24.1	436	24.1	436	24.1	403	26.0	403	26.1	403	26.1
456.hmmer	192	48.6	192	48.5	193	48.4	188	49.7	187	49.9	187	49.8
458.sjeng	478	25.3	478	25.3	478	25.3	459	26.4	459	26.4	459	26.3
462.libquantum	57.7	359	57.7	359	57.7	359	57.7	359	57.7	359	57.7	359
464.h264ref	490	45.2	490	45.1	490	45.2	445	49.7	446	49.7	446	49.6
471.omnetpp	385	16.2	385	16.2	385	16.2	357	17.5	357	17.5	357	17.5
473.astar	376	18.7	375	18.7	376	18.7	376	18.7	375	18.7	376	18.7
483.xalancbmk	279	24.8	279	24.7	279	24.7	279	24.8	279	24.7	279	24.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

Tested systems can be used with Shin-G ATX case,
PC Power and Cooling 1200W power supply
OMP_NUM_THREADS set to number of processors cores
KMP_AFFINITY set to granularity=fine,scatter

Base Compiler Invocation

C benchmarks:

```
icl -Qvc9 -Qstd=c99
```

C++ benchmarks:

```
icl -Qvc9
```

Base Portability Flags

```

400.perlbench: -DSPEC_CPU_P64 -DSPEC_CPU_WIN64_X64
               -DSPEC_CPU_NO_NEED_VA_COPY
401.bzip2: -DSPEC_CPU_P64
403.gcc: -DSPEC_CPU_P64 -DSPEC_CPU_WIN64
429.mcf: -DSPEC_CPU_P64
445.gobmk: -DSPEC_CPU_P64
456.hmmer: -DSPEC_CPU_P64
458.sjeng: -DSPEC_CPU_P64

```

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Intel Corporation

SPECint2006 = 33.1

Intel DH57JG Motherboard (Intel Core i5-661)

SPECint_base2006 = 31.5

CPU2006 license: 13

Test date: Jul-2011

Test sponsor: Intel Corporation

Hardware Availability: Jan-2010

Tested by: Intel Corporation

Software Availability: Apr-2011

Base Portability Flags (Continued)

```

462.libquantum: -DSPEC_CPU_P64
464.h264ref: -DSPEC_CPU_P64 -DWIN32 -DSPEC_CPU_NO_INTTYPES
471.omnetpp: -DSPEC_CPU_P64 -DSPEC_CPU_WIN64
473.astar: -DSPEC_CPU_P64
483.xalancbmk: -DSPEC_CPU_P64 -Qoption, cpp, --no_wchar_t_keyword

```

Base Optimization Flags

C benchmarks:

```

-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qparallel
-Qauto-ilp32 /F512000000

```

C++ benchmarks:

```

-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qcxx-features
-Qauto-ilp32 /F512000000 shlw64M.lib -link /FORCE:MULTIPLE

```

Base Other Flags

C benchmarks:

```

403.gcc: -Dalloca=_alloca

```

Peak Compiler Invocation

C benchmarks:

```

icl -Qvc9 -Qstd=c99

```

C++ benchmarks:

```

icl -Qvc9

```

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

```

400.perlbench: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qansi-alias -Qopt-prefetch
-Qauto-ilp32 /F512000000 shlw64M.lib

```

Continued on next page

Standard Performance Evaluation Corporation

info@spec.org

http://www.spec.org/

Page 3



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Intel Corporation

SPECint2006 = 33.1

Intel DH57JG Motherboard (Intel Core i5-661)

SPECint_base2006 = 31.5

CPU2006 license: 13

Test date: Jul-2011

Test sponsor: Intel Corporation

Hardware Availability: Jan-2010

Tested by: Intel Corporation

Software Availability: Apr-2011

Peak Optimization Flags (Continued)

400.perlbench (continued):

-link /FORCE:MULTIPLE

401.bzip2: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qopt-prefetch -Qansi-alias
-Qauto-ilp32 /F512000000

403.gcc: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qauto-ilp32 /F512000000

429.mcf: -QxSSE4.2 -Qipo -O3 -Qprec-div- -Qopt-prefetch
-Qauto-ilp32 /F512000000

445.gobmk: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O2 -Qprec-div- -Qansi-alias -Qauto-ilp32
/F512000000

456.hmmer: -QxSSE4.2 -Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias
-Qauto-ilp32 /F512000000

458.sjeng: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qunroll4 -Qauto-ilp32 /F512000000

462.libquantum: basepeak = yes

464.h264ref: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias
-Qauto-ilp32 /F512000000

C++ benchmarks:

471.omnetpp: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qansi-alias
-Qopt-ra-region-strategy=block -Qauto-ilp32 /F512000000
shlW64M.lib -link /FORCE:MULTIPLE

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Intel Corporation

SPECint2006 = 33.1

Intel DH57JG Motherboard (Intel Core i5-661)

SPECint_base2006 = 31.5

CPU2006 license: 13

Test sponsor: Intel Corporation

Tested by: Intel Corporation

Test date: Jul-2011

Hardware Availability: Jan-2010

Software Availability: Apr-2011

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12-winx64-revC.html>

<http://www.spec.org/cpu2006/flags/Intel-Windows-Platform-Settings.20110719.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12-winx64-revC.xml>

<http://www.spec.org/cpu2006/flags/Intel-Windows-Platform-Settings.20110719.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Wed Jul 23 22:13:52 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 22 August 2011.