



# SPEC® CINT2006 Result

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## Intel Corporation

SPECint®2006 = 45.1

Intel DH67BLB3 Motherboard (Intel Core i5-2500)

SPECint\_base2006 = 42.7

CPU2006 license: 13

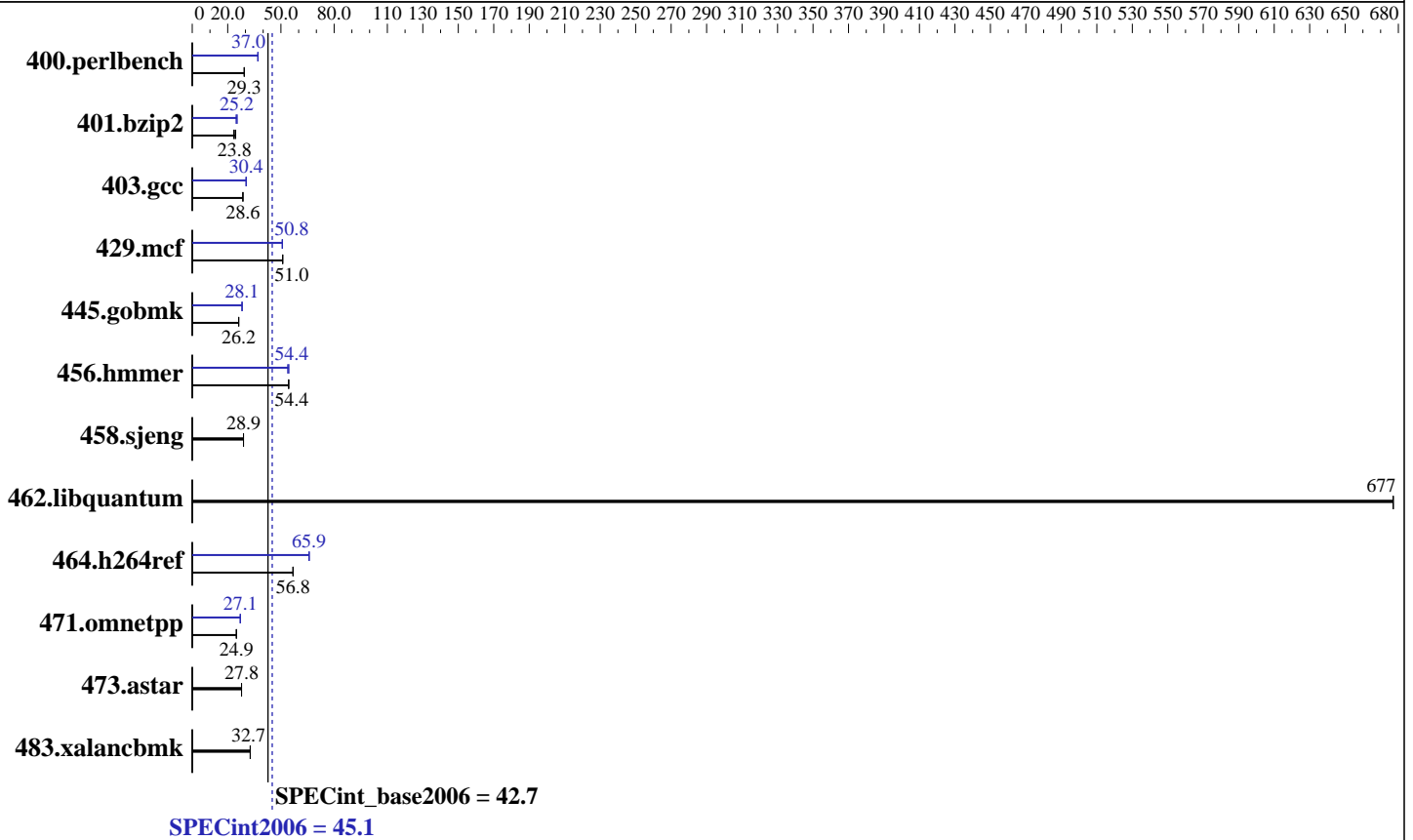
Test date: Jul-2011

Test sponsor: Intel Corporation

Hardware Availability: Mar-2011

Tested by: Intel Corporation

Software Availability: Apr-2011



### Hardware

CPU Name: Intel Core i5-2500  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.7 GHz  
 CPU MHz: 3300  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 6 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 4 GB (2 x 2 GB 2Rx8 PC3-10600U-9)  
 Disk Subsystem: Seagate 1 TB SATA, 7200 RPM  
 Other Hardware: None

### Software

Operating System: Windows 7 Ultimate (64-bit)  
 Compiler: Intel C++ Compiler XE for IA32 and Intel 64 Version 12.0.3.176 Build 20110309  
 Microsoft Visual Studio 2008 Professional SP1 (for libraries)  
 Auto Parallel: Yes  
 File System: NTFS  
 System State: Default  
 Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: SmartHeap Library Version 9.01 from <http://www.microquill.com/>



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	334	29.3	<b>333</b>	<b>29.3</b>	333	29.4	264	37.0	264	37.1	<b>264</b>	<b>37.0</b>
401.bzip2	<b>406</b>	<b>23.8</b>	412	23.4	394	24.5	381	25.3	<b>383</b>	<b>25.2</b>	391	24.7
403.gcc	282	28.6	<b>282</b>	<b>28.6</b>	282	28.5	265	30.4	<b>265</b>	<b>30.4</b>	265	30.4
429.mcf	<b>179</b>	<b>51.0</b>	179	51.0	179	51.0	179	50.9	<b>180</b>	<b>50.8</b>	180	50.7
445.gobmk	400	26.2	<b>400</b>	<b>26.2</b>	400	26.2	373	28.1	373	28.1	<b>373</b>	<b>28.1</b>
456.hmmer	172	54.3	172	54.4	<b>172</b>	<b>54.4</b>	<b>172</b>	<b>54.4</b>	171	54.4	173	53.8
458.sjeng	419	28.9	<b>419</b>	<b>28.9</b>	419	28.9	419	28.9	<b>419</b>	<b>28.9</b>	419	28.9
462.libquantum	30.6	677	<b>30.6</b>	<b>677</b>	30.6	677	30.6	677	<b>30.6</b>	<b>677</b>	30.6	677
464.h264ref	<b>389</b>	<b>56.8</b>	389	56.8	389	56.9	336	65.9	336	65.9	<b>336</b>	<b>65.9</b>
471.omnetpp	251	24.9	251	24.9	<b>251</b>	<b>24.9</b>	<b>231</b>	<b>27.1</b>	230	27.1	231	27.1
473.astar	253	27.8	<b>252</b>	<b>27.8</b>	252	27.8	253	27.8	<b>252</b>	<b>27.8</b>	252	27.8
483.xalancbmk	211	32.7	211	32.7	<b>211</b>	<b>32.7</b>	211	32.7	211	32.7	<b>211</b>	<b>32.7</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## General Notes

Tested systems can be used with Shin-G ATX case,  
PC Power and Cooling 1200W power supply  
OMP\_NUM\_THREADS set to number of processors cores  
KMP\_AFFINITY set to granularity=fine,scatter

## Base Compiler Invocation

C benchmarks:

```
icl -Qvc9 -Qstd=c99
```

C++ benchmarks:

```
icl -Qvc9
```

## Base Portability Flags

```

400.perlbench: -DSPEC_CPU_P64 -DSPEC_CPU_WIN64_X64
               -DSPEC_CPU_NO_NEED_VA_COPY
401.bzip2: -DSPEC_CPU_P64
403.gcc: -DSPEC_CPU_P64 -DSPEC_CPU_WIN64
429.mcf: -DSPEC_CPU_P64
445.gobmk: -DSPEC_CPU_P64
456.hmmer: -DSPEC_CPU_P64
458.sjeng: -DSPEC_CPU_P64

```

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## Base Portability Flags (Continued)

```

462.libquantum: -DSPEC_CPU_P64
464.h264ref: -DSPEC_CPU_P64 -DWIN32 -DSPEC_CPU_NO_INTTYPES
471.omnetpp: -DSPEC_CPU_P64 -DSPEC_CPU_WIN64
473.astar: -DSPEC_CPU_P64
483.xalancbmk: -DSPEC_CPU_P64 -Qoption, cpp, --no_wchar_t_keyword

```

## Base Optimization Flags

C benchmarks:

```

-QxAVX -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qparallel -Qauto-ilp32
/F512000000

```

C++ benchmarks:

```

-QxAVX -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qcxx-features
-Qauto-ilp32 /F512000000 shlw64M.lib -link /FORCE:MULTIPLE

```

## Base Other Flags

C benchmarks:

```

403.gcc: -Dalloca=_alloca

```

## Peak Compiler Invocation

C benchmarks:

```

icl -Qvc9 -Qstd=c99

```

C++ benchmarks:

```

icl -Qvc9

```

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

```

400.perlbench: -QxAVX(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo
-O3 -Qprec-div- -Qansi-alias -Qopt-prefetch -Qauto-ilp32
/F512000000 shlw64M.lib -link /FORCE:MULTIPLE

```

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## Peak Optimization Flags (Continued)

401.bzip2: -QxAVX(pass 2) -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -Qipo  
-O3 -Qprec-div- -Qopt-prefetch -Qansi-alias -Qauto-ilp32  
/F512000000

403.gcc: -QxAVX(pass 2) -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -Qipo  
-O3 -Qprec-div- -Qauto-ilp32 /F512000000

429.mcf: -QxAVX -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qauto-ilp32  
/F512000000

445.gobmk: -QxAVX(pass 2) -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -Qipo  
-O2 -Qprec-div- -Qansi-alias -Qauto-ilp32 /F512000000

456.hmmer: -QxAVX -Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias  
-Qauto-ilp32 /F512000000

458.sjeng: basepeak = yes

462.libquantum: basepeak = yes

464.h264ref: -QxAVX(pass 2) -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -Qipo  
-O3 -Qprec-div- -Qunroll2 -Qansi-alias -Qauto-ilp32  
/F512000000

C++ benchmarks:

471.omnetpp: -QxAVX(pass 2) -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -Qipo  
-O3 -Qprec-div- -Qansi-alias  
-Qopt-ra-region-strategy=block -Qauto-ilp32 /F512000000  
shlW64M.lib -link /FORCE:MULTIPLE

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12-winx64-revB.20110808.html>



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You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic12-winx64-revB.20110808.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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