SPEC® CINT2006 Result
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Dell Inc.

SPECint®_rate2006 = Not Run

PowerEdge R810 (Intel Xeon E7-2820, 2.00 GHz)

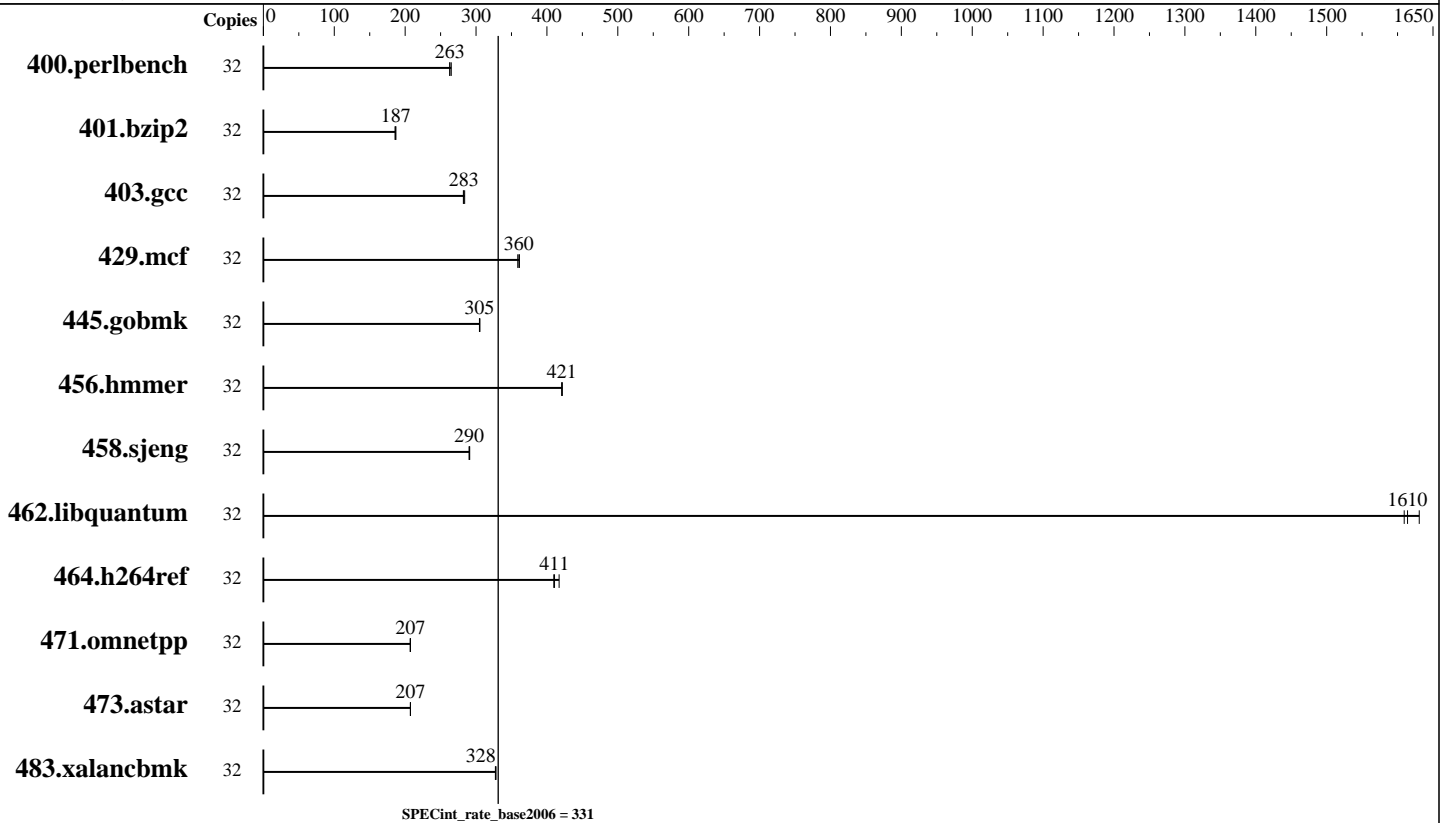SPECint_rate_base2006 = 331

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Apr-2011
Hardware Availability: Apr-2011
Software Availability: Jan-2011

Benchmark	Copies	Result
400.perlbench	32	263
401.bzip2	32	187
403.gcc	32	283
429.mcf	32	360
445.gobmk	32	305
456.hmmer	32	421
458.sjeng	32	290
462.libquantum	32	1610
464.h264ref	32	411
471.omnetpp	32	207
473.astar	32	207
483.xalancbmk	32	328

SPECint_rate_base2006 = 331

Hardware

CPU Name: Intel Xeon E7-2820
CPU Characteristics: Intel Turbo Boost Technology up to 2.26 GHz
CPU MHz: 2000
FPU: Integrated
CPU(s) enabled: 16 cores, 2 chips, 8 cores/chip, 2 threads/core
CPU(s) orderable: 2,4 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core
L3 Cache: 18 MB I+D on chip per chip
Other Cache: None
Memory: 256 GB (32 x 8 GB 4Rx8 PC3L-8500R-7, ECC, running at 978 MHz)
Disk Subsystem: 2 x 300 GB 10000 RPM SAS, RAID0
Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 11 SP1 (x86_64), Kernel 2.6.32.12-0.7-default
Compiler: Intel C++ Compiler XE for applications running on IA-32
Version 12.0.1.116 Build 20101116
Auto Parallel: No
File System: ext3
System State: Run level 3 (multi-user)
Base Pointers: 32-bit
Peak Pointers: Not Applicable
Other Software: Microquill SmartHeap V9.01

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Results Table

Benchmark	Copies	Base Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Peak Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	32	1189	263	**1188**	**263**	1180	265							
401.bzip2	32	1663	186	**1654**	**187**	1654	187							
403.gcc	32	**911**	**283**	913	282	907	284							
429.mcf	32	813	359	**810**	**360**	808	361							
445.gobmk	32	1101	305	**1101**	**305**	1099	306							
456.hmmer	32	708	422	**709**	**421**	709	421							
458.sjeng	32	**1333**	**290**	1333	290	1330	291							
462.libquantum	32	412	1610	407	1630	**411**	**1610**							
464.h264ref	32	1697	417	1729	410	**1724**	**411**							
471.omnetpp	32	964	207	**965**	**207**	965	207							
473.astar	32	1083	207	1082	208	**1083**	**207**							
483.xalancbmk	32	675	327	**673**	**328**	673	328							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
   The config file option 'submit' was used.
    numactl was used to bind copies to the cores
```

Operating System Notes

```
    'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
   'mount -t hugetlbfs nodev /mnt/hugepages' was used to enable large pages
   echo 14400> /proc/sys/vm/nr_hugepages
   export HUGETLB_MORECORE=yes
   export LD_PRELOAD=/usr/lib64/libhugetlbfs.so
```

Platform Notes

```
   BIOS Settings:
   Power Management = Maximum Performance (Default = Active Power Controller)
```

General Notes

```
   Binaries were compiled on RHEL5.5
```

Base Compiler Invocation

C benchmarks:
```
     icc  -m32
```

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Base Compiler Invocation (Continued)

C++ benchmarks:
      icpc -m32

Base Portability Flags

       400.perlbench: -DSPEC_CPU_LINUX_IA32
       462.libquantum: -DSPEC_CPU_LINUX
       483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
      -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch
      -B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

C++ benchmarks:
      -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
      -L/smartheap -lsmartheap
      -B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

Base Other Flags

C benchmarks:

          403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html
http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20110524.00.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml
http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20110524.00.xml

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Tested with SPEC CPU2006 v1.1.
Report generated on Wed Jul 23 17:32:59 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 21 June 2011.