



SPEC[®] CFP2006 Result

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Dell Inc.

SPECfp[®]_rate2006 = 427

PowerEdge M915 (AMD Opteron 6124 HE, 1.80 GHz)

SPECfp_rate_base2006 = 385

CPU2006 license: 55

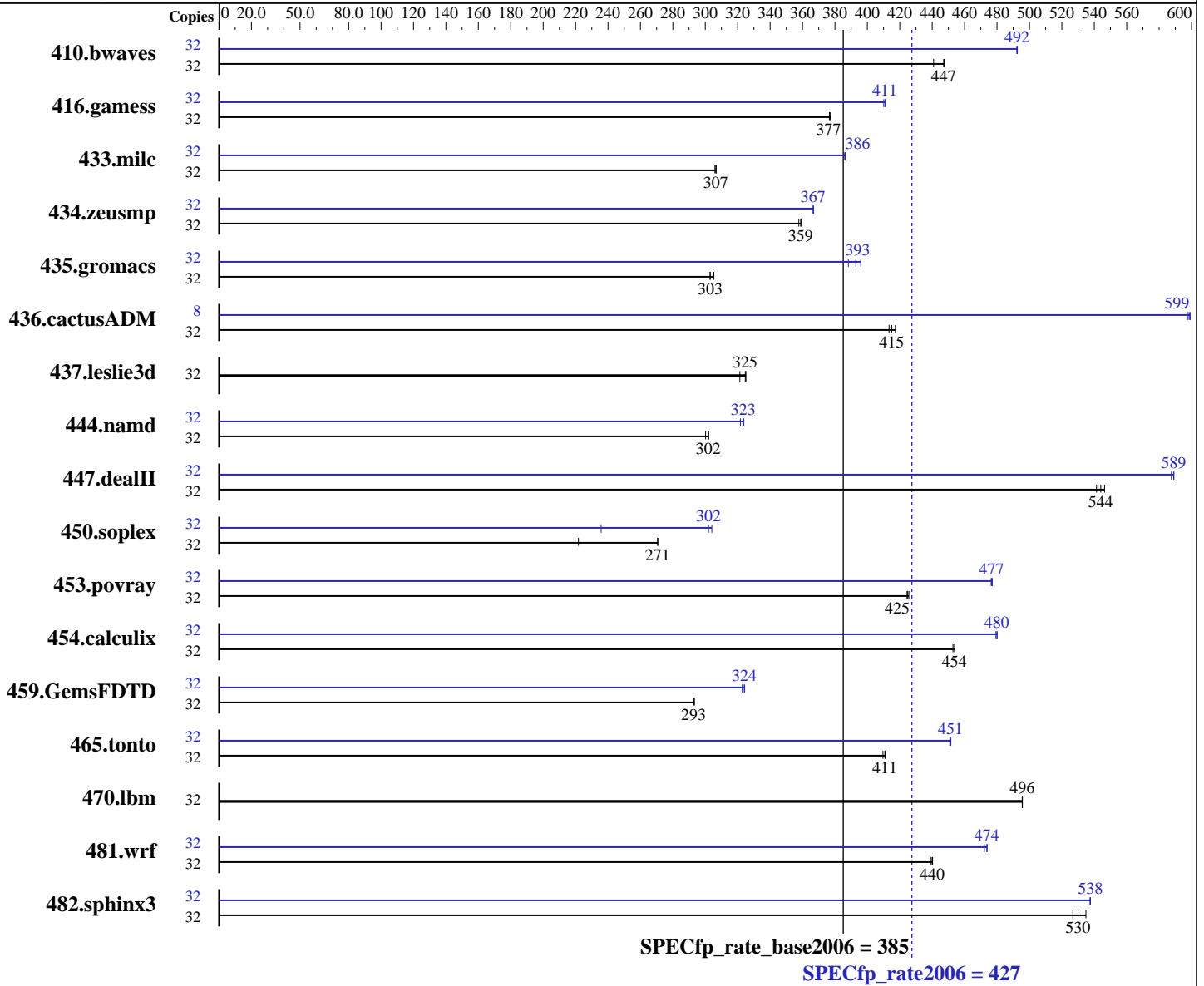
Test date: May-2011

Test sponsor: Dell Inc.

Hardware Availability: Jul-2010

Tested by: Dell Inc.

Software Availability: Jul-2010



Hardware

CPU Name: AMD Opteron 6124 HE
 CPU Characteristics:
 CPU MHz: 1800
 FPU: Integrated
 CPU(s) enabled: 32 cores, 4 chips, 8 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 64 KB I + 64 KB D on chip per core
 Secondary Cache: 512 KB I+D on chip per core

Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64), Kernel 2.6.27.19-5-default
 Compiler: x86 Open64 4.2.4 Compiler Suite (from AMD)
 Auto Parallel: Yes
 File System: ext3
 System State: Run level 3 (Full multiuser with network)
 Base Pointers: 64-bit
 Peak Pointers: 32/64-bit
 Other Software: None

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L3 Cache: 12 MB I+D on chip per chip, 6 MB shared / 4 cores
 Other Cache: None
 Memory: 128 GB (32 x 4 GB 2Rx4 PC3-10600R-9, ECC)
 Disk Subsystem: 1 x 146 GB 10000 RPM SAS
 Other Hardware: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	32	986	441	<u>973</u>	<u>447</u>	972	447	32	883	493	884	492	<u>883</u>	<u>492</u>
416.gamess	32	1664	377	<u>1661</u>	<u>377</u>	1659	378	32	1528	410	1524	411	<u>1525</u>	<u>411</u>
433.milc	32	<u>958</u>	<u>307</u>	958	307	960	306	32	761	386	<u>761</u>	<u>386</u>	760	386
434.zeusmp	32	<u>811</u>	<u>359</u>	811	359	814	358	32	794	367	795	366	<u>794</u>	<u>367</u>
435.gromacs	32	754	303	748	305	<u>754</u>	<u>303</u>	32	<u>582</u>	<u>393</u>	577	396	588	388
436.cactusADM	32	925	413	916	417	<u>921</u>	<u>415</u>	8	<u>160</u>	<u>599</u>	160	598	160	599
437.leslie3d	32	936	321	<u>926</u>	<u>325</u>	925	325	32	936	321	<u>926</u>	<u>325</u>	925	325
444.namd	32	855	300	850	302	<u>850</u>	<u>302</u>	32	798	322	<u>793</u>	<u>323</u>	793	324
447.dealII	32	<u>673</u>	<u>544</u>	670	546	676	541	32	<u>621</u>	<u>589</u>	621	589	623	588
450.soplex	32	1204	222	<u>986</u>	<u>271</u>	986	271	32	1132	236	<u>884</u>	<u>302</u>	878	304
453.povray	32	400	426	<u>401</u>	<u>425</u>	401	424	32	<u>357</u>	<u>477</u>	357	477	357	476
454.calculix	32	<u>582</u>	<u>454</u>	583	453	581	454	32	551	479	550	480	<u>550</u>	<u>480</u>
459.GemsFDTD	32	1157	293	1160	293	<u>1159</u>	<u>293</u>	32	<u>1048</u>	<u>324</u>	1052	323	1047	324
465.tonto	32	769	410	<u>766</u>	<u>411</u>	766	411	32	698	451	697	452	<u>698</u>	<u>451</u>
470.lbm	32	887	496	887	496	<u>887</u>	<u>496</u>	32	887	496	887	496	<u>887</u>	<u>496</u>
481.wrf	32	814	439	<u>812</u>	<u>440</u>	812	440	32	754	474	<u>755</u>	<u>474</u>	757	472
482.sphinx3	32	1184	527	1166	535	<u>1177</u>	<u>530</u>	32	1160	538	<u>1160</u>	<u>538</u>	1161	537

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
 'numactl' was used to bind copies to the cores.
 See the configuration file for details.

Operating System Notes

'ulimit -s unlimited' was used to set environment stack size
 'ulimit -l 2097152' was used to set environment locked pages in memory limit
 Set vm/nr_hugepages=28672 in /etc/sysctl.conf
 mount -t hugetlbfs nodev /mnt/hugepages



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General Notes

environment variables set by runspec before the start of the run:

HUGETLB_LIMIT = "896"

LD_LIBRARY_PATH = "/root/cpu2006-1.1/amd1002-rate-libs-revC/64:/root/cpu2006-1.1/amd1002-rate-libs-revC/32"

OMP_NUM_THREADS = "4"

The x86 Open64 Compiler Suite is only available from (and supported by) AMD at

<http://developer.amd.com/cpu/open64>

Binaries were compiled on SLES10 SP2 with binutils 2.18

Base Compiler Invocation

C benchmarks:

opencc

C++ benchmarks:

openCC

Fortran benchmarks:

openf95

Benchmarks using both Fortran and C:

opencc openf95

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
 416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
 434.zeusmp: -DSPEC_CPU_LP64
 435.gromacs: -DSPEC_CPU_LP64
 436.cactusADM: -DSPEC_CPU_LP64 -fno-second-underscore
 437.leslie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
 447.dealII: -DSPEC_CPU_LP64
 450.soplex: -DSPEC_CPU_LP64
 453.povray: -DSPEC_CPU_LP64
 454.calculix: -DSPEC_CPU_LP64
 459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX -DSPEC_CPU_CASE_FLAG
 -fno-second-underscore
 482.sphinx3: -DSPEC_CPU_LP64



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Base Optimization Flags

C benchmarks:

-march=barcelona -mso -Ofast -OPT:malloc_alg=1 -HP:bdt=2m

C++ benchmarks:

-march=barcelona -mso -Ofast -static -INLINE:aggressive=on
-OPT:malloc_alg=1 -HP:bdt=2m

Fortran benchmarks:

-march=barcelona -mso -Ofast -HP

Benchmarks using both Fortran and C:

-march=barcelona -mso -Ofast -OPT:malloc_alg=1 -HP:bdt=2m -HP

Peak Compiler Invocation

C benchmarks:

opencc

C++ benchmarks:

openCC

Fortran benchmarks:

openf95

Benchmarks using both Fortran and C:

opencc openf95

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64
436.cactusADM: -DSPEC_CPU_LP64 -fno-second-underscore
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
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Peak Optimization Flags

C benchmarks:

433.milc: -march=barcelona -mso -Ofast -CG:movnti=1
-CG:local_sched_alg=1 -CG:locs_shallow_depth=1
-HP:bdt=2m:heap=2m -LNO:prefetch=3

470.lbm: basepeak = yes

482.sphinx3: -march=barcelona -mso -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -Ofast -OPT:malloc_alg=2
-CG:sse_cse_regs=0 -CG:locs_shallow_depth=1 -CG:cmp_peep=on
-CG:local_sched_alg=1 -INLINE:aggressive=on

C++ benchmarks:

444.namd: -march=barcelona -mso -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -Ofast -LNO:ignore_feedback=off
-CG:local_sched_alg=2 -CG:load_exe=0 -CG:compute_to=on
-OPT:unroll_size=256 -fno-exceptions -HP:bdt=2m:heap=2m

447.deallI: -march=barcelona -mso -Ofast -static -INLINE:aggressive=on
-LNO:opt=0 -fno-emit-exceptions -m32
-OPT:unroll_times_max=8 -OPT:unroll_size=256
-OPT:unroll_level=2 -HP:bdt=2m:heap=2m -GRA:unspill=on
-CG:cmp_peep=on -TENV:frame_pointer=off

450.soplex: -march=barcelona -mso -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -O3 -INLINE:aggressive=on
-OPT:IEEE_arith=3 -OPT:IEEE_NaN_Inf=off
-OPT:fold_unsigned_relops=on -OPT:malloc_alg=1
-CG:load_exe=0 -fno-exceptions -m32 -HP:bdt=2m

453.povray: -march=barcelona -mso -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -Ofast -INLINE:aggressive=on

Fortran benchmarks:

410.bwaves: -march=barcelona -mso -O3 -OPT:Ofast -OPT:treeheight=on
-LNO:blocking=off -LNO:prefetch_ahead=5
-LNO:ignore_feedback=off -WOPT:aggstr=0 -HP:bdt=2m:heap=2m
-CG:cmp_peep=on

416.gamess: -march=barcelona -mso -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -O3 -LNO:fu=6 -LNO:blocking=0
-LNO:prefetch=0 -OPT:Ofast -OPT:ro=3 -OPT:unroll_size=256
-HP:bdt=2m:heap=2m

434.zeusmp: -march=barcelona -mso -Ofast -LNO:blocking=off
-LNO:interchange=off -OPT:treeheight=on -OPT:unroll_size=256
-CG:cmp_peep=on -GRA:prioritize_by_density=on -HP

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Peak Optimization Flags (Continued)

437.leslie3d: basepeak = yes

459.GemsFDTD: -march=barcelona -mso -Ofast -LNO:fission=2
-LNO:prefetch_ahead=1 -CG:load_exe=0 -CG:local_sched_alg=1
-HP

465.tonto: -march=barcelona -mso -Ofast
-OPT:alias=no_f90_pointer_alias -LNO:blocking=off
-CG:load_exe=1 -IPA:plimit=525 -HP

Benchmarks using both Fortran and C:

435.gromacs: -march=barcelona -mso -Ofast -OPT:rsqrt=2
-HP:bdt=2m:heap=2m

436.cactusADM: -march=barcelona -mso -fb_create fbdata(pass 1)
-fb_opt fbdata(pass 2) -Ofast -apo -LNO:prefetch_ahead=1
-HP:bdt=2m:heap=2m -LANG:heap_allocation_threshold=100

454.calculix: -march=barcelona -mso -Ofast -CG:load_exe=0
-CG:ptr_load_use=0 -CG:local_sched_alg=2 -CG:compute_to=on
-LNO:prefetch_ahead=30 -WOPT:unroll=2
-GRA:optimize_boundary=on -HP:bdt=2m:heap=2m

481.wrf: -march=barcelona -mso -Ofast -LNO:blocking=off
-LNO:prefetch_ahead=10 -LANG:copyinout=off
-IPA:callee_limit=5000 -GRA:prioritize_by_density=on -m3dnow
-HP

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/x86-open64-424-flags-rate-revC.20100901.html>
<http://www.spec.org/cpu2006/flags/amd-platform-rate-revC.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/x86-open64-424-flags-rate-revC.20100901.xml>
<http://www.spec.org/cpu2006/flags/amd-platform-rate-revC.xml>

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For other inquiries, please contact webmaster@spec.org.

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