SPEC® CINT2006 Result
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Itautec

Servidor Itautec MX224 (Intel Xeon X5650)

SPECint®_rate2006 = 366

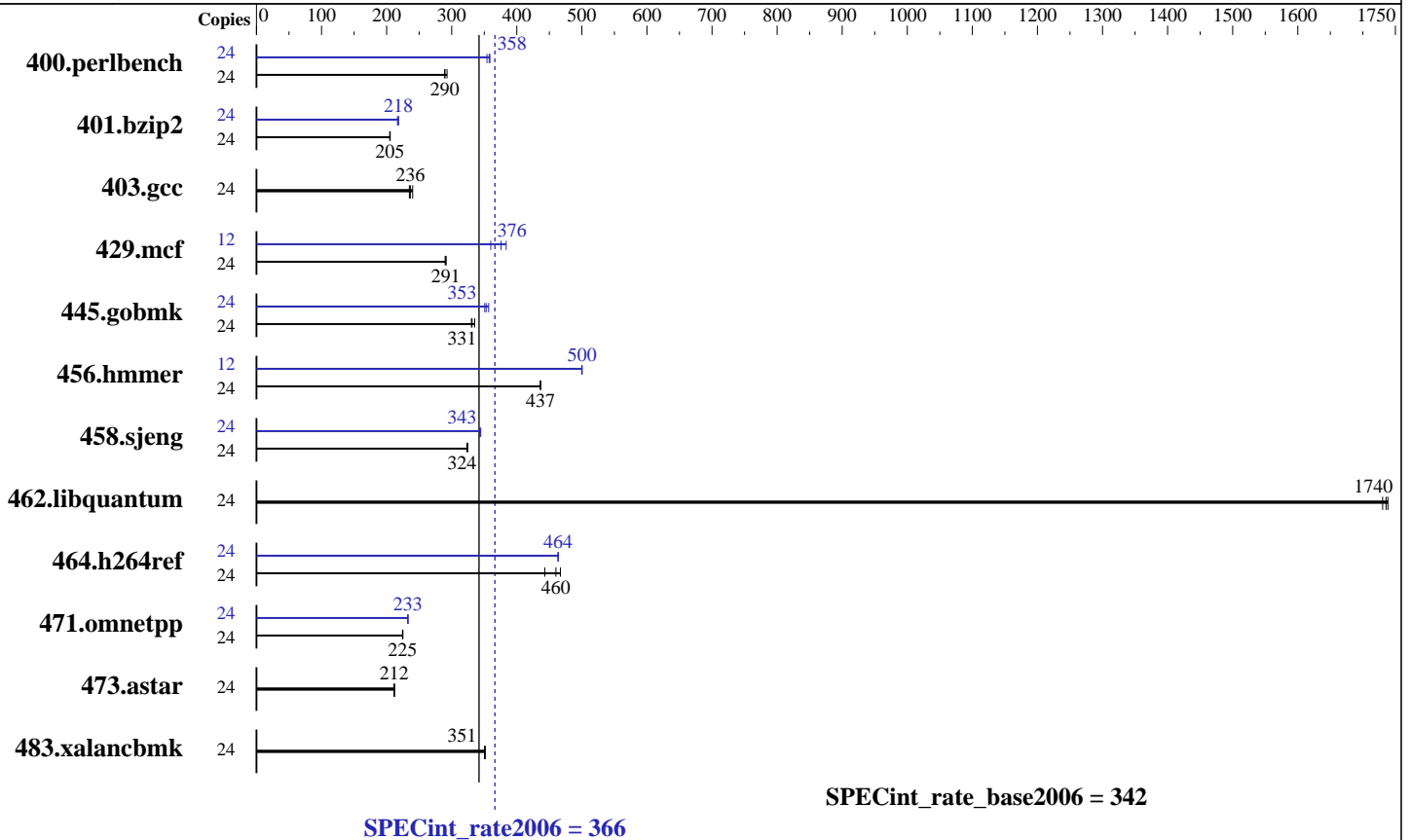SPECint_rate_base2006 = 342

CPU2006 license: 9001
Test sponsor: Itautec
Tested by: Itautec

Test date: May-2011
Hardware Availability: Feb-2011
Software Availability: Jan-2011

Copies

Benchmark	Copies	Result (blue)	Copies	Result (base)
400.perlbench	24	358	24	290
401.bzip2	24	218	24	205
403.gcc			24	236
429.mcf	12	376	24	291
445.gobmk	24	353	24	331
456.hmmer	12	500	24	437
458.sjeng	24	343	24	324
462.libquantum			24	1740
464.h264ref	24	464	24	460
471.omnetpp	24	233	24	225
473.astar			24	212
483.xalancbmk			24	351

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Hardware

CPU Name: Intel Xeon X5650
CPU Characteristics: Intel Turbo Boost Technology up to 3.06 GHz
CPU MHz: 2667
FPU: Integrated
CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core
L3 Cache: 12 MB I+D on chip per chip
Other Cache: None
Memory: 48 GB (12 x 4 GB 2Rx4 PC3-10600R-9, ECC)
Disk Subsystem: 1 x 500 GB SATA-2, 7200 RPM
Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 11 SP1 (x86_64), Kernel 2.6.32.12-0.7-default
Compiler: Intel C++ Compiler XE for applications running on IA-32
Version 12.0.2 Build 20110112
Auto Parallel: No
File System: ext3
System State: Run level 3 (multi-user)
Base Pointers: 32-bit
Peak Pointers: 32/64-bit
Other Software: Microquill SmartHeap V8.1

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Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	24	801	293	810	289	**810**	**290**	24	662	354	**656**	**358**	654	359
401.bzip2	24	**1128**	**205**	1131	205	1127	205	24	**1063**	**218**	1061	218	1069	217
403.gcc	24	805	240	822	235	**817**	**236**	24	805	240	822	235	**817**	**236**
429.mcf	24	755	290	751	291	**752**	**291**	12	**291**	**376**	304	360	285	384
445.gobmk	24	751	335	762	330	**760**	**331**	24	**713**	**353**	706	357	718	351
456.hmmer	24	**513**	**437**	514	436	512	437	12	224	500	**224**	**500**	224	500
458.sjeng	24	**897**	**324**	898	323	894	325	24	843	345	846	343	**846**	**343**
462.libquantum	24	**286**	**1740**	287	1730	286	1740	24	**286**	**1740**	287	1730	286	1740
464.h264ref	24	1137	467	**1154**	**460**	1199	443	24	1146	463	**1146**	**464**	1145	464
471.omnetpp	24	**667**	**225**	669	224	667	225	24	644	233	**645**	**233**	645	233
473.astar	24	795	212	796	212	**795**	**212**	24	795	212	796	212	**795**	**212**
483.xalancbmk	24	**471**	**351**	471	351	473	350	24	**471**	**351**	471	351	473	350

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
    The config file option 'submit' was used.
     numactl was used to bind copies to the cores
```

Operating System Notes

```
     'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.
     Large pages were not enabled for this run
```

General Notes

```
    This result was measured on the Servidor Itautec MX224.
    The Servidor Itautec MX203+, Servidor Itautec MX223+ and the Servidor Itautec MX224
    are electronically equivalent.
```

Base Compiler Invocation

C benchmarks:
```
     icc  -m32
```

C++ benchmarks:
```
     icpc -m32
```

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Base Portability Flags

```
    400.perlbench: -DSPEC_CPU_LINUX_IA32
    462.libquantum: -DSPEC_CPU_LINUX
    483.xalancbmk: -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:

```
    -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch
    -B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT
```

C++ benchmarks:

```
    -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
    -L/home/rcaneca/sh/SmartHeap_8.1/lib -lsmartheap
    -B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT
```

Base Other Flags

C benchmarks:

```
        403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks (except as noted below):
```
    icc  -m32
```

```
    400.perlbench: icc -m64
```

```
       401.bzip2: icc -m64
```

```
      456.hmmer: icc -m64
```

```
       458.sjeng: icc -m64
```

C++ benchmarks:
```
    icpc -m32
```

Peak Portability Flags

```
    400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
       401.bzip2: -DSPEC_CPU_LP64
```

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Peak Portability Flags (Continued)

```
    456.hmmer: -DSPEC_CPU_LP64
    458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
  400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                 -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

     401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                 -opt-prefetch -auto-ilp32 -ansi-alias
                 -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

      403.gcc: basepeak = yes

      429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                 -ansi-alias -auto-ilp32

    445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
                 -ansi-alias -auto-ilp32

    456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
                 -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

    458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                 -unroll4 -auto-ilp32
                 -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

462.libquantum: basepeak = yes

    464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                 -unroll2 -ansi-alias
```

C++ benchmarks:

```
   471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                 -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
                 -L/home/rcaneca/sh/SmartHeap_8.1/lib -lsmartheap
```

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Peak Optimization Flags (Continued)

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html
http://www.spec.org/cpu2006/flags/Itautec-Intel-Linux64-Platform.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml
http://www.spec.org/cpu2006/flags/Itautec-Intel-Linux64-Platform.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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