# SPEC® CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Dell Inc.

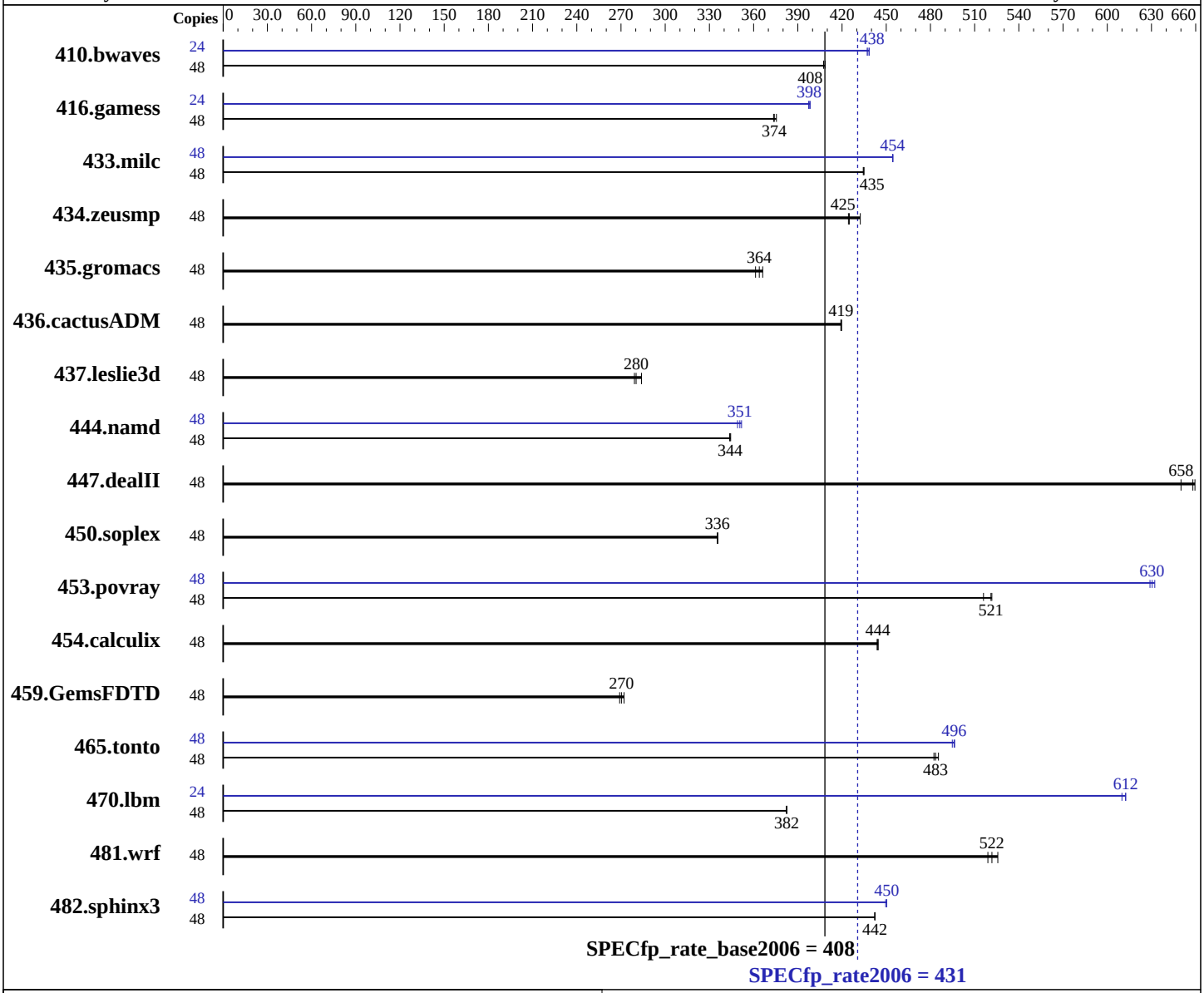
PowerEdge R910 (Intel Xeon L7545, 1.87 GHz)

SPECfp®_rate2006 = 431

SPECfp_rate_base2006 = 408

**CPU2006 license:** 55
**Test sponsor:** Dell Inc.
**Tested by:** Dell Inc.

**Test date:** Apr-2011
**Hardware Availability:** Jul-2011
**Software Availability:** Jan-2011




## Hardware

| | |
|---|---|
| CPU Name: | Intel Xeon L7545 |
| CPU Characteristics: | Intel Turbo Boost Technology up to 2.53 GHz |
| CPU MHz: | 1866 |
| FPU: | Integrated |
| CPU(s) enabled: | 24 cores, 4 chips, 6 cores/chip, 2 threads/core |
| CPU(s) orderable: | 2,4 chips |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 256 KB I+D on chip per core |

Continued on next page

## Software

| | |
|---|---|
| Operating System: | SUSE Linux Enterprise Server 11 SP1 (x86_64), Kernel 2.6.32.12-0.7-default |
| Compiler: | Intel C++ and Fortran Intel 64 Compiler XE for applications running on Intel 64 Version 12.0.1.116 Build 20101116 |
| Auto Parallel: | No |
| File System: | ext3 |
| System State: | Run level 3 (multi-user) |
| Base Pointers: | 64-bit |

Continued on next page

# SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Dell Inc.

PowerEdge R910 (Intel Xeon L7545, 1.87 GHz)

SPECfp_rate2006 = 431

SPECfp_rate_base2006 = 408

**CPU2006 license:** 55
**Test sponsor:** Dell Inc.
**Tested by:** Dell Inc.

**Test date:** Apr-2011
**Hardware Availability:** Jul-2011
**Software Availability:** Jan-2011

L3 Cache: 18 MB I+D on chip per chip
Other Cache: None
Memory: 512 GB (64 x 8 GB 4Rx8 PC3-8500R-7, ECC, running at 978 MHz)
Disk Subsystem: 1 x 500 GB 7200 RPM SAS 6Gb
Other Hardware: None

Peak Pointers: 32/64-bit
Other Software: None

## Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 410.bwaves | 48 | 1597 | 409 | 1601 | 407 | **1600** | **408** | 24 | 746 | 437 | 744 | 438 | **745** | **438** |
| 416.gamess | 48 | 2503 | 375 | 2516 | 374 | **2513** | **374** | 24 | 1183 | 397 | 1180 | 398 | **1181** | **398** |
| 433.milc | 48 | 1013 | 435 | 1014 | 434 | **1013** | **435** | 48 | 970 | 454 | **970** | **454** | 969 | 455 |
| 434.zeusmp | 48 | 1029 | 424 | **1028** | **425** | 1010 | 432 | 48 | 1029 | 424 | **1028** | **425** | 1010 | 432 |
| 435.gromacs | 48 | 936 | 366 | 949 | 361 | **942** | **364** | 48 | 936 | 366 | 949 | 361 | **942** | **364** |
| 436.cactusADM | 48 | **1368** | **419** | 1368 | 419 | 1367 | 420 | 48 | **1368** | **419** | 1368 | 419 | 1367 | 420 |
| 437.leslie3d | 48 | 1589 | 284 | 1616 | 279 | **1610** | **280** | 48 | 1589 | 284 | 1616 | 279 | **1610** | **280** |
| 444.namd | 48 | 1118 | 344 | 1120 | 344 | **1119** | **344** | 48 | 1103 | 349 | **1098** | **351** | 1094 | 352 |
| 447.dealII | 48 | 845 | 650 | 833 | 660 | **834** | **658** | 48 | 845 | 650 | 833 | 660 | **834** | **658** |
| 450.soplex | 48 | 1193 | 336 | 1193 | 335 | **1193** | **336** | 48 | 1193 | 336 | 1193 | 335 | **1193** | **336** |
| 453.povray | 48 | 490 | 522 | **490** | **521** | 495 | 516 | 48 | 404 | 632 | 406 | 629 | **405** | **630** |
| 454.calculix | 48 | 891 | 445 | 892 | 444 | **891** | **444** | 48 | 891 | 445 | 892 | 444 | **891** | **444** |
| 459.GemsFDTD | 48 | 1893 | 269 | 1872 | 272 | **1884** | **270** | 48 | 1893 | 269 | 1872 | 272 | **1884** | **270** |
| 465.tonto | 48 | **977** | **483** | 973 | 485 | 979 | 482 | 48 | 951 | 496 | **952** | **496** | 955 | 495 |
| 470.lbm | 48 | 1726 | 382 | **1724** | **382** | 1724 | 383 | 24 | 541 | 610 | **538** | **612** | 538 | 613 |
| 481.wrf | 48 | 1020 | 526 | 1033 | 519 | **1028** | **522** | 48 | 1020 | 526 | 1033 | 519 | **1028** | **522** |
| 482.sphinx3 | 48 | 2116 | 442 | **2115** | **442** | 2115 | 442 | 48 | **2078** | **450** | 2081 | 450 | 2077 | 450 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

```
The config file option 'submit' was used.
numactl was used to bind copies to the cores
```

## Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'mount -t hugetlbfs nodev /mnt/hugepages' was used to enable large pages
echo 43200 > /proc/sys/vm/nr_hugepages
export HUGETLB_MORECORE=yes
export LD_PRELOAD=/usr/lib64/libhugetlbfs.so
```

## Platform Notes

```
BIOS Settings:
Power Management = Maximum Performance (Default = Active Power Controller)
```

## General Notes

```
The Dell PowerEdge R910 and
the Bull NovaScale R480 F2 models are electronically equivalent.
The results have been measured on a Dell PowerEdge R910 model.
Binaries were compiled on RHEL5.5
```

# Base Compiler Invocation

C benchmarks:
```
icc   -m64
```

C++ benchmarks:
```
icpc  -m64
```

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc   -m64 ifort -m64
```

# Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

## Base Optimization Flags

C benchmarks:
`-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias`

C++ benchmarks:
`-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias`

Fortran benchmarks:
`-xSSE4.2 -ipo -O3 -no-prec-div -static`

Benchmarks using both Fortran and C:
`-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias`

## Peak Compiler Invocation

C benchmarks (except as noted below):
`icc -m64`

482.sphinx3: `icc -m32`

C++ benchmarks:
`icpc -m64`

Fortran benchmarks:
`ifort -m64`

Benchmarks using both Fortran and C:
`icc -m64 ifort -m64`

## Peak Portability Flags

410.bwaves: `-DSPEC_CPU_LP64`
416.gamess: `-DSPEC_CPU_LP64`
433.milc: `-DSPEC_CPU_LP64`
434.zeusmp: `-DSPEC_CPU_LP64`
435.gromacs: `-DSPEC_CPU_LP64 -nofor_main`
436.cactusADM: `-DSPEC_CPU_LP64 -nofor_main`
437.leslie3d: `-DSPEC_CPU_LP64`
444.namd: `-DSPEC_CPU_LP64`
447.dealII: `-DSPEC_CPU_LP64`
450.soplex: `-DSPEC_CPU_LP64`
453.povray: `-DSPEC_CPU_LP64`
454.calculix: `-DSPEC_CPU_LP64 -nofor_main`
459.GemsFDTD: `-DSPEC_CPU_LP64`
465.tonto: `-DSPEC_CPU_LP64`
470.lbm: `-DSPEC_CPU_LP64`

Continued on next page

## Peak Portability Flags (Continued)

481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

## Peak Optimization Flags

C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32

470.lbm: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3 -ansi-alias -opt-prefetch -static -auto-ilp32

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

Fortran benchmarks:

410.bwaves: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -static

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto -inline-calloc -opt-malloc-options=3 -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

Continued on next page

## Peak Optimization Flags (Continued)

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html
http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20110524.00.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml
http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20110524.00.xml


SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.


For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Wed Jul 23 20:39:31 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 24 May 2011.