



SPEC® CINT2006 Result

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IBM Corporation

SPECint®2006 = 39.5

IBM BladeCenter HS22V (Intel Xeon X5647)

SPECint_base2006 = 37.3

CPU2006 license: 11

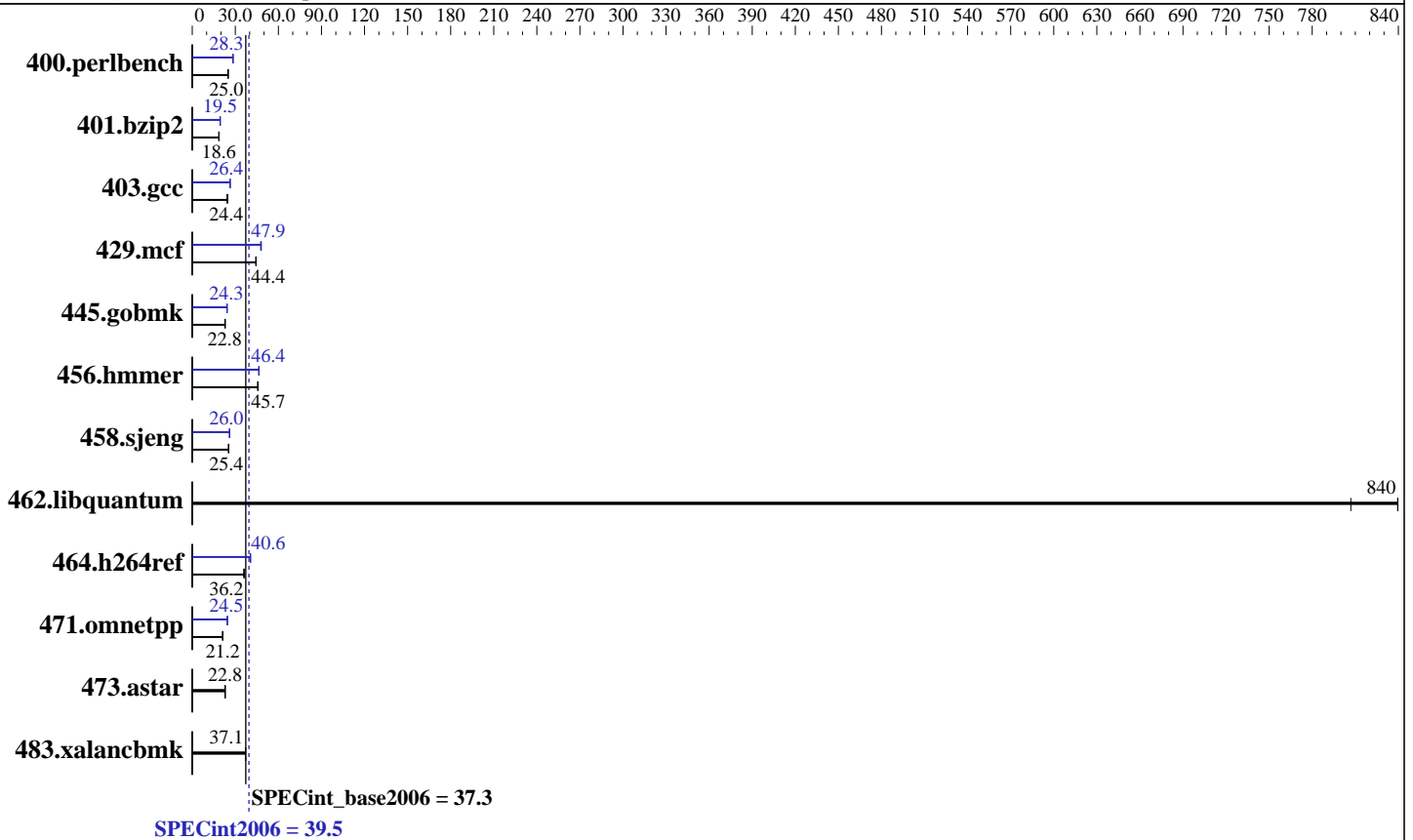
Test date: May-2011

Test sponsor: IBM Corporation

Hardware Availability: Feb-2011

Tested by: IBM Corporation

Software Availability: Jan-2011



Hardware

CPU Name: Intel Xeon X5647
 CPU Characteristics: Intel Turbo Boost Technology up to 3.20 GHz
 CPU MHz: 2933
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 12 MB I+D on chip per chip
 Other Cache: None
 Memory: 48 GB (12 x 4 GB 2Rx8 PC3-10600R-9, ECC, running at 1066 MHz)
 Disk Subsystem: 2 x 50 GB SATA, SSD, RAID 0
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 11 SP1 (x86_64), Kernel 2.6.32.12-0.7-default
 Compiler: Intel C++ Intel 64 Compiler XE for applications running on Intel 64 Version 12.0.1.116 Build 20101116
 Auto Parallel: Yes
 File System: ext3
 System State: Run level 3 (multi-user)
 Base Pointers: 32/64-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V9.01



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	<u>391</u>	<u>25.0</u>	393	24.8	389	25.1	<u>345</u>	<u>28.3</u>	342	28.6	346	28.3
401.bzip2	519	18.6	519	18.6	<u>519</u>	<u>18.6</u>	<u>495</u>	<u>19.5</u>	495	19.5	495	19.5
403.gcc	332	24.3	326	24.7	<u>329</u>	<u>24.4</u>	<u>305</u>	<u>26.4</u>	305	26.4	305	26.4
429.mcf	<u>206</u>	<u>44.4</u>	206	44.3	205	44.5	189	48.1	191	47.7	<u>191</u>	<u>47.9</u>
445.gobmk	462	22.7	<u>461</u>	<u>22.8</u>	454	23.1	432	24.3	432	24.3	<u>432</u>	<u>24.3</u>
456.hammer	204	45.7	<u>204</u>	<u>45.7</u>	204	45.7	201	46.4	<u>201</u>	<u>46.4</u>	201	46.4
458.sjeng	476	25.4	480	25.2	<u>477</u>	<u>25.4</u>	466	26.0	<u>466</u>	<u>26.0</u>	466	26.0
462.libquantum	<u>24.7</u>	<u>840</u>	24.7	840	25.7	807	<u>24.7</u>	<u>840</u>	24.7	840	25.7	807
464.h264ref	612	36.2	616	35.9	<u>612</u>	<u>36.2</u>	<u>545</u>	<u>40.6</u>	545	40.6	547	40.4
471.omnetpp	295	21.2	<u>295</u>	<u>21.2</u>	295	21.2	255	24.5	<u>255</u>	<u>24.5</u>	256	24.4
473.astar	304	23.1	<u>308</u>	<u>22.8</u>	308	22.8	304	23.1	<u>308</u>	<u>22.8</u>	308	22.8
483.xalancbmk	186	37.0	185	37.3	<u>186</u>	<u>37.1</u>	186	37.0	185	37.3	<u>186</u>	<u>37.1</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited
'nodev /mnt/hugepages hugetlbfs defaults 0 0' added to /etc/fstab
echo 900 > /proc/sys/vm/nr_hugepages
export HUGETLB_MORECORE=yes
export LD_PRELOAD=/usr/lib64/libhugetlbfs.so
```

Platform Notes

Load Default BIOS Settings and then change the following:

```
Turbo Mode enabled
Turbo Boost set to Traditional
Power C-states enabled
Demand Scrub disabled
```

General Notes

Binaries compiled on RHEL5.5

Base Compiler Invocation

C benchmarks:
 icc -m64

C++ benchmarks:
 icpc -m64



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Base Portability Flags

```

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

```

Base Optimization Flags

C benchmarks:

```

-xSSE4.2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

```

C++ benchmarks:

```

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/smartheap -lsmartheap64
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

```

Base Other Flags

C benchmarks:

```

403.gcc: -Dalloca=_alloca

```

Peak Compiler Invocation

C benchmarks (except as noted below):

```

icc -m64

400.perlbench: icc -m32

429.mcf: icc -m32

445.gobmk: icc -m32

464.h264ref: icc -m32

```

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Peak Compiler Invocation (Continued)

C++ benchmarks (except as noted below):

icpc -m64

471.omnetpp: icpc -m32

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
 401.bzip2: -DSPEC_CPU_LP64
 403.gcc: -DSPEC_CPU_LP64
 456.hmmmer: -DSPEC_CPU_LP64
 458.sjeng: -DSPEC_CPU_LP64
 462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
 473.astar: -DSPEC_CPU_LP64
 483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -opt-prefetch -ansi-alias
 -B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32
 -opt-prefetch -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -inline-calloc
 -opt-malloc-options=3 -auto-ilp32
 -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -auto-ilp32 -ansi-alias
 -B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
 -auto-ilp32 -ansi-alias
 -B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

456.hmmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
 -ansi-alias
 -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

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Peak Optimization Flags (Continued)

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll4

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-ra-region-strategy=block -ansi-alias -Wl,-z,muldefs
-L/smartheap -lsmartheap
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html>
<http://www.spec.org/cpu2006/flags/IBM-platform-linux64-revA.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml>
<http://www.spec.org/cpu2006/flags/IBM-platform-linux64-revA.xml>



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