



# SPEC® CINT2006 Result

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## Fujitsu

SPECint®\_rate2006 = 157

PRIMERGY RX100 S7, Intel Xeon E3-1230, 3.20 GHz

SPECint\_rate\_base2006 = 150

CPU2006 license: 19

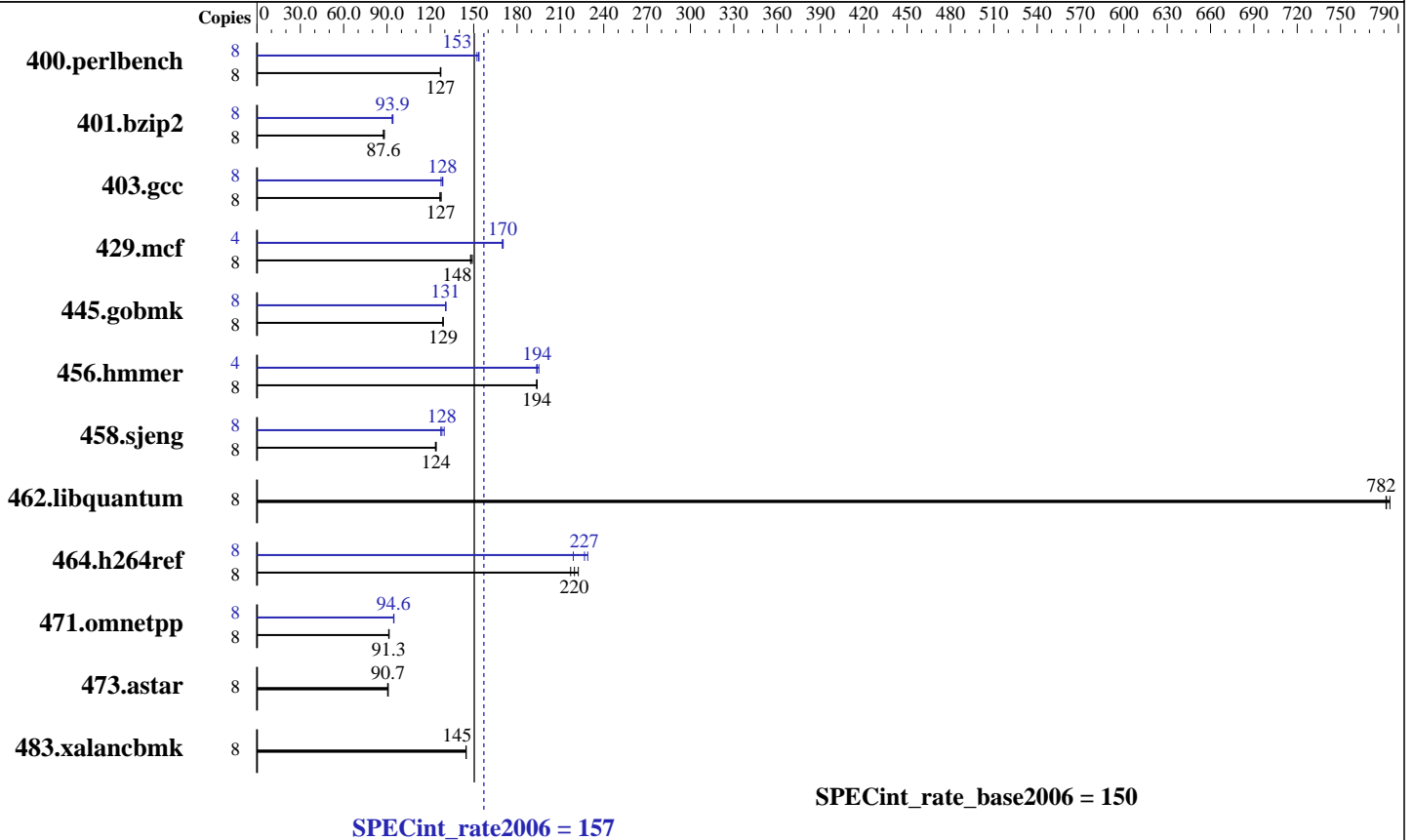
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Apr-2011

Hardware Availability: Jun-2011

Software Availability: Jan-2011



### Hardware

CPU Name: Intel Xeon E3-1230  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.60 GHz  
 CPU MHz: 3200  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 8 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 8 GB (2 x 4 GB 2Rx8 PC3-10600E-9, ECC)  
 Disk Subsystem: 1 x SATA, 300 GB, 7200 RPM  
 Other Hardware: --

### Software

Operating System: SUSE Linux Enterprise Server 11 (x86\_64) with SP1, Kernel 2.6.32.12-0.7-default  
 Compiler: Intel C++ Compiler XE for applications running on IA-32, Version 12.0.1.116 Build 20101116  
 Auto Parallel: No  
 File System: ext3  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V9.01



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## Results Table

| Benchmark      | Base   |            |            |            |             |            | Peak        |        |            |             |            |            |            |             |
|----------------|--------|------------|------------|------------|-------------|------------|-------------|--------|------------|-------------|------------|------------|------------|-------------|
|                | Copies | Seconds    | Ratio      | Seconds    | Ratio       | Seconds    | Ratio       | Copies | Seconds    | Ratio       | Seconds    | Ratio      | Seconds    | Ratio       |
| 400.perlbench  | 8      | <b>615</b> | <b>127</b> | 616        | 127         | 614        | 127         | 8      | 509        | 154         | 513        | 152        | <b>509</b> | <b>153</b>  |
| 401.bzip2      | 8      | 875        | 88.2       | 882        | 87.5        | <b>881</b> | <b>87.6</b> | 8      | 820        | 94.1        | 825        | 93.5       | <b>822</b> | <b>93.9</b> |
| 403.gcc        | 8      | 506        | 127        | <b>506</b> | <b>127</b>  | 509        | 127         | 8      | 506        | 127         | <b>501</b> | <b>128</b> | 501        | 129         |
| 429.mcf        | 8      | 494        | 148        | <b>492</b> | <b>148</b>  | 490        | 149         | 4      | 214        | 170         | 215        | 170        | <b>214</b> | <b>170</b>  |
| 445.gobmk      | 8      | <b>652</b> | <b>129</b> | 652        | 129         | 651        | 129         | 8      | <b>643</b> | <b>131</b>  | 643        | 130        | 641        | 131         |
| 456.hammer     | 8      | 386        | 193        | 385        | 194         | <b>385</b> | <b>194</b>  | 4      | 191        | 195         | 193        | 194        | <b>192</b> | <b>194</b>  |
| 458.sjeng      | 8      | 780        | 124        | 784        | 123         | <b>782</b> | <b>124</b>  | 8      | 761        | 127         | 747        | 130        | <b>756</b> | <b>128</b>  |
| 462.libquantum | 8      | 212        | 781        | <b>212</b> | <b>782</b>  | 211        | 784         | 8      | 212        | 781         | <b>212</b> | <b>782</b> | 211        | 784         |
| 464.h264ref    | 8      | 796        | 222        | 816        | 217         | <b>806</b> | <b>220</b>  | 8      | 809        | 219         | 773        | 229        | <b>781</b> | <b>227</b>  |
| 471.omnetpp    | 8      | 549        | 91.1       | <b>548</b> | <b>91.3</b> | 547        | 91.5        | 8      | <b>528</b> | <b>94.6</b> | 527        | 94.8       | 529        | 94.6        |
| 473.astar      | 8      | 621        | 90.4       | 619        | 90.8        | <b>619</b> | <b>90.7</b> | 8      | 621        | 90.4        | 619        | 90.8       | <b>619</b> | <b>90.7</b> |
| 483.xalancbmk  | 8      | <b>381</b> | <b>145</b> | 382        | 145         | 381        | 145         | 8      | <b>381</b> | <b>145</b>  | 382        | 145        | 381        | 145         |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run  
Large pages were not enabled for this run

## General Notes

For information about Fujitsu please visit: <http://www.fujitsu.com>  
Binaries were compiled on RHEL5.5 with binutils-2.17.50.0.6-14.el5

## Base Compiler Invocation

C benchmarks:  
icc -m32  
  
C++ benchmarks:  
icpc -m32



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## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xAVX -ipo -O3 -no-prec-div -opt-prefetch  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/smartheap -lsmartheap  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:

icpc -m32

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64

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## Peak Portability Flags (Continued)

456.hmmcr: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2)  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

401.bzips2: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch  
-auto-ilp32 -ansi-alias  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

403.gcc: -xAVX -ipo -O3 -no-prec-div  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

429.mcf: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias  
-auto-ilp32

445.gobmk: -xAVX(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
-ansi-alias -auto-ilp32

456.hmmcr: -xAVX -ipo -O3 -no-prec-div -unroll2 -auto-ilp32  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

458.sjeng: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4  
-auto-ilp32  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

462.libquantum: basepeak = yes

464.h264ref: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-ansi-alias

C++ benchmarks:

471.omnetpp: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias  
-opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/smartheap -lsmartheap

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## Peak Optimization Flags (Continued)

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.20110316.html>

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform-Intel-Linux64.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.20110316.xml>

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform-Intel-Linux64.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.1.

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