



# SPEC<sup>®</sup> CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Acer Incorporated

SPECint<sup>®</sup>\_rate2006 = 232

## Acer AT350 F1 (Intel Xeon E5620)

SPECint\_rate\_base2006 = 220

CPU2006 license: 97

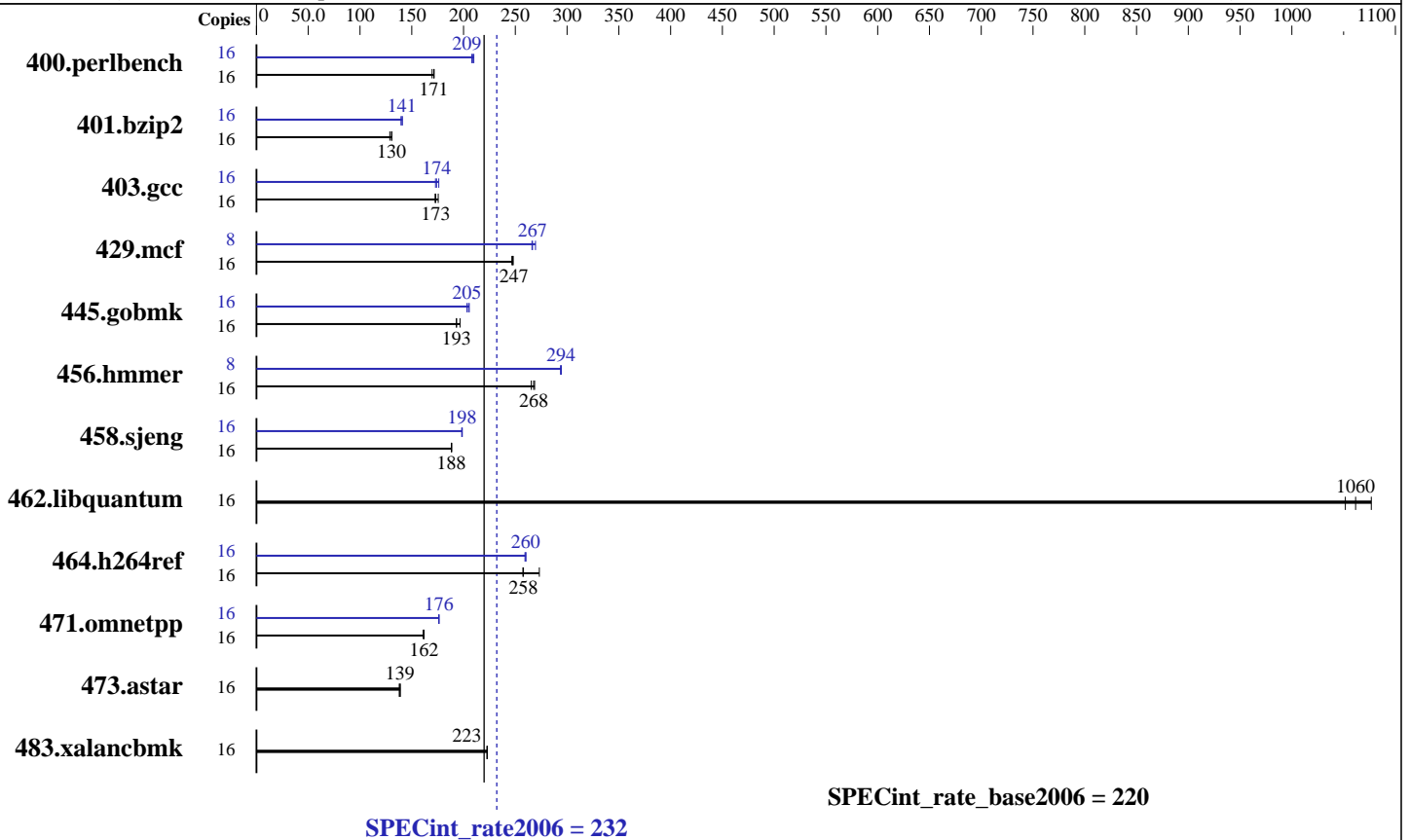
Test sponsor: Acer Incorporated

Tested by: Acer Incorporated

Test date: Apr-2011

Hardware Availability: Feb-2011

Software Availability: Jan-2011



### Hardware

CPU Name: Intel Xeon E5620  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.66 GHz  
 CPU MHz: 2400  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 12 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 48 GB (12 x 4 GB 2Rx8 PC3-1066R-9 ECC, running at 1066 MHz)  
 Disk Subsystem: 1 x 500 GB SATA, 7200RPM  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 11 (x86\_64) SP1  
 Kernel 2.6.32.12-0.7-default  
 Compiler: Intel C++ Compiler XE for applications running on IA-32  
 Version 12.0.1.116 Build 20101116  
 Auto Parallel: No  
 File System: ext3  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V9.01



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Acer Incorporated

SPECint\_rate2006 = 232

Acer AT350 F1 (Intel Xeon E5620)

SPECint\_rate\_base2006 = 220

CPU2006 license: 97  
Test sponsor: Acer Incorporated  
Tested by: Acer Incorporated

Test date: Apr-2011  
Hardware Availability: Feb-2011  
Software Availability: Jan-2011

## Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	911	172	922	170	<u>912</u>	<u>171</u>	16	745	210	<u>748</u>	<u>209</u>	752	208
401.bzip2	16	1199	129	<u>1183</u>	<u>130</u>	1181	131	16	1106	140	1096	141	<u>1098</u>	<u>141</u>
403.gcc	16	734	176	747	173	<u>744</u>	<u>173</u>	16	731	176	745	173	<u>740</u>	<u>174</u>
429.mcf	16	592	247	<u>590</u>	<u>247</u>	588	248	8	<u>274</u>	<u>267</u>	274	266	271	270
445.gobmk	16	854	197	869	193	<u>869</u>	<u>193</u>	16	817	206	<u>820</u>	<u>205</u>	826	203
456.hammer	16	<u>558</u>	<u>268</u>	556	269	562	265	8	254	294	<u>254</u>	<u>294</u>	254	294
458.sjeng	16	<u>1028</u>	<u>188</u>	1026	189	1029	188	16	974	199	<u>976</u>	<u>198</u>	976	198
462.libquantum	16	308	1080	<u>312</u>	<u>1060</u>	315	1050	16	308	1080	<u>312</u>	<u>1060</u>	315	1050
464.h264ref	16	1296	273	1376	257	<u>1373</u>	<u>258</u>	16	<u>1362</u>	<u>260</u>	1361	260	1365	259
471.omnetpp	16	<u>618</u>	<u>162</u>	618	162	621	161	16	568	176	<u>567</u>	<u>176</u>	567	176
473.astar	16	808	139	<u>811</u>	<u>139</u>	815	138	16	808	139	<u>811</u>	<u>139</u>	815	138
483.xalancbmk	16	495	223	<u>495</u>	<u>223</u>	496	223	16	495	223	<u>495</u>	<u>223</u>	496	223

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

## Operating System Notes

'ulimit -s unlimited' was used to set environment stack size  
Large pages were disabled for this run

## Platform Notes

BIOS settings:  
Fan speed = full speed (Default = Balanced)  
Data Reuse = Disabled (Default = Enabled)

## General Notes

Binaries compiled on RHEL5.5  
This result was measured on Gateway GT350 F1  
Acer AT350 F1 is electronically equivalent

## Base Compiler Invocation

C benchmarks:  
icc -m32

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Acer Incorporated

SPECint\_rate2006 = 232

Acer AT350 F1 (Intel Xeon E5620)

SPECint\_rate\_base2006 = 220

CPU2006 license: 97

Test sponsor: Acer Incorporated

Tested by: Acer Incorporated

Test date: Apr-2011

Hardware Availability: Feb-2011

Software Availability: Jan-2011

## Base Compiler Invocation (Continued)

C++ benchmarks:  
icpc -m32

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

C++ benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/smartheap -lsmartheap  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:  
icpc -m32



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Acer Incorporated

SPECint\_rate2006 = 232

Acer AT350 F1 (Intel Xeon E5620)

SPECint\_rate\_base2006 = 220

CPU2006 license: 97

Test sponsor: Acer Incorporated

Tested by: Acer Incorporated

Test date: Apr-2011

Hardware Availability: Feb-2011

Software Availability: Jan-2011

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
 401.bzip2: -DSPEC\_CPU\_LP64  
 456.hmmer: -DSPEC\_CPU\_LP64  
 458.sjeng: -DSPEC\_CPU\_LP64  
 462.libquantum: -DSPEC\_CPU\_LINUX  
 483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -opt-prefetch -auto-ilp32 -ansi-alias  
 -B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div  
 -B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -ansi-alias -auto-ilp32

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
 -ansi-alias -auto-ilp32

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32  
 -B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -unroll4 -auto-ilp32  
 -B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Acer Incorporated

SPECint\_rate2006 = 232

Acer AT350 F1 (Intel Xeon E5620)

SPECint\_rate\_base2006 = 220

CPU2006 license: 97

Test sponsor: Acer Incorporated

Tested by: Acer Incorporated

Test date: Apr-2011

Hardware Availability: Feb-2011

Software Availability: Jan-2011

## Peak Optimization Flags (Continued)

471.omnetpp (continued):  
-L/smartheap -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Acer-Intel-Linux-Settings-flags.html>

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Acer-Intel-Linux-Settings-flags.xml>

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.1.

Report generated on Wed Jul 23 18:34:48 2014 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 7 June 2011.

Standard Performance Evaluation Corporation

[info@spec.org](mailto:info@spec.org)

<http://www.spec.org/>