



# SPEC® CINT2006 Result

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## Cisco Systems

**SPECint®\_rate2006 = 409**

Cisco UCS B230 M1 (Intel Xeon X7560, 2.27 GHz)

**SPECint\_rate\_base2006 = 390**

CPU2006 license: 9019

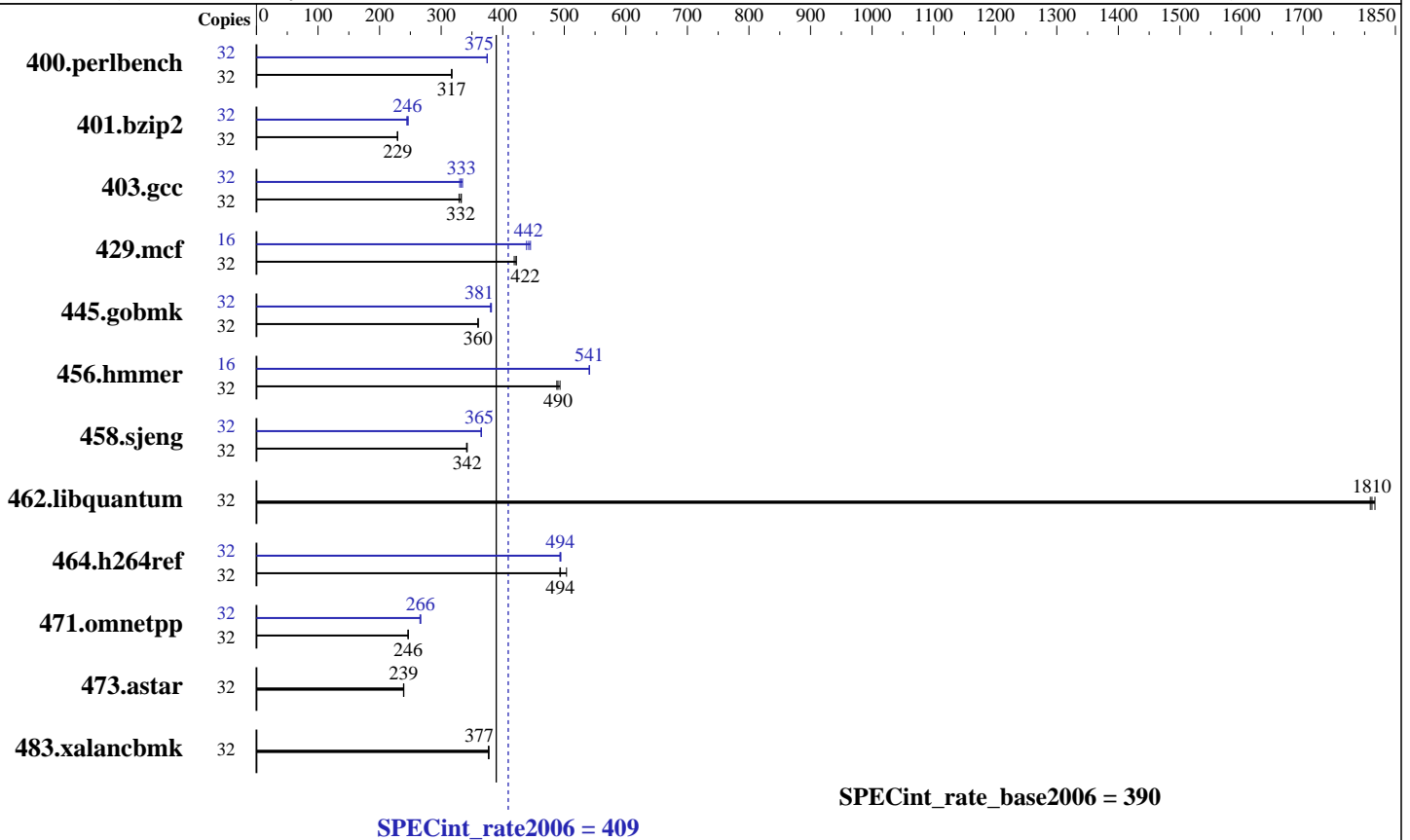
Test date: Mar-2011

Test sponsor: Cisco Systems

Hardware Availability: Mar-2011

Tested by: Cisco Systems

Software Availability: Jan-2011



**SPECint\_rate\_base2006 = 390**

### Hardware

CPU Name: Intel Xeon X7560  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.67 GHz  
 CPU MHz: 2266  
 FPU: Integrated  
 CPU(s) enabled: 16 cores, 2 chips, 8 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 24 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 128 GB (32 x 4 GB 2Rx4 PC3-10600R-9, ECC)  
 Disk Subsystem: 64 GB SSD, SATA Gen2, 3Gb/s  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 11 (x86\_64) with SP1, Kernel 2.6.32.12-0.7-default  
 Compiler: Intel C++ Compiler XE for applications running on IA-32, Version 12.0.1.116 Build 20101116  
 Auto Parallel: No  
 File System: ext3  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V9.01, Binaries compiled on RHEL5.5 with binutils-2.17.50.0.6-14.el5



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	32	985	317	<b>986</b>	<b>317</b>	986	317	32	834	375	834	375	<b>834</b>	<b>375</b>
401.bzip2	32	1347	229	<b>1347</b>	<b>229</b>	1349	229	32	1264	244	<b>1257</b>	<b>246</b>	1253	246
403.gcc	32	774	333	783	329	<b>776</b>	<b>332</b>	32	780	330	<b>774</b>	<b>333</b>	770	335
429.mcf	32	692	422	697	419	<b>692</b>	<b>422</b>	16	333	439	328	445	<b>330</b>	<b>442</b>
445.gobmk	32	<b>933</b>	<b>360</b>	932	360	933	360	32	879	382	882	380	<b>882</b>	<b>381</b>
456.hammer	32	612	488	605	493	<b>610</b>	<b>490</b>	16	<b>276</b>	<b>541</b>	276	541	276	541
458.sjeng	32	1135	341	<b>1132</b>	<b>342</b>	1132	342	32	<b>1060</b>	<b>365</b>	1061	365	1060	365
462.libquantum	32	365	1820	366	1810	<b>366</b>	<b>1810</b>	32	365	1820	366	1810	<b>366</b>	<b>1810</b>
464.h264ref	32	1437	493	1406	504	<b>1434</b>	<b>494</b>	32	1432	494	<b>1434</b>	<b>494</b>	1436	493
471.omnetpp	32	811	247	812	246	<b>812</b>	<b>246</b>	32	<b>751</b>	<b>266</b>	753	266	750	267
473.astar	32	<b>940</b>	<b>239</b>	938	239	941	239	32	<b>940</b>	<b>239</b>	938	239	941	239
483.xalancbmk	32	585	378	<b>585</b>	<b>377</b>	586	377	32	585	378	<b>585</b>	<b>377</b>	586	377

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

## Operating System Notes

ulimit -s unlimited was used to set the stacksize to unlimited prior to run  
Large pages were not enabled for this run

## Platform Notes

BIOS Configuration : Data Reuse Optimization = Disabled

## Base Compiler Invocation

C benchmarks:  
icc -m32

C++ benchmarks:  
icpc -m32



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## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/smartheap -lsmartheap  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:

icpc -m32

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64

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## Peak Portability Flags (Continued)

456.hmmr: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -auto-ilp32

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
-ansi-alias -auto-ilp32

456.hmmr: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/smartheap -lsmartheap

473.astar: basepeak = yes

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## Peak Optimization Flags (Continued)

483.xalanbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html>

<http://www.spec.org/cpu2006/flags/Cisco-BIOS-Platform-Settings.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml>

<http://www.spec.org/cpu2006/flags/Cisco-BIOS-Platform-Settings.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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