



# SPEC® CINT2006 Result

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## Fujitsu

**SPECint®2006 = 39.5**

PRIMERGY TX300 S6, Intel Xeon X5647, 2.93 GHz

**SPECint\_base2006 = 37.4**

CPU2006 license: 19

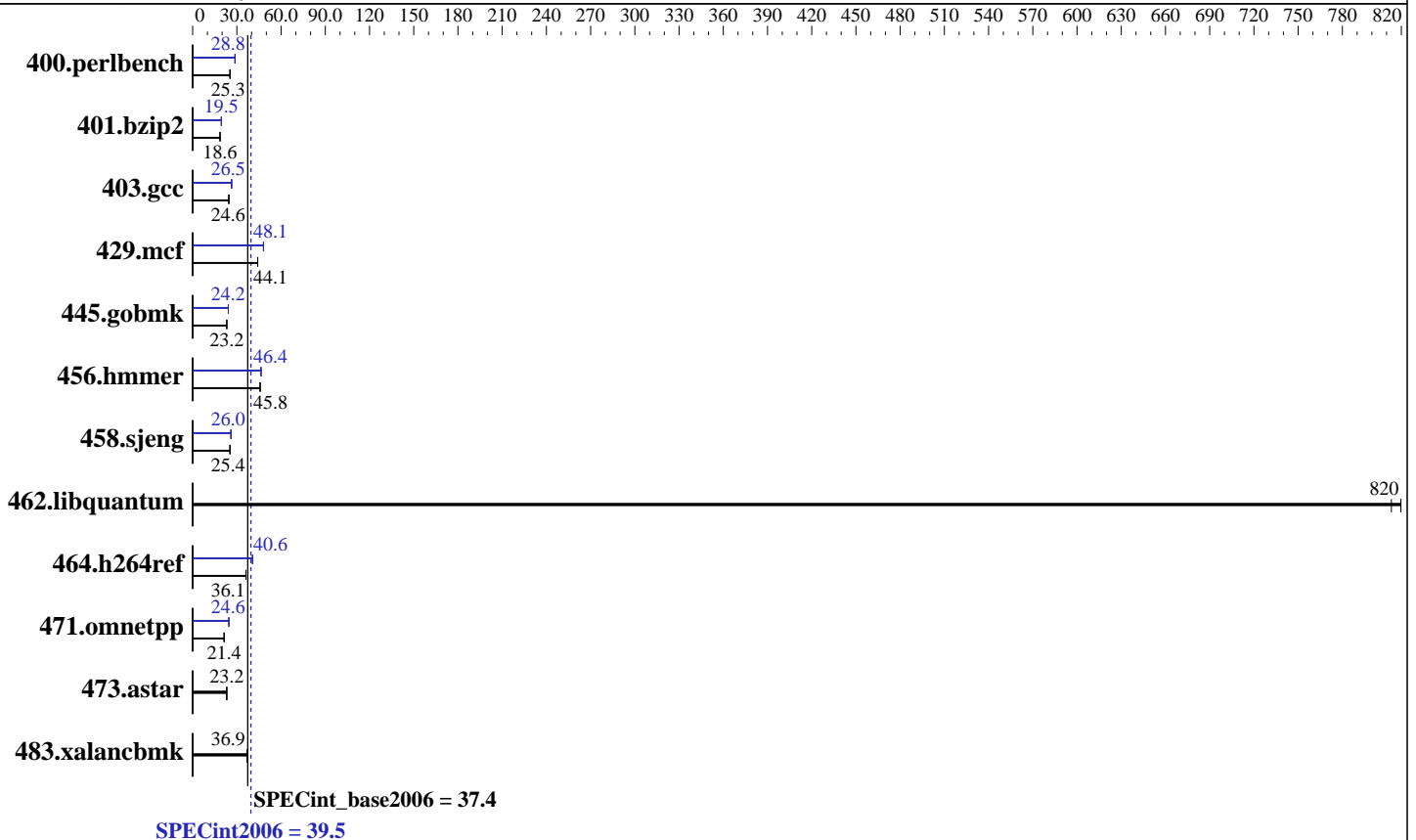
Test date: Mar-2011

Test sponsor: Fujitsu

Hardware Availability: Feb-2011

Tested by: Fujitsu

Software Availability: Jan-2011



### Hardware

CPU Name: Intel Xeon X5647  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.20 GHz  
 CPU MHz: 2933  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 12 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 48 GB (12 x 4 GB 2Rx4 PC3-10600R-9, ECC)  
 Disk Subsystem: 1 x SAS, 300 GB, 10000 RPM  
 Other Hardware: --

### Software

Operating System: SUSE Linux Enterprise Server 11 (x86\_64) with SP1, Kernel 2.6.32.12-0.7-default  
 Compiler: Intel C++ Intel 64 Compiler XE for applications running on Intel 64 Version 12.0.1.116 Build 20101116  
 Auto Parallel: Yes  
 File System: ext3  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V9.01



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	<b>386</b>	<b>25.3</b>	385	25.4	386	25.3	339	28.8	340	28.7	<b>340</b>	<b>28.8</b>
401.bzip2	<b>519</b>	<b>18.6</b>	519	18.6	519	18.6	495	19.5	495	19.5	<b>495</b>	<b>19.5</b>
403.gcc	<b>328</b>	<b>24.6</b>	327	24.6	329	24.5	304	26.5	304	26.5	<b>304</b>	<b>26.5</b>
429.mcf	207	44.1	<b>207</b>	<b>44.1</b>	207	44.2	<b>190</b>	<b>48.1</b>	190	48.0	189	48.1
445.gobmk	453	23.2	453	23.2	<b>453</b>	<b>23.2</b>	<b>433</b>	<b>24.2</b>	433	24.2	433	24.2
456.hammer	<b>204</b>	<b>45.8</b>	204	45.8	204	45.7	201	46.4	<b>201</b>	<b>46.4</b>	201	46.4
458.sjeng	476	25.4	480	25.2	<b>476</b>	<b>25.4</b>	<b>466</b>	<b>26.0</b>	466	26.0	466	26.0
462.libquantum	<b>25.3</b>	<b>820</b>	25.5	813	25.3	820	<b>25.3</b>	<b>820</b>	25.5	813	25.3	820
464.h264ref	612	36.2	<b>612</b>	<b>36.1</b>	612	36.1	544	40.7	545	40.6	<b>545</b>	<b>40.6</b>
471.omnetpp	292	21.4	293	21.3	<b>292</b>	<b>21.4</b>	255	24.5	<b>254</b>	<b>24.6</b>	254	24.6
473.astar	<b>303</b>	<b>23.2</b>	304	23.1	303	23.2	<b>303</b>	<b>23.2</b>	304	23.1	303	23.2
483.xalancbmk	185	37.3	<b>187</b>	<b>36.9</b>	187	36.8	185	37.3	<b>187</b>	<b>36.9</b>	187	36.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'nODEV /mnt/hugepages hugetlbfs defaults 0 0' added to /etc/fstab
echo 900 > /proc/sys/vm/nr_hugepages
export HUGETLB_MORECORE=yes
export LD_PRELOAD=/usr/lib64/libhugetlbfs.so
```

## Platform Notes

BIOS configuration:  
Data Reuse Optimization = Disable  
Intel HT Technology = Disable

## General Notes

OMP\_NUM\_THREADS set to number of cores  
This result was measured on the PRIMERGY TX300 S6. The PRIMERGY TX300 S6 and the PRIMERGY RX300 S6 are electronically equivalent.  
For information about Fujitsu please visit: <http://www.fujitsu.com>  
Binaries were compiled on RHEL5.5 with binutils-2.17.50.0.6-14.el5

Submitted\_by: "Klassen, Peter" <peter.klassen@ts.fujitsu.com>  
Submitted: Mon Mar 28 12:08:25 EDT 2011  
Submission: cpu2006-20110328-15219.sub



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## Base Compiler Invocation

C benchmarks:  
icc -m64  
  
C++ benchmarks:  
icpc -m64

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64  
403.gcc: -DSPEC\_CPU\_LP64  
429.mcf: -DSPEC\_CPU\_LP64  
445.gobmk: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
464.h264ref: -DSPEC\_CPU\_LP64  
471.omnetpp: -DSPEC\_CPU\_LP64  
473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT  
  
C++ benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/smartheap -lsmartheap64  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc -m64

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## Peak Compiler Invocation (Continued)

400.perlbench: `icc -m32`

429.mcf: `icc -m32`

445.gobmk: `icc -m32`

464.h264ref: `icc -m32`

C++ benchmarks (except as noted below):  
`icpc -m64`

471.omnetpp: `icpc -m32`

## Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`  
401.bzip2: `-DSPEC_CPU_LP64`  
403.gcc: `-DSPEC_CPU_LP64`  
456.hmmer: `-DSPEC_CPU_LP64`  
458.sjeng: `-DSPEC_CPU_LP64`  
462.libquantum: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`  
473.astar: `-DSPEC_CPU_LP64`  
483.xalancbmk: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`

## Peak Optimization Flags

C benchmarks:

400.perlbench: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-prefetch -ansi-alias  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT`

401.bzip2: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32  
-opt-prefetch -ansi-alias`

403.gcc: `-xSSE4.2 -ipo -O3 -no-prec-div -inline-alloc  
-opt-malloc-options=3 -auto-ilp32  
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT`

429.mcf: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-auto-ilp32 -ansi-alias  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT`

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## Peak Optimization Flags (Continued)

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
-auto-ilp32 -ansi-alias  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32  
-ansi-alias  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll4

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll2 -ansi-alias  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

### C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-ra-region-strategy=block -ansi-alias -Wl,-z,muldefs  
-L/smartheap -lsmartheap  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

## Peak Other Flags

### C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.20110316.html>  
<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revA.20110316.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.20110316.xml>  
<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revA.20110316.xml>



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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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