SPEC® CFP2006 Result
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Acer Incorporated

$SPECfp®\_rate2006 = 260$

Gateway GT350 F1 (Intel Xeon X5675)

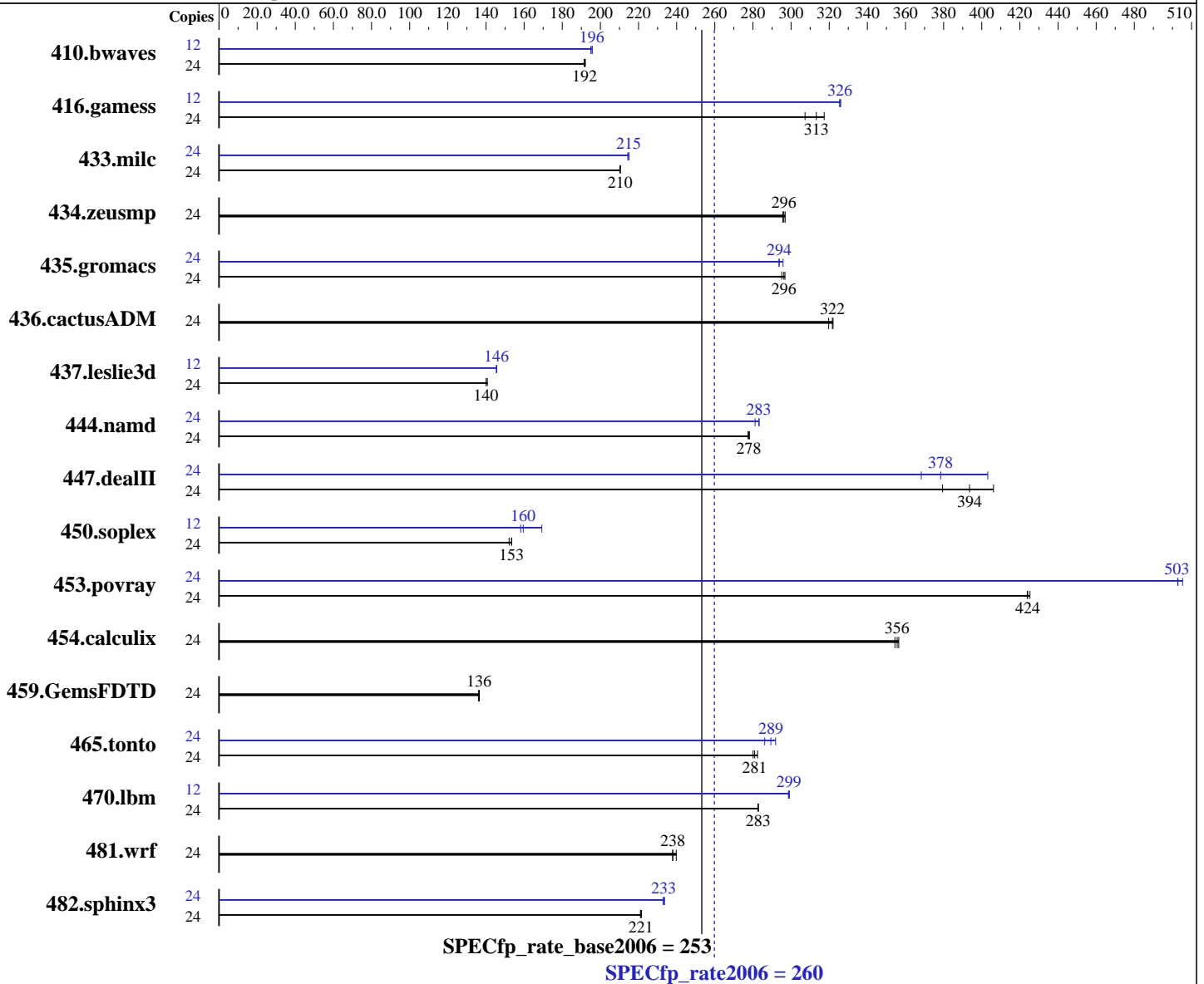$SPECfp\_rate\_base2006 = 253$

CPU2006 license: 97
Test sponsor: Acer Incorporated
Tested by: Acer Incorporated

Test date: Mar-2011
Hardware Availability: Feb-2011
Software Availability: Jan-2011

SPECfp_rate_base2006 = 253

SPECfp_rate2006 = 260

Benchmark	Copies	Result
410.bwaves	12	196
	24	192
416.gamess	12	326
	24	313
433.milc	24	215
	24	210
434.zeusmp	24	296
435.gromacs	24	294
	24	296
436.cactusADM	24	322
437.leslie3d	12	146
	24	140
444.namd	24	283
	24	278
447.dealII	24	378
	24	394
450.soplex	12	160
	24	153
453.povray	24	503
	24	424
454.calculix	24	356
459.GemsFDTD	24	136
465.tonto	24	289
	24	281
470.lbm	12	299
	24	283
481.wrf	24	238
482.sphinx3	24	233
	24	221

Hardware

CPU Name:	Intel Xeon X5675
CPU Characteristics:	Intel Turbo Boost Technology up to 3.46 GHz
CPU MHz:	3067
FPU:	Integrated
CPU(s) enabled:	12 cores, 2 chips, 6 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

Continued on next page

Software

Operating System:	SUSE Linux Enterprise Server 11 (x86_64) SP1, Kernel 2.6.32.12-0.7-default
Compiler:	Intel C++ and Fortran Intel 64 Compiler XE for applications running on Intel 64 Version 12.0.1.116 Build 20101116
Auto Parallel:	No
File System:	ext3
System State:	Run level 3 (multi-user)
Base Pointers:	64-bit

Continued on next page

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L3 Cache:	12 MB I+D on chip per chip
Other Cache:	None
Memory:	48 GB (12 x 4 GB 2Rx8 PC3-10600R-9,ECC)
Disk Subsystem:	1 x 300 GB SATA, 10000 RPM SATA HDD
Other Hardware:	None
Peak Pointers:	32/64-bit
Other Software:	None

Results Table

Benchmark	Base						Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	24	1698	192	**1701**	**192**	1704	191	12	833	196	**834**	**196**	837	195
416.gamess	24	1481	317	1529	307	**1500**	**313**	12	721	326	**722**	**326**	722	325
433.milc	24	**1047**	**210**	1047	210	1048	210	24	1028	214	**1026**	**215**	1025	215
434.zeusmp	24	739	296	**737**	**296**	736	297	24	739	296	**737**	**296**	736	297
435.gromacs	24	577	297	**578**	**296**	581	295	24	584	294	579	296	**583**	**294**
436.cactusADM	24	891	322	**892**	**322**	897	320	24	891	322	**892**	**322**	897	320
437.leslie3d	24	1611	140	1603	141	**1611**	**140**	12	775	146	**775**	**146**	775	145
444.namd	24	**693**	**278**	694	277	692	278	24	**680**	**283**	685	281	679	283
447.dealII	24	724	379	**698**	**394**	676	406	24	681	403	746	368	**725**	**378**
450.soplex	24	1316	152	1304	153	**1305**	**153**	12	633	158	**627**	**160**	591	169
453.povray	24	**301**	**424**	300	425	301	424	24	254	503	**254**	**503**	253	505
454.calculix	24	555	356	559	354	**557**	**356**	24	555	356	559	354	**557**	**356**
459.GemsFDTD	24	1872	136	1865	137	**1867**	**136**	24	1872	136	1865	137	**1867**	**136**
465.tonto	24	843	280	836	282	**841**	**281**	24	809	292	825	286	**816**	**289**
470.lbm	24	1166	283	**1166**	**283**	1166	283	12	551	299	**552**	**299**	552	299
481.wrf	24	1118	240	1127	238	**1126**	**238**	24	1118	240	1127	238	**1126**	**238**
482.sphinx3	24	2117	221	2111	222	**2114**	**221**	24	2009	233	**2005**	**233**	2002	234

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

    The config file option 'submit' was used.
     numactl was used to bind copies to the cores

Operating System Notes

    'ulimit -s unlimited' was used to set environment stack size
    Large pages were disabled for this run

Platform Notes

    BIOS Settings:
    Fan speed = full speed (Default = Balanced)
    Data Reuse = Disabled (Default = Enabled)

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General Notes

Binaries compiled on RHEL5.5
This result was measured on the Gateway GT350 F1.
The Acer AT350 F1 is electronically equivalent

Base Compiler Invocation

C benchmarks:
icc   -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc   -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias

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Base Optimization Flags (Continued)

Fortran benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -static
```

Benchmarks using both Fortran and C:
```
-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias
```

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc    -m64
```

    482.sphinx3: `icc -m32`

C++ benchmarks (except as noted below):
```
icpc   -m64
```

    450.soplex: `icpc -m32`

Fortran benchmarks:
```
ifort -m64
```

Benchmarks using both Fortran and C:
```
icc    -m64 ifort -m64
```

Peak Portability Flags

    410.bwaves: `-DSPEC_CPU_LP64`
    416.gamess: `-DSPEC_CPU_LP64`
    433.milc: `-DSPEC_CPU_LP64`
    434.zeusmp: `-DSPEC_CPU_LP64`
    435.gromacs: `-DSPEC_CPU_LP64 -nofor_main`
    436.cactusADM: `-DSPEC_CPU_LP64 -nofor_main`
    437.leslie3d: `-DSPEC_CPU_LP64`
    444.namd: `-DSPEC_CPU_LP64`
    447.dealII: `-DSPEC_CPU_LP64`
    453.povray: `-DSPEC_CPU_LP64`
    454.calculix: `-DSPEC_CPU_LP64 -nofor_main`
    459.GemsFDTD: `-DSPEC_CPU_LP64`
    465.tonto: `-DSPEC_CPU_LP64`
    470.lbm: `-DSPEC_CPU_LP64`
    481.wrf: `-DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX`

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Peak Optimization Flags

C benchmarks:

```
    433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
              -no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32

    470.lbm: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
             -no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3
             -ansi-alias -opt-prefetch -static -auto-ilp32

    482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2
```

C++ benchmarks:

```
    444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
              -no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
              -auto-ilp32

    447.dealII: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
                -no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32

    450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
                -no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3
                -B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

    453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
                -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias
                -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT
```

Fortran benchmarks:

```
    410.bwaves: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
                -no-prec-div(pass 2) -prof-use(pass 2) -static

    416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
                -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
                -inline-level=0 -scalar-rep- -static

    434.zeusmp: basepeak = yes

    437.leslie3d: -xSSE4.2 -ipo -O3 -no-prec-div
                  -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

    459.GemsFDTD: basepeak = yes

    465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
               -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto
               -inline-calloc -opt-malloc-options=3
               -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT
```

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Peak Optimization Flags (Continued)

Benchmarks using both Fortran and C:

435.gromacs: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2) `-O3`(pass 2)
`-no-prec-div`(pass 2) `-prof-use`(pass 2) `-opt-prefetch`
`-static -auto-ilp32`

436.cactusADM: `basepeak = yes`

454.calculix: `basepeak = yes`

481.wrf: `basepeak = yes`

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Acer-Intel-Linux-Settings-flags.html
http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Acer-Intel-Linux-Settings-flags.xml
http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Wed Jul 23 17:14:34 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 28 April 2011.