



# SPEC® CINT2006 Result

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## Acer Incorporated

SPECint®\_rate2006 = 566

## Acer AR585 F1 (AMD Opteron 6134)

SPECint\_rate\_base2006 = 488

CPU2006 license: 97

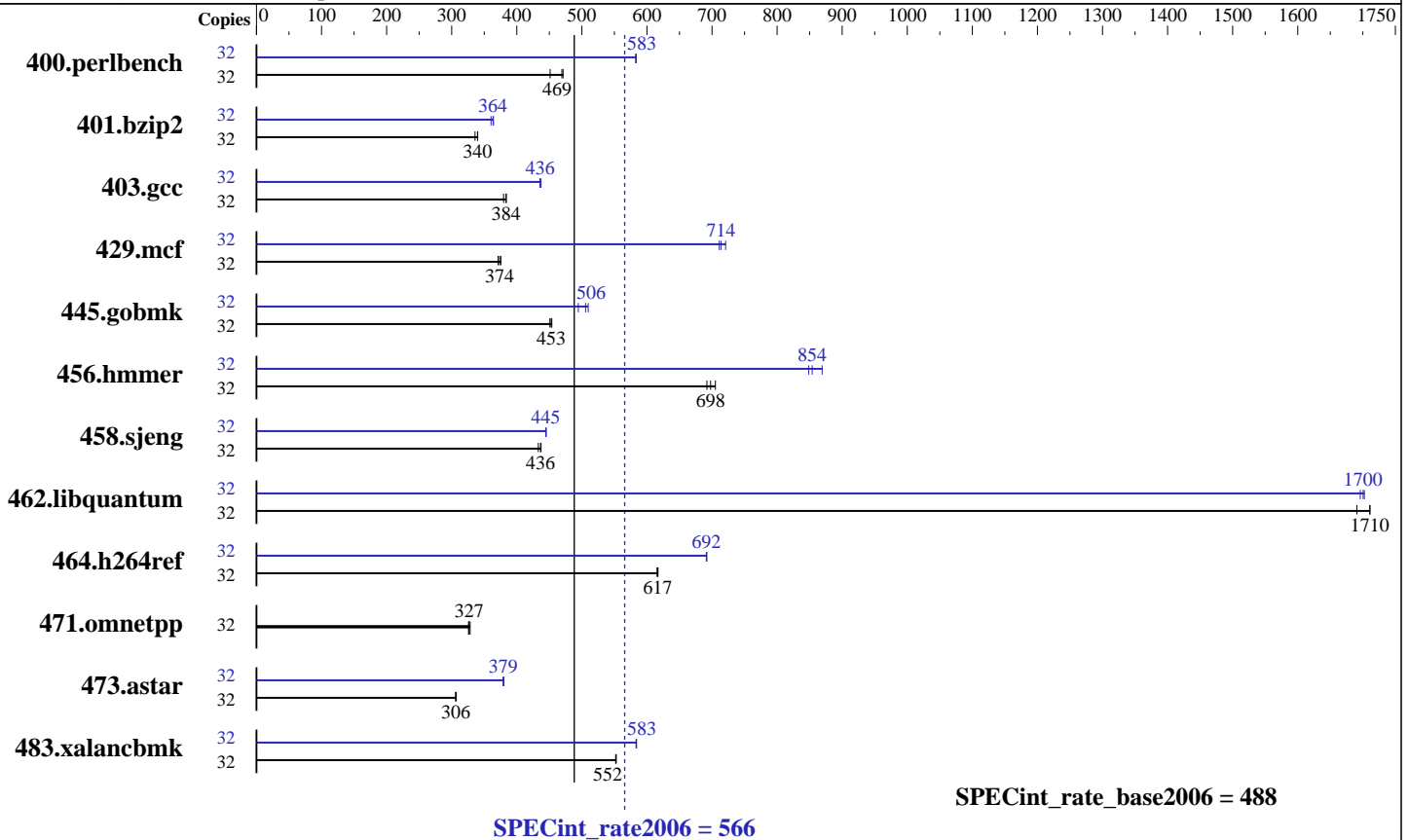
Test sponsor: Acer Incorporated

Tested by: Acer Incorporated

Test date: Mar-2011

Hardware Availability: Jul-2010

Software Availability: Jul-2010



### Hardware

CPU Name: AMD Opteron 6134  
 CPU Characteristics:  
 CPU MHz: 2300  
 FPU: Integrated  
 CPU(s) enabled: 32 cores, 4 chips, 8 cores/chip  
 CPU(s) orderable: 2,4 chips  
 Primary Cache: 64 KB I + 64 KB D on chip per core  
 Secondary Cache: 512 KB I+D on chip per core  
 L3 Cache: 12 MB I+D on chip per chip, 6 MB shared / 4 cores  
 Other Cache: None  
 Memory: 128 GB (32 x 4 GB 2Rx8 PC3-10600R-9, ECC)  
 Disk Subsystem: 1 x 500 GB SATA, 7200 RPM  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 11 SP1  
 Kernel 2.6.32.12-0.7-default  
 Compiler: x86 Open64 4.2.4 Compiler Suite (from AMD)  
 Auto Parallel: No  
 File System: ext3  
 System State: Run level 3 (multiuser)  
 Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: SmartHeap 8.1 32-bit Library for Linux



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	32	693	451	<b>666</b>	<b>469</b>	664	471	32	536	583	536	583	<b>536</b>	<b>583</b>
401.bzip2	32	919	336	908	340	<b>909</b>	<b>340</b>	32	857	360	<b>850</b>	<b>364</b>	848	364
403.gcc	32	<b>671</b>	<b>384</b>	678	380	671	384	32	<b>591</b>	<b>436</b>	589	437	591	436
429.mcf	32	786	371	<b>781</b>	<b>374</b>	777	376	32	410	711	405	721	<b>409</b>	<b>714</b>
445.gobmk	32	<b>741</b>	<b>453</b>	745	451	740	454	32	679	495	<b>664</b>	<b>506</b>	658	510
456.hammer	32	<b>428</b>	<b>698</b>	431	692	423	705	32	<b>350</b>	<b>854</b>	343	869	352	849
458.sjeng	32	894	433	<b>887</b>	<b>436</b>	885	437	32	870	445	<b>871</b>	<b>445</b>	871	444
462.libquantum	32	392	1690	388	1710	<b>388</b>	<b>1710</b>	32	389	1700	<b>390</b>	<b>1700</b>	391	1700
464.h264ref	32	1148	617	1151	615	<b>1149</b>	<b>617</b>	32	1024	691	<b>1024</b>	<b>692</b>	1023	692
471.omnetpp	32	<b>612</b>	<b>327</b>	609	328	614	326	32	<b>612</b>	<b>327</b>	609	328	614	326
473.astar	32	734	306	732	307	<b>733</b>	<b>306</b>	32	<b>592</b>	<b>379</b>	591	380	592	379
483.xalancbmk	32	399	553	400	552	<b>400</b>	<b>552</b>	32	378	583	378	584	<b>378</b>	<b>583</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
'numactl' was used to bind copies to the cores.  
See the configuration file for details.

## Operating System Notes

'ulimit -s unlimited' was used to set environment stack size  
'ulimit -l 2097152' was used to set environment locked pages in memory limit

Set vm/nr\_hugepages=28672 in /etc/sysctl.conf  
mount -t hugetlbfs nodev /mnt/hugepages

## General Notes

Environment variables set by runspec before the start of the run:  
HUGETLB\_LIMIT = "896"  
LD\_LIBRARY\_PATH = "/usr/cpu2006/amd1002-rate-libs-revC/64:/usr/cpu2006/amd1002-rate-libs-revC/32"

The x86 Open64 Compiler Suite is only available from (and supported by) AMD at <http://developer.amd.com/cpu/open64>  
Binaries were compiled on SLES10 SP2 with binutils 2.18  
This result was measured on the Gateway GR585 F1.  
The Acer AR585 F1 and Gateway GR585 F1 are electronically equivalent.



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## Base Compiler Invocation

C benchmarks:  
opencc

C++ benchmarks:  
openCC

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64  
403.gcc: -DSPEC\_CPU\_LP64  
429.mcf: -DSPEC\_CPU\_LP64  
445.gobmk: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
464.h264ref: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-march=barcelona -mso -Ofast -CG:local\_sched\_alg=1  
-INLINE:aggressive=on -IPA:plimit=8000 -IPA:small\_pu=100  
-HP:bdt=2m:heap=2m

C++ benchmarks:  
-march=barcelona -mso -Ofast -m32 -INLINE:aggressive=on  
-CG:cmp\_peep=on -L/root/work/libraries/SmartHeap-8.1/lib -lsmartheap

## Peak Compiler Invocation

C benchmarks:  
opencc

C++ benchmarks:  
openCC

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64

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## Peak Portability Flags (Continued)

```

401.bzip2: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

```

## Peak Optimization Flags

C benchmarks:

```

400.perlbench: -march=barcelona -mso -fb_create fbdata(pass 1)
               -fb_opt fbdata(pass 2) -Ofast -IPA:plimit=20000 -LNO:opt=0
               -OPT:unroll_times_max=8 -OPT:unroll_size=256
               -OPT:unroll_level=2 -OPT:keep_ext=on -WOPT:if_conv=0
               -CG:local_sched_alg=1 -CG:unroll_fb_req=on
               -HP:bdt=2m:heap=2m

401.bzip2: -march=barcelona -mso -fb_create fbdata(pass 1)
            -fb_opt fbdata(pass 2) -O3 -OPT:alias=disjoint
            -OPT:goto=off -CG:local_sched_alg=1 -HP:bdt=2m:heap=2m

403.gcc: -march=barcelona -mso -fb_create fbdata(pass 1)
          -fb_opt fbdata(pass 2) -Ofast -LNO:trip_count=256
          -LNO:prefetch Ahead=10 -CG:cmp_peep=on -m32
          -HP:bdt=2m:heap=2m -GRA:unspill=on -IPA:small_pu=200

429.mcf: -march=barcelona -mso -O3 -ipa -INLINE:aggressive=on
          -CG:gcm=off -GRA:prioritize_by_density=on -m32
          -HP:bdt=2m:heap=2m

445.gobmk: -march=barcelona -mso -fb_create fbdata(pass 1)
            -fb_opt fbdata(pass 2) -O3 -OPT:alias=restrict
            -OPT:unroll_times_max=8 -OPT:unroll_size=256
            -OPT:unroll_level=2 -OPT:keep_ext=on -ipa -IPA:plimit=750
            -IPA:min_hotness=300 -IPA:pu_reorder=1 -LNO:prefetch=1
            -LNO:ignore_feedback=off -CG:p2align=on
            -CG:unroll_fb_req=on -HP:bdt=2m:heap=2m

456.hmmer: -march=barcelona -mso -fb_create fbdata(pass 1)
            -fb_opt fbdata(pass 2) -Ofast -LNO:prefetch=0
            -OPT:alias=disjoint -OPT:unroll_times_max=8
            -OPT:unroll_size=256 -OPT:unroll_level=2 -OPT:keep_ext=on
            -CG:local_sched_alg=1 -CG:cflow=0
            -CG:push_pop_int_saved_regs=off -CG:cmp_peep=on
            -HP:bdt=2m:heap=2m

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## Peak Optimization Flags (Continued)

458.sjeng: -march=barcelona -mso -fb\_create fbdata(pass 1)  
 -fb\_opt fbdata(pass 2) -O3 -ipa -LNO:ignore\_feedback=off  
 -LNO:full\_unroll=10 -LNO:fusion=0 -LNO:fission=2  
 -IPA:pu\_reorder=2 -CG:ptr\_load\_use=0  
 -OPT:unroll\_times\_max=8 -INLINE:aggressive=on

462.libquantum: -march=barcelona -mso -Ofast -LNO:pf2=0 -CG:gcm=off  
 -CG:use\_prefetchnta=on -CG:cmp\_peep=on -WOPT:aggstr=0  
 -HP:bdt=2m:heap=2m -OPT:alias=disjoint  
 -INLINE:aggressive=on -IPA:space=1000 -IPA:plimit=20000

464.h264ref: -march=barcelona -mso -fb\_create fbdata(pass 1)  
 -fb\_opt fbdata(pass 2) -O3 -IPA:plimit=20000  
 -OPT:alias=disjoint -LNO:prefetch=0 -CG:ptr\_load\_use=0  
 -CG:push\_pop\_int\_saved\_regs=off

C++ benchmarks:

471.omnetpp: basepeak = yes

473.astar: -march=barcelona -mso -fb\_create fbdata(pass 1)  
 -fb\_opt fbdata(pass 2) -Ofast -TENV:frame\_pointer=off  
 -WOPT:if\_conv=0 -GRA:optimize\_boundary=on  
 -OPT:alias=disjoint -INLINE:aggressive=on  
 -IPA:small\_pu=3000 -IPA:plimit=3000 -m32  
 -HP:bdt=2m:heap=2m

483.xalancbmk: -march=barcelona -mso -Ofast -INLINE:aggressive=on -m32  
 -CG:cmp\_peep=on -GRA:unspill=on -TENV:frame\_pointer=off  
 -fno-emit-exceptions  
 -L/root/work/libraries/SmartHeap-8.1/lib -lsmarheap

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/x86-open64-424-flags-rate-revC.20100901.html>

<http://www.spec.org/cpu2006/flags/amd-platform-rate-revC.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/x86-open64-424-flags-rate-revC.20100901.xml>

<http://www.spec.org/cpu2006/flags/amd-platform-rate-revC.xml>



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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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