SPEC® CINT2006 Result
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Intel Corporation

Intel DH55PJ Motherboard (Intel Core i3-540)

$SPECint^®\_rate2006 =$ 61.2

$SPECint\_rate\_base2006 =$ 55.8

Copies		
400.perlbench	4	61.6
	4	46.8
401.bzip2	4	33.2
	4	31.6
403.gcc	4	50.0
	4	49.2
429.mcf	2	57.4
	4	60.4
445.gobmk	4	62.0
	4	54.8
456.hmmer	4	58.8
	4	41.6
458.sjeng	4	56.8
	4	52.0
462.libquantum	4	327
	4	313
464.h264ref	4	79.6
	4	76.0
471.omnetpp	4	40.0
	4	36.4
473.astar	4	39.2
	4	36.8
483.xalancbmk	4	55.2

SPECint_rate_base2006 = 55.8

SPECint_rate2006 = 61.2

Hardware

CPU Name:	Intel Core i3-540
CPU Characteristics:	
CPU MHz:	3066
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip, 2 threads/core
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	4 MB I+D on chip per chip
Other Cache:	None
Memory:	4 GB (2 x 2 GB 2Rx8 PC3-10600U-9)
Disk Subsystem:	Seagate 1 TB SATA, 7200 RPM
Other Hardware:	None

Software

Operating System:	Windows Vista Ultimate w/ SP1 (64-bit)
Compiler:	Intel C++ Compiler XE for IA32 and Intel 64 Version 12.0.0.104 Build 20101006 Microsoft Visual Studio 2008 Professional SP1 (for libraries)
Auto Parallel:	No
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	SmartHeap Library Version 9.01 from http://www.microquill.com/

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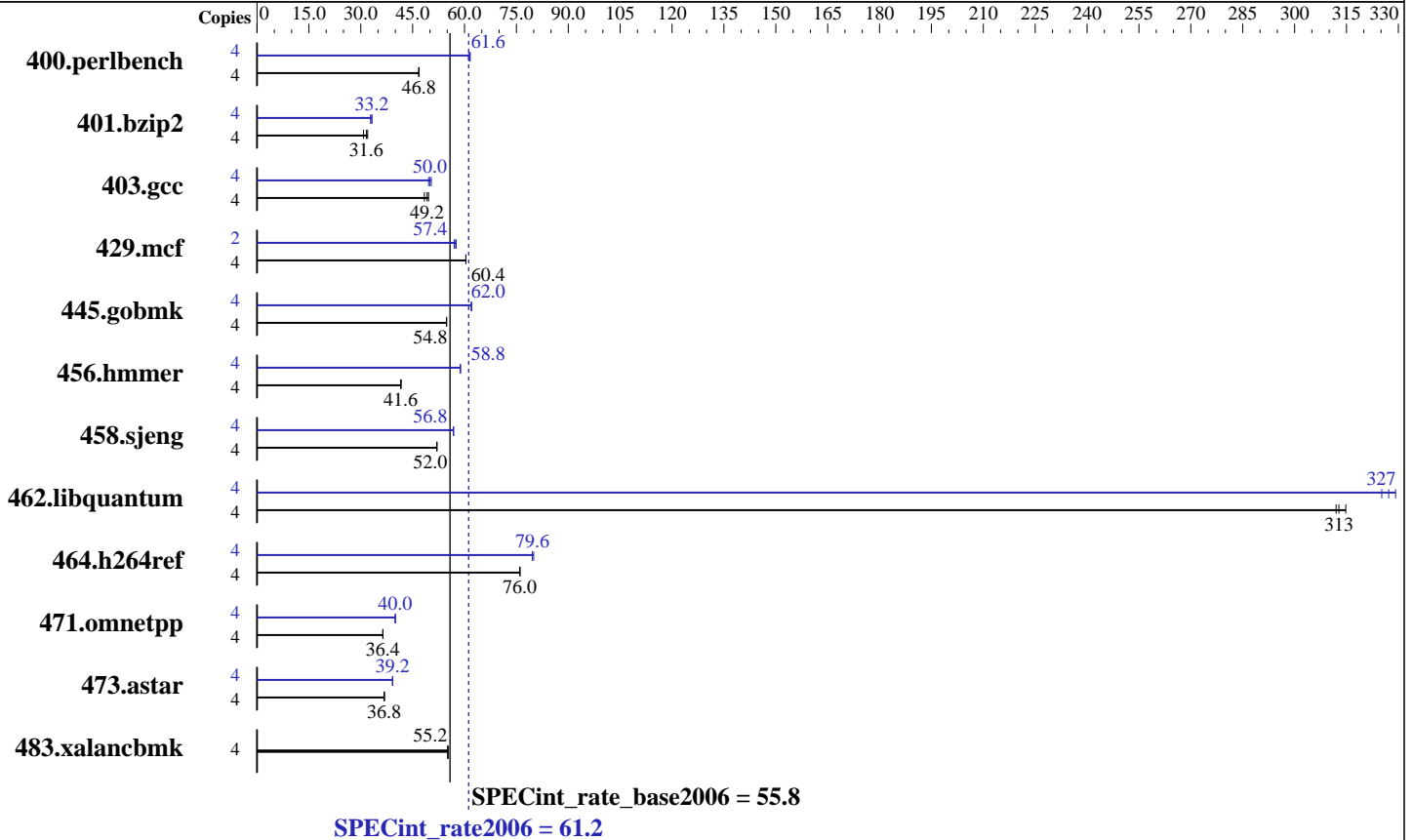SPECint_rate_base2006 = 55.8

CPU2006 license: 13
Test sponsor: Intel Corporation
Tested by: Intel Corporation

Test date: Feb-2011
Hardware Availability: Jan-2010
Software Availability: Oct-2010

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	834	46.8	**834**	**46.8**	838	46.8	4	**634**	**61.6**	637	61.2	633	61.6
401.bzip2	4	1252	30.8	**1217**	**31.6**	1210	32.0	4	1177	32.8	1166	33.2	**1168**	**33.2**
403.gcc	4	666	48.4	**656**	**49.2**	650	49.6	4	647	49.6	641	50.4	**643**	**50.0**
429.mcf	4	**602**	**60.4**	605	60.4	602	60.4	2	**318**	**57.4**	317	57.6	320	57.0
445.gobmk	4	765	54.8	**765**	**54.8**	765	54.8	4	**678**	**62.0**	678	62.0	679	62.0
456.hmmer	4	898	41.6	898	41.6	**898**	**41.6**	4	**636**	**58.8**	636	58.8	636	58.8
458.sjeng	4	928	52.0	929	52.0	**928**	**52.0**	4	853	56.8	**853**	**56.8**	854	56.8
462.libquantum	4	**265**	**313**	266	312	263	315	4	252	329	**253**	**327**	255	325
464.h264ref	4	**1163**	**76.0**	1164	76.0	1163	76.0	4	1109	79.6	1109	80.0	**1109**	**79.6**
471.omnetpp	4	**688**	**36.4**	687	36.4	688	36.4	4	627	40.0	**627**	**40.0**	628	40.0
473.astar	4	764	36.8	765	36.8	**764**	**36.8**	4	**714**	**39.2**	714	39.2	713	39.2
483.xalancbmk	4	**499**	**55.2**	498	55.2	499	55.2	4	**499**	**55.2**	498	55.2	499	55.2

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

    The config file option 'submit' was used.
    The start command with the /affinity switch was used to bind processes to cores

General Notes

     Tested systems can be used with Shin-G ATX case,
     PC Power and Cooling 1200W power supply

Base Compiler Invocation

C benchmarks:
     icl -Qvc9 -Qstd=c99

C++ benchmarks:
     icl -Qvc9

Base Portability Flags

        403.gcc: -DSPEC_CPU_WIN32
     464.h264ref: -DWIN32 -DSPEC_CPU_NO_INTTYPES
     483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword

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Base Optimization Flags

C benchmarks:
```
-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qopt-prefetch /F512000000
```

C++ benchmarks:
```
-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qcxx-features
/F512000000 shlW32M.lib          -link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icl -Qvc9 -Qstd=c99
```

```
  456.hmmer: C:/Program Files (x86)/Intel/ComposerXE-2011/bin/intel64/icl.exe
             -link -LIBPATH:C:/Program Files (x86)/Intel/ComposerXE-2011/compiler/lib/intel
             -link -LIBPATH:C:/Program Files (x86)/Microsoft Visual Studio 9.0/VC/lib/AMD64
             -link -LIBPATH:C:/Program Files (x86)/Microsoft Visual Studio 9.0/VC/lib
             -link -LIBPATH:C:/Program Files (x86)/Microsoft SDKs/Windows/v7.0A/lib/x64

   458.sjeng: C:/Program Files (x86)/Intel/ComposerXE-2011/bin/intel64/icl.exe
             -link -LIBPATH:C:/Program Files (x86)/Intel/ComposerXE-2011/compiler/lib/intel
             -link -LIBPATH:C:/Program Files (x86)/Microsoft Visual Studio 9.0/VC/lib/AMD64
             -link -LIBPATH:C:/Program Files (x86)/Microsoft Visual Studio 9.0/VC/lib
             -link -LIBPATH:C:/Program Files (x86)/Microsoft SDKs/Windows/v7.0A/lib/x64

462.libquantum: C:/Program Files (x86)/Intel/ComposerXE-2011/bin/intel64/icl.exe
             -Qstd=c99
             -link -LIBPATH:C:/Program Files (x86)/Intel/ComposerXE-2011/compiler/lib/intel
             -link -LIBPATH:C:/Program Files (x86)/Microsoft Visual Studio 9.0/VC/lib/AMD64
             -link -LIBPATH:C:/Program Files (x86)/Microsoft Visual Studio 9.0/VC/lib
             -link -LIBPATH:C:/Program Files (x86)/Microsoft SDKs/Windows/v7.0A/lib/x64
```

C++ benchmarks (except as noted below):
```
icl -Qvc9
```

```
   473.astar: C:/Program Files (x86)/Intel/ComposerXE-2011/bin/intel64/icl.exe
             -link -LIBPATH:C:/Program Files (x86)/Intel/ComposerXE-2011/compiler/lib/intel
             -link -LIBPATH:C:/Program Files (x86)/Microsoft Visual Studio 9.0/VC/lib/AMD64
             -link -LIBPATH:C:/Program Files (x86)/Microsoft Visual Studio 9.0/VC/lib
             -link -LIBPATH:C:/Program Files (x86)/Microsoft SDKs/Windows/v7.0A/lib/x64
```

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Test date: Feb-2011
Hardware Availability: Jan-2010
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Peak Portability Flags

```
       403.gcc: -DSPEC_CPU_WIN32
     456.hmmer: -DSPEC_CPU_P64
     458.sjeng: -DSPEC_CPU_P64
 462.libquantum: -DSPEC_CPU_P64
    464.h264ref: -DWIN32 -DSPEC_CPU_NO_INTTYPES
     473.astar: -DSPEC_CPU_P64
 483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword
```

Peak Optimization Flags

C benchmarks:

```
  400.perlbench: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
                 -Qipo -O3 -Qprec-div- -Qansi-alias -Qopt-prefetch
                 /F512000000 shlW32M.lib          -link /FORCE:MULTIPLE

      401.bzip2: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
                 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qansi-alias
                 /F512000000

       403.gcc: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
                 -Qipo -O3 -Qprec-div- -Qopt-prefetch /F512000000

       429.mcf: -QxSSE4.2 -Qipo -O3 -Qprec-div- -Qopt-prefetch
                 /F512000000

     445.gobmk: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
                 -Qipo -O2 -Qprec-div- -Qansi-alias /F512000000

     456.hmmer: -Qauto-ilp32 -QxSSE4.2(pass 2) -Qprof_gen(pass 1)
                 -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qopt-prefetch
                 /F512000000

     458.sjeng: -Qauto-ilp32 -QxSSE4.2(pass 2) -Qprof_gen(pass 1)
                 -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll4
                 /F512000000

 462.libquantum: -Qauto-ilp32 -QxSSE4.2 -Qipo -O3 -Qprec-div-
                 -Qopt-prefetch /F512000000

    464.h264ref: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
                 -Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias /F512000000
```

C++ benchmarks:

```
    471.omnetpp: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
                 -Qipo -O3 -Qprec-div- -Qansi-alias
                 -Qopt-ra-region-strategy=block /F512000000 shlW32M.lib
                           -link /FORCE:MULTIPLE
```

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Tested by:	Intel Corporation	**Software Availability:**	Oct-2010

Peak Optimization Flags (Continued)

```
473.astar: -Qauto-ilp32 -QxSSE4.2 -Qipo -O3 -Qprec-div-
           -Qopt-prefetch /F512000000 shlW64M.lib
                       -link /FORCE:MULTIPLE

483.xalancbmk: basepeak = yes
```

Peak Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12-win32-revA.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic12-win32-revA.xml

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For other inquiries, please contact webmaster@spec.org.

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