



SPEC[®] CFP2006 Result

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Fujitsu

SPECfp[®]_rate2006 = 137

PRIMERGY BX924 S2, Intel Xeon X5675, 3.07 GHz

SPECfp_rate_base2006 = 133

CPU2006 license: 19

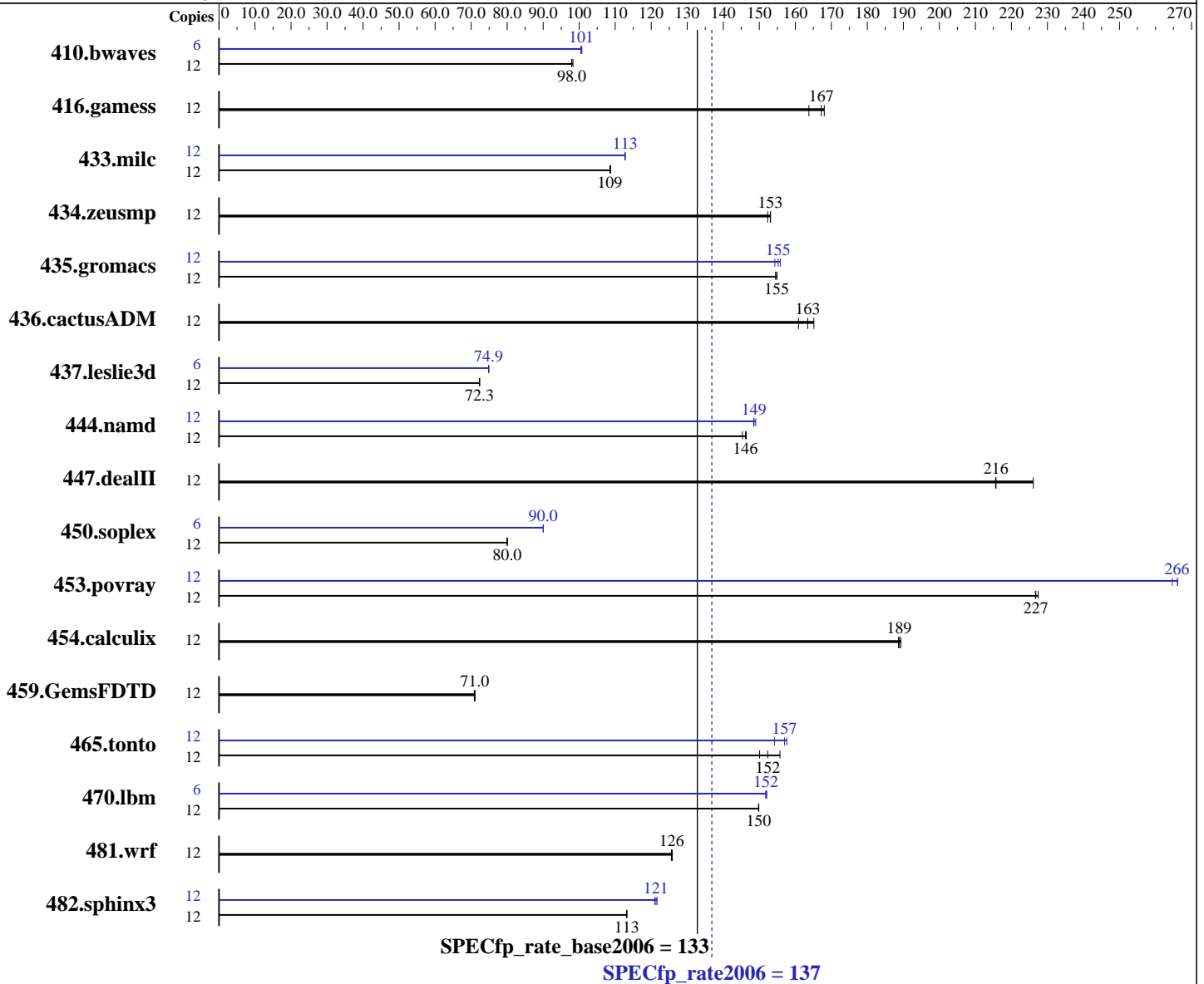
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Jan-2011

Hardware Availability: Feb-2011

Software Availability: Jan-2011



Hardware

CPU Name: Intel Xeon X5675
 CPU Characteristics: Intel Turbo Boost Technology up to 3.47 GHz
 CPU MHz: 3067
 FPU: Integrated
 CPU(s) enabled: 6 cores, 1 chip, 6 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64) with SP1, Kernel 2.6.32.12-0.7-default
 Compiler: Intel C++ and Fortran Intel 64 Compiler XE for applications running on Intel 64, Version 12.0.1.116 Build 20101116
 Auto Parallel: No
 File System: ext3
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit

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L3 Cache: 12 MB I+D on chip per chip
Other Cache: None
Memory: 48 GB (6 x 8 GB 2Rx4 PC3-10600R-9, ECC)
Disk Subsystem: 1 x SAS, 300 GB, 10000 RPM
Other Hardware: --

Peak Pointers: 32/64-bit
Other Software: None

Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|---------------|--------|-------------|------------|-------------|-------------|-------------|-------------|--------|------------|------------|-------------|-------------|-------------|-------------|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 410.bwaves | 12 | 1660 | 98.3 | 1665 | 98.0 | 1666 | 97.9 | 6 | 811 | 101 | 809 | 101 | 812 | 100 |
| 416.gamess | 12 | 1398 | 168 | 1435 | 164 | 1405 | 167 | 12 | 1398 | 168 | 1435 | 164 | 1405 | 167 |
| 433.milc | 12 | 1013 | 109 | 1015 | 109 | 1015 | 109 | 12 | 977 | 113 | 977 | 113 | 977 | 113 |
| 434.zeusmp | 12 | 713 | 153 | 717 | 152 | 713 | 153 | 12 | 713 | 153 | 717 | 152 | 713 | 153 |
| 435.gromacs | 12 | 553 | 155 | 555 | 155 | 553 | 155 | 12 | 555 | 154 | 550 | 156 | 552 | 155 |
| 436.cactusADM | 12 | 891 | 161 | 868 | 165 | 877 | 163 | 12 | 891 | 161 | 868 | 165 | 877 | 163 |
| 437.leslie3d | 12 | 1560 | 72.3 | 1558 | 72.4 | 1560 | 72.3 | 6 | 753 | 74.9 | 753 | 74.9 | 753 | 74.9 |
| 444.namd | 12 | 662 | 145 | 658 | 146 | 657 | 146 | 12 | 648 | 148 | 648 | 149 | 646 | 149 |
| 447.dealII | 12 | 637 | 216 | 607 | 226 | 636 | 216 | 12 | 637 | 216 | 607 | 226 | 636 | 216 |
| 450.soplex | 12 | 1250 | 80.1 | 1251 | 80.0 | 1251 | 80.0 | 6 | 556 | 90.0 | 556 | 90.0 | 556 | 90.0 |
| 453.povray | 12 | 282 | 227 | 281 | 227 | 282 | 227 | 12 | 240 | 266 | 241 | 265 | 240 | 266 |
| 454.calculix | 12 | 523 | 189 | 525 | 189 | 524 | 189 | 12 | 523 | 189 | 525 | 189 | 524 | 189 |
| 459.GemsFDTD | 12 | 1796 | 70.9 | 1792 | 71.1 | 1792 | 71.0 | 12 | 1796 | 70.9 | 1792 | 71.1 | 1792 | 71.0 |
| 465.tonto | 12 | 787 | 150 | 758 | 156 | 775 | 152 | 12 | 749 | 158 | 752 | 157 | 766 | 154 |
| 470.lbm | 12 | 1100 | 150 | 1100 | 150 | 1101 | 150 | 6 | 542 | 152 | 543 | 152 | 543 | 152 |
| 481.wrf | 12 | 1065 | 126 | 1067 | 126 | 1066 | 126 | 12 | 1065 | 126 | 1067 | 126 | 1066 | 126 |
| 482.sphinx3 | 12 | 2065 | 113 | 2067 | 113 | 2065 | 113 | 12 | 1923 | 122 | 1928 | 121 | 1933 | 121 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
Hugepages were not configured on the system

Platform Notes

BIOS configuration:
Data Reuse Optimization = Disable
Performance/Power Setting = Traditional



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General Notes

For information about Fujitsu please visit: <http://www.fujitsu.com>
Binaries were compiled on RHEL5.5 with binutils-2.17.50.0.6-14.el5

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias

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Base Optimization Flags (Continued)

Fortran benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static

Benchmarks using both Fortran and C:

-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
 416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
 434.zeusmp: -DSPEC_CPU_LP64
 435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
 437.leslie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
 447.dealII: -DSPEC_CPU_LP64
 453.povray: -DSPEC_CPU_LP64
 454.calculix: -DSPEC_CPU_LP64 -nofor_main
 459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX



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Peak Optimization Flags

C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32

470.lbm: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3
-ansi-alias -opt-prefetch -static -auto-ilp32

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

Fortran benchmarks:

410.bwaves: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -static

416.gamess: basepeak = yes

434.zeusmp: basepeak = yes

437.leslie3d: -xSSE4.2 -ipo -O3 -no-prec-div
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

459.GemsFDTD: basepeak = yes

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto
-inline-calloc -opt-malloc-options=3
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch
-static -auto-ilp32

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Peak Optimization Flags (Continued)

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revA.20110222.00.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revA.20110222.00.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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