



SPEC® CINT2006 Result

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Supermicro

SPECint®_rate2006 = 131

Motherboard X8SIE-F (Intel Xeon X3480, 3.06 GHz)

SPECint_rate_base2006 = 124

CPU2006 license: 001176

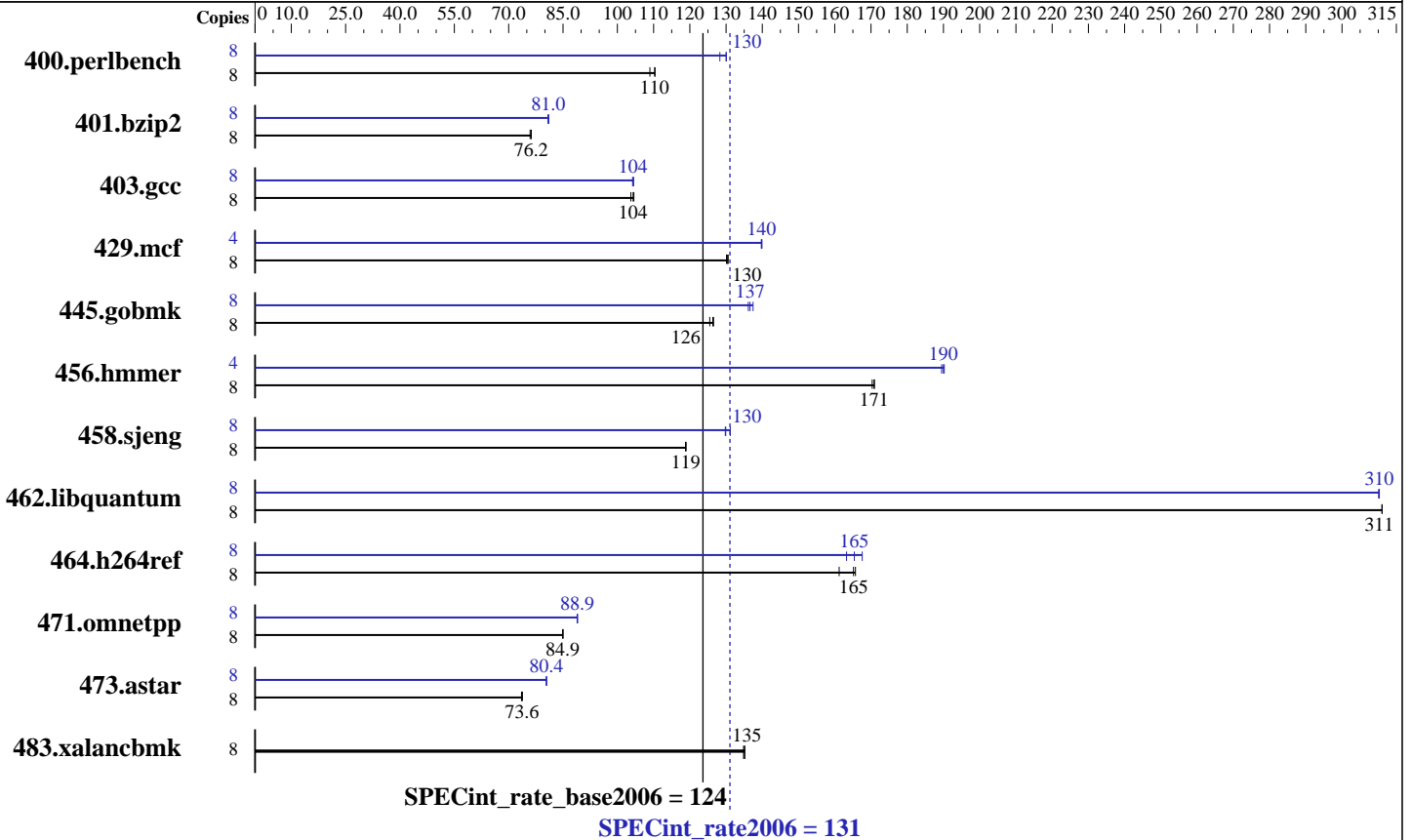
Test date: Oct-2010

Test sponsor: Supermicro

Hardware Availability: Jun-2010

Tested by: Supermicro

Software Availability: Jan-2010



Hardware

CPU Name: Intel Xeon X3480
 CPU Characteristics: Intel Turbo Boost Technology up to 3.73 GHz
 CPU MHz: 3067
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 8 MB I+D on chip per chip
 Other Cache: None
 Memory: 16 GB (4 x 4 GB 2Rx8 PC3-10600R-9, ECC)
 Disk Subsystem: 1 x 160 GB SATA II, 7200 RPM
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64)
 Kernel 2.6.27.19-5-default
 Compiler: Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1
 Build 20091130 Package ID: l_cproc_p_11.1.064
 Auto Parallel: No
 File System: ext3
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1



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Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|----------------|--------|-------------|-------------|------------|-------------|-------------|-------------|--------|-------------|-------------|------------|-------------|------------|------------|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 8 | 709 | 110 | 708 | 110 | 717 | 109 | 8 | 601 | 130 | 609 | 128 | 601 | 130 |
| 401.bzip2 | 8 | 1014 | 76.2 | 1013 | 76.2 | 1017 | 75.9 | 8 | 953 | 81.0 | 955 | 80.8 | 953 | 81.0 |
| 403.gcc | 8 | 616 | 105 | 617 | 104 | 621 | 104 | 8 | 616 | 104 | 618 | 104 | 617 | 104 |
| 429.mcf | 8 | 561 | 130 | 560 | 130 | 559 | 131 | 4 | 261 | 140 | 261 | 140 | 261 | 140 |
| 445.gobmk | 8 | 663 | 127 | 664 | 126 | 669 | 126 | 8 | 611 | 137 | 614 | 137 | 616 | 136 |
| 456.hammer | 8 | 437 | 171 | 438 | 170 | 437 | 171 | 4 | 196 | 190 | 196 | 190 | 197 | 189 |
| 458.sjeng | 8 | 815 | 119 | 814 | 119 | 814 | 119 | 8 | 746 | 130 | 738 | 131 | 745 | 130 |
| 462.libquantum | 8 | 533 | 311 | 533 | 311 | 533 | 311 | 8 | 534 | 310 | 535 | 310 | 534 | 310 |
| 464.h264ref | 8 | 1068 | 166 | 1098 | 161 | 1072 | 165 | 8 | 1070 | 165 | 1056 | 168 | 1084 | 163 |
| 471.omnetpp | 8 | 588 | 85.0 | 589 | 84.9 | 589 | 84.9 | 8 | 562 | 88.9 | 562 | 88.9 | 562 | 89.0 |
| 473.astar | 8 | 761 | 73.8 | 763 | 73.6 | 764 | 73.5 | 8 | 698 | 80.5 | 698 | 80.4 | 699 | 80.4 |
| 483.xalancbmk | 8 | 409 | 135 | 409 | 135 | 408 | 135 | 8 | 409 | 135 | 409 | 135 | 408 | 135 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes

'ulimit -s unlimited' was used to set the stack size to unlimited prior to run

Platform Notes

Fan speed set to Full Speed in BIOS Setup.
As tested, the system used a Supermicro CSE-813MTQ-280C chassis.
The chassis is bundled with a PWS-281-1H power supply, SNK-P0046P heatsink,
and 4 FAN-0061L4 cooling fans.

General Notes

Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502

Base Compiler Invocation

C benchmarks:
icc -m32

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Base Compiler Invocation (Continued)

C++ benchmarks:
icpc -m32

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m32

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

462.libquantum: icc -m64

C++ benchmarks (except as noted below):
icpc -m32

473.astar: icpc -m64



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Peak Portability Flags

```

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

```

Peak Optimization Flags

C benchmarks:

```

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
               -prof-use(pass 2) -ansi-alias
401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
            -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
            -prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32
403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static
429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch
445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
            -ipo -no-prec-div -ansi-alias
456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
            -ansi-alias -auto-ilp32
458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
            -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
            -prof-use(pass 2) -unroll4 -auto-ilp32
462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32
                -opt-prefetch
464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
              -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
              -prof-use(pass 2) -unroll2 -ansi-alias

```

C++ benchmarks:

```

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
              -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
              -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
              -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

```

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Peak Optimization Flags (Continued)

```
473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
           -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
           -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs
           -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64
```

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100316.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100316.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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