



# SPEC® CINT2006 Result

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## Hewlett-Packard Company

### SPECint®\_rate2006 = 338

ProLiant DL380 G7  
(2.66 GHz, Intel Xeon X5650)

### SPECint\_rate\_base2006 = 317

CPU2006 license: 3

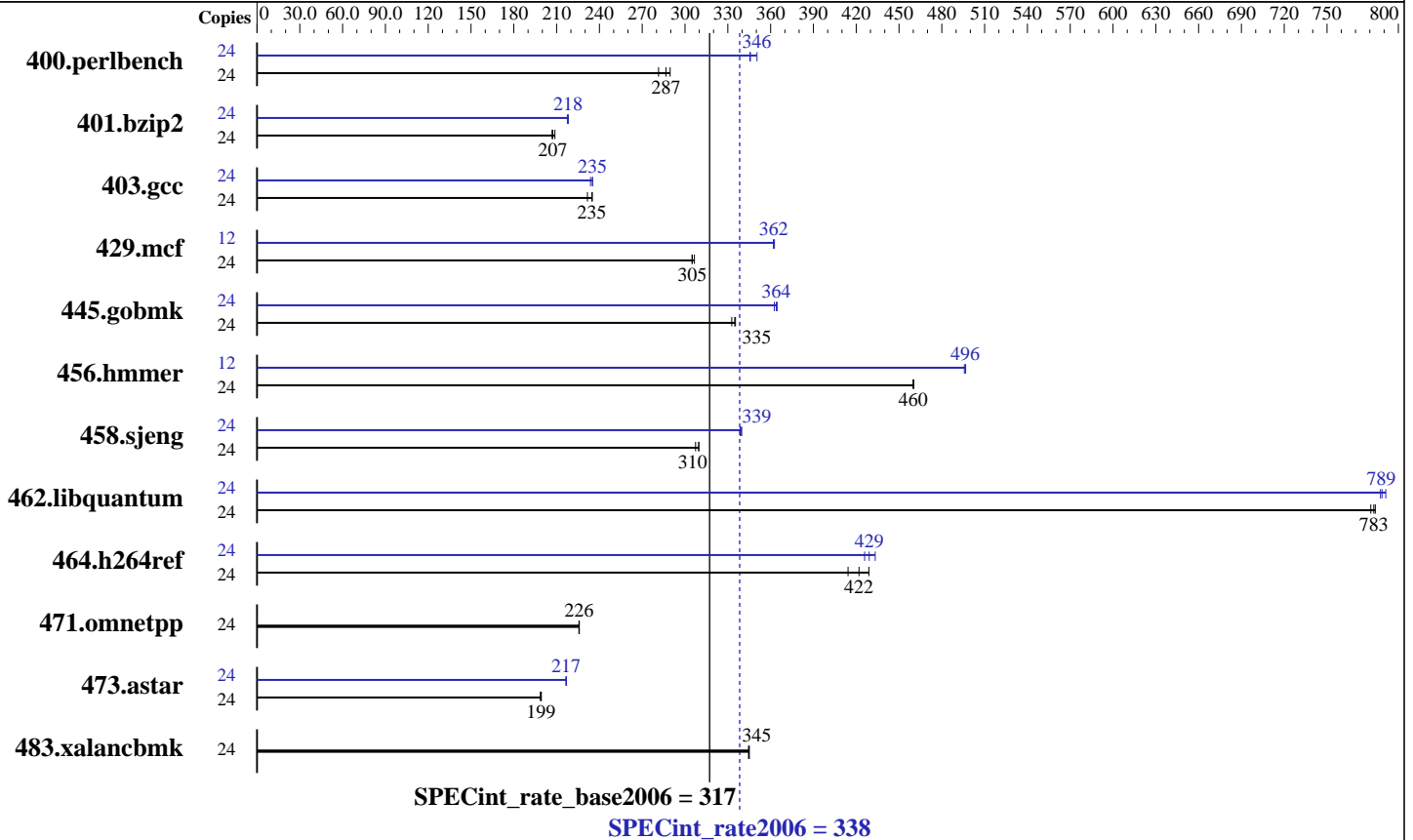
Test date: Jul-2010

Test sponsor: Hewlett-Packard Company

Hardware Availability: Jun-2010

Tested by: Hewlett-Packard Company

Software Availability: Apr-2010



### Hardware

CPU Name: Intel Xeon X5650  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.06 GHz  
 CPU MHz: 2667  
 FPU: Integrated  
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 12 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 48 GB (12 x 4 GB 2Rx4 PC3-10600R-9, ECC)  
 Disk Subsystem: 1x146 GB 10 K SAS  
 Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server release 5.4  
 Kernel 2.6.18-164.el5  
 Compiler: Intel C++ Compiler 11.1 for Linux  
 Build 20100414 Package ID: l\_cprof\_p\_11.1.072  
 Auto Parallel: No  
 File System: ext3  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1  
 Binutils 2.17.50.0.18



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### Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	24	810	290	833	281	<b>818</b>	<b>287</b>	24	<b>678</b>	<b>346</b>	669	350	679	345
401.bzip2	24	<b>1117</b>	<b>207</b>	1120	207	1109	209	24	1062	218	1065	218	<b>1063</b>	<b>218</b>
403.gcc	24	822	235	<b>823</b>	<b>235</b>	834	232	24	821	235	<b>822</b>	<b>235</b>	826	234
429.mcf	24	<b>717</b>	<b>305</b>	714	307	718	305	12	<b>302</b>	<b>362</b>	302	363	302	362
445.gobmk	24	751	335	<b>752</b>	<b>335</b>	757	333	24	694	363	<b>692</b>	<b>364</b>	691	365
456.hammer	24	486	460	<b>487</b>	<b>460</b>	487	460	12	225	497	<b>226</b>	<b>496</b>	226	496
458.sjeng	24	938	310	<b>938</b>	<b>310</b>	945	307	24	855	340	<b>856</b>	<b>339</b>	858	339
462.libquantum	24	637	781	<b>635</b>	<b>783</b>	634	784	24	632	787	<b>631</b>	<b>789</b>	629	791
464.h264ref	24	<b>1259</b>	<b>422</b>	1282	414	1238	429	24	1226	433	1247	426	<b>1238</b>	<b>429</b>
471.omnetpp	24	<b>664</b>	<b>226</b>	665	226	664	226	24	<b>664</b>	<b>226</b>	665	226	664	226
473.astar	24	848	199	845	199	<b>846</b>	<b>199</b>	24	<b>778</b>	<b>217</b>	777	217	778	217
483.xalancbmk	24	<b>480</b>	<b>345</b>	480	345	481	345	24	<b>480</b>	<b>345</b>	480	345	481	345

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

### Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

### Platform Notes

BIOS configuration:  
HP Power Profile set to Maximum Performance  
Thermal Configuration set to Increased Cooling  
Data Reuse set to Disabled

### Base Compiler Invocation

C benchmarks:  
icc  
  
C++ benchmarks:  
icpc



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## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc  
-opt-malloc-options=3 -opt-prefetch

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/cpu2006/SmartHeap\_8.1/lib -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/Compiler/11.1/056/bin/intel64/icc

456.hmmer: /opt/intel/Compiler/11.1/056/bin/intel64/icc

458.sjeng: /opt/intel/Compiler/11.1/056/bin/intel64/icc

C++ benchmarks:

icpc

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX

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## Peak Portability Flags (Continued)

483.xalanbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -ansi-alias -opt-prefetch

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc  
-opt-malloc-options=3

429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2  
-ipo -no-prec-div -ansi-alias

456.hmmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2  
-ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static  
-opt-malloc-options=3 -opt-prefetch

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: basepeak = yes

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs  
-L/cpu2006/SmartHeap\_8.1/lib -lsmartheap

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## Peak Optimization Flags (Continued)

483.xalanbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revF.html>

<http://www.spec.org/cpu2006/flags/HP-Intel-Linux-Settings-flags.20100525.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revF.xml>

<http://www.spec.org/cpu2006/flags/HP-Intel-Linux-Settings-flags.20100525.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.1.

Report generated on Wed Jul 23 14:57:30 2014 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 17 November 2010.