



# SPEC® CINT2006 Result

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## IBM Corporation

**SPECint®2006 = 36.8**

### IBM System x3630 M3 (Intel Xeon X5650)

**SPECint\_base2006 = 34.2**

CPU2006 license: 11

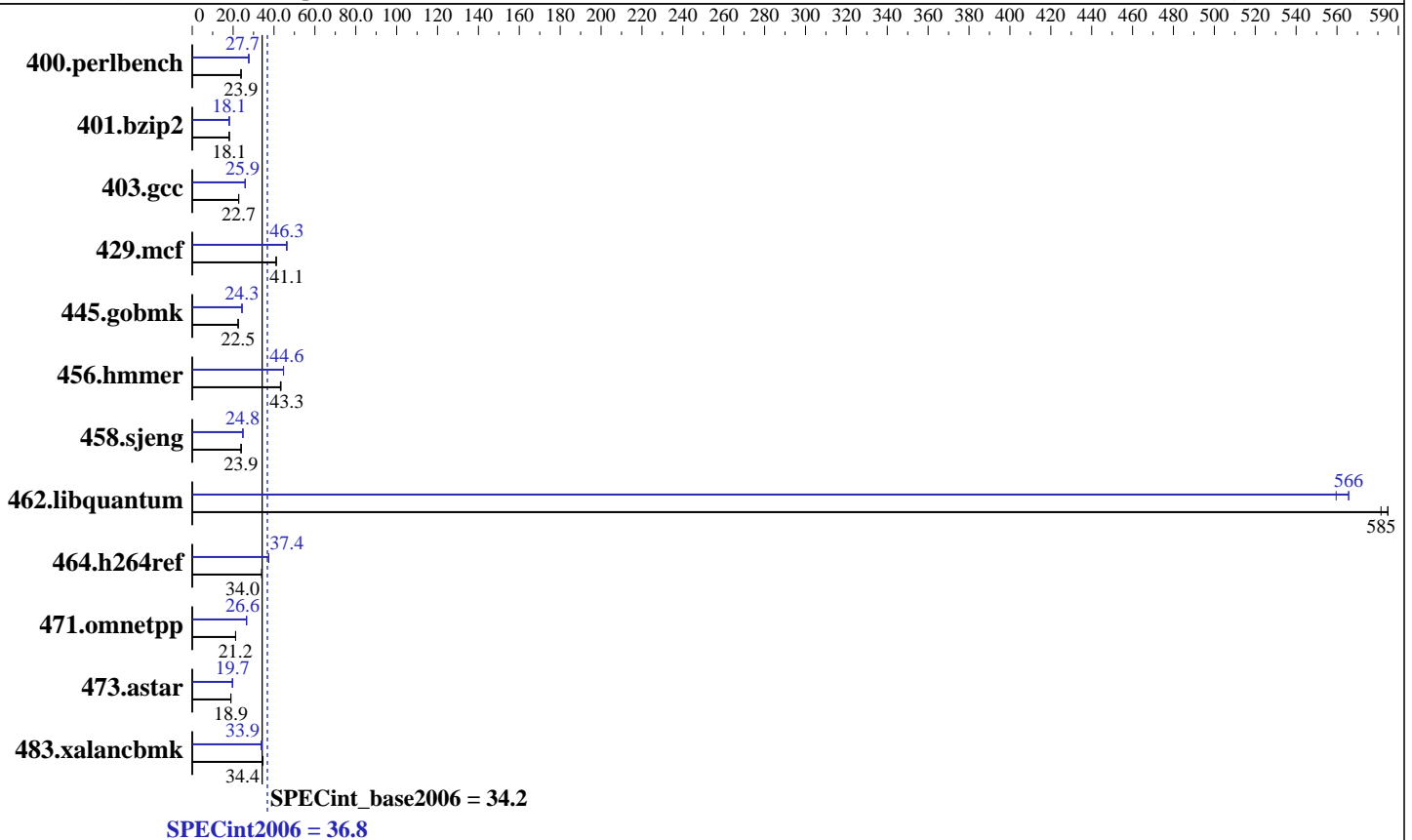
Test date: Sep-2010

Test sponsor: IBM Corporation

Hardware Availability: Oct-2010

Tested by: IBM Corporation

Software Availability: Jan-2010



### Hardware

CPU Name: Intel Xeon X5650  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.06 GHz  
 CPU MHz: 2667  
 FPU: Integrated  
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 12 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 48 GB (12 x 4 GB 2Rx4 PC3-10600R-9, ECC)  
 Disk Subsystem: 1 x 146 GB SAS, 15000 RPM  
 Other Hardware: None

### Software

Operating System: SuSe Linux Enterprise Server 11 (x86\_64), Kernel 2.6.27.19-5-default  
 Compiler: Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l\_cproc\_p\_11.1.064  
 Auto Parallel: Yes  
 File System: ext3  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	409	23.9	<b>409</b>	<b>23.9</b>	409	23.9	<b>353</b>	<b>27.7</b>	353	27.7	353	27.7
401.bzip2	<b>533</b>	<b>18.1</b>	533	18.1	533	18.1	<b>532</b>	<b>18.1</b>	532	18.1	532	18.1
403.gcc	<b>354</b>	<b>22.7</b>	355	22.7	354	22.7	311	25.9	<b>311</b>	<b>25.9</b>	311	25.9
429.mcf	<b>222</b>	<b>41.1</b>	222	41.0	222	41.1	197	46.3	<b>197</b>	<b>46.3</b>	198	46.1
445.gobmk	467	22.5	<b>467</b>	<b>22.5</b>	467	22.5	<b>431</b>	<b>24.3</b>	431	24.3	431	24.4
456.hammer	216	43.1	<b>216</b>	<b>43.3</b>	215	43.4	209	44.7	209	44.6	<b>209</b>	<b>44.6</b>
458.sjeng	<b>506</b>	<b>23.9</b>	505	23.9	506	23.9	488	24.8	489	24.8	<b>488</b>	<b>24.8</b>
462.libquantum	<b>35.4</b>	<b>585</b>	35.6	582	35.4	585	37.0	560	<b>36.6</b>	<b>566</b>	36.6	566
464.h264ref	<b>650</b>	<b>34.0</b>	649	34.1	650	34.0	591	37.4	592	37.4	<b>591</b>	<b>37.4</b>
471.omnetpp	<b>294</b>	<b>21.2</b>	295	21.2	294	21.2	<b>235</b>	<b>26.6</b>	235	26.6	235	26.5
473.astar	371	18.9	372	18.9	<b>372</b>	<b>18.9</b>	357	19.7	356	19.7	<b>356</b>	<b>19.7</b>
483.xalancbmk	201	34.3	<b>201</b>	<b>34.4</b>	199	34.6	204	33.8	<b>204</b>	<b>33.9</b>	203	34.0

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Platform Notes

BIOS Settings:  
 Turbo Mode Enable  
 Turbo Boost set to Traditional  
 CPU C State Enable  
 Data Reuse Disable

## General Notes

Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502  
 'ulimit -s unlimited' was used to set the stack size to unlimited prior to run  
 OMP\_NUM\_THREADS set to number of cores  
 KMP\_AFFINITY set to granularity=fine,scatter

## Base Compiler Invocation

C benchmarks:  
 icc -m64

C++ benchmarks:  
 icpc -m64



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## Base Portability Flags

```

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

```

## Base Optimization Flags

C benchmarks:

`-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch`

C++ benchmarks:

`-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64`

## Base Other Flags

C benchmarks:

`403.gcc: -Dalloca=_alloca`

## Peak Compiler Invocation

C benchmarks (except as noted below):

`icc -m64`

`400.perlbench: icc -m32`

`429.mcf: icc -m32`

`445.gobmk: icc -m32`

`464.h264ref: icc -m32`

C++ benchmarks (except as noted below):

`icpc -m32`

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## Peak Compiler Invocation (Continued)

473.astar: icpc -m64

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
 401.bzip2: -DSPEC\_CPU\_LP64  
 403.gcc: -DSPEC\_CPU\_LP64  
 456.hmmer: -DSPEC\_CPU\_LP64  
 458.sjeng: -DSPEC\_CPU\_LP64  
 462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
 473.astar: -DSPEC\_CPU\_LP64  
 483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
 -prof-use(pass 2) -ansi-alias -opt-prefetch

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div -static(pass 2) -prof-use(pass 2)  
 -auto-ilp32 -opt-prefetch -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-alloc  
 -opt-malloc-options=3 -auto-ilp32

429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2  
 -ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2  
 -ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
 -prof-use(pass 2) -unroll4

462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static -parallel  
 -opt-prefetch -par-schedule-static=32768 -ansi-alias

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
 -prof-use(pass 2) -unroll2 -ansi-alias

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## Peak Optimization Flags (Continued)

C++ benchmarks:

```

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
             -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
             -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
             -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
           -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
           -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs
           -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64

483.xalancbmk: -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch
              -Wl,-z,muldefs
              -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

```

## Peak Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100929.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100929.xml>

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Tested with SPEC CPU2006 v1.1.  
Report generated on Wed Jul 23 14:03:52 2014 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 9 November 2010.