SPEC® CINT2006 Result
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HITACHI

BladeSymphony BS320 (Intel Xeon L5630)

SPECint®_rate2006 = 190
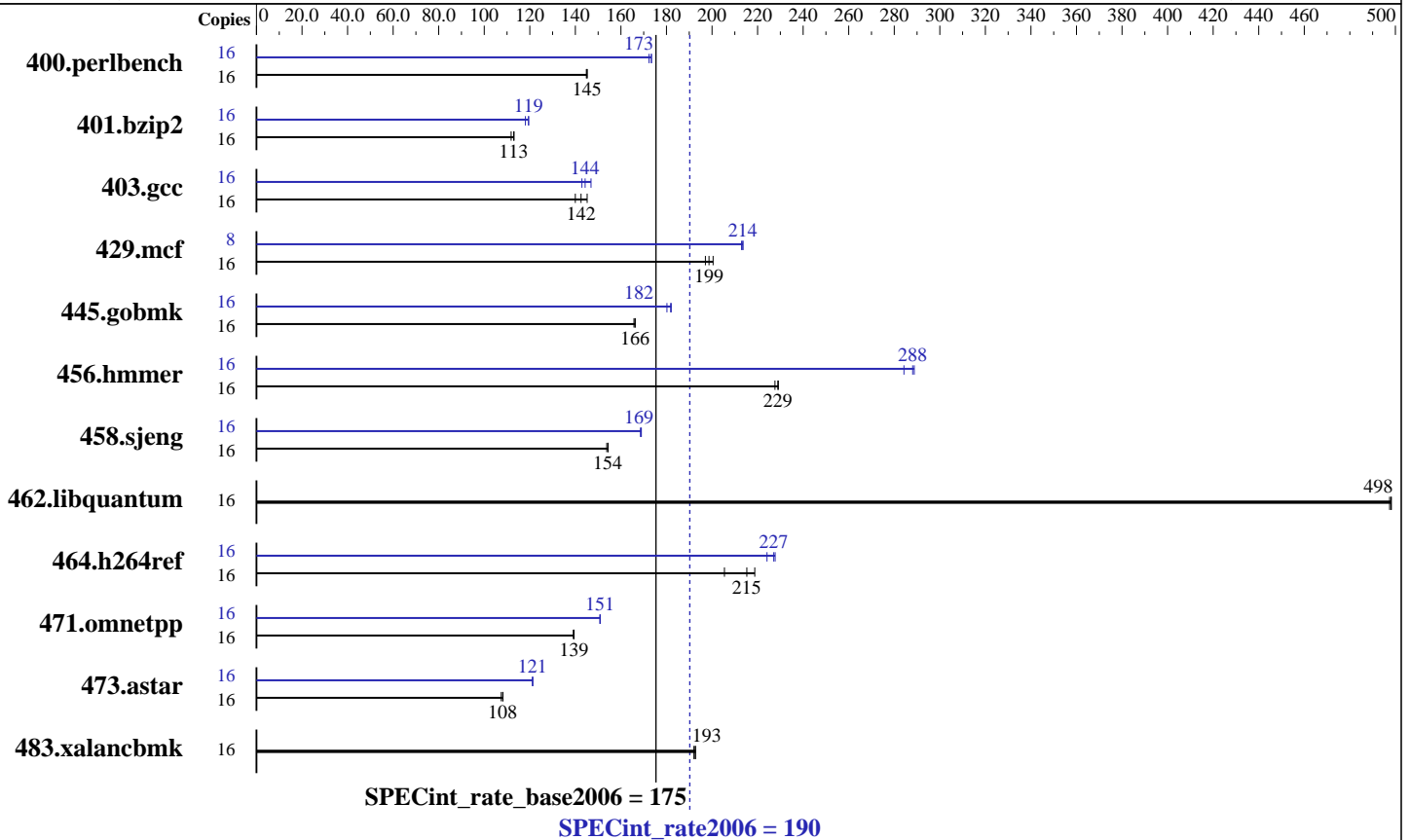
SPECint_rate_base2006 = 175

CPU2006 license: 872
Test sponsor: HITACHI
Tested by: HITACHI

Test date: Jul-2010
Hardware Availability: Mar-2010
Software Availability: Dec-2009

Benchmark	Copies	Base	Peak
400.perlbench	16	145	173
401.bzip2	16	113	119
403.gcc	16	142	144
429.mcf	8 / 16	199	214
445.gobmk	16	166	182
456.hmmer	16	229	288
458.sjeng	16	154	169
462.libquantum	16	498	
464.h264ref	16	215	227
471.omnetpp	16	139	151
473.astar	16	108	121
483.xalancbmk	16	193	

SPECint_rate_base2006 = 175
SPECint_rate2006 = 190

Hardware

CPU Name:	Intel Xeon L5630
CPU Characteristics:	Intel Turbo Boost Technology up to 2.40 GHz
CPU MHz:	2133
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip, 2 threads/core
CPU(s) orderable:	1, 2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	12 MB I+D on chip per chip
Other Cache:	None
Memory:	24 GB (6 x 4 GB PC3-10600R, 2 rank, CL9-9-9)
Disk Subsystem:	1 x 73 GB 10000 rpm SAS
Other Hardware:	None

Software

Operating System:	Red Hat Enterprise Linux Server release 5.4.3, Advanced Platform, Kernel 2.6.18-164.9.1.el5 on an x86_64
Compiler:	Intel C++ Compiler 11.1 for Linux Build 20091012 Package ID: l_cproc_p_11.1.059
Auto Parallel:	No
File System:	ext3
System State:	Multi-user run level 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V8.1

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Results Table

Benchmark	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
				Base							**Peak**			
400.perlbench	16	1079	145	**1077**	**145**	1077	145	16	907	172	**902**	**173**	902	173
401.bzip2	16	1381	112	1365	113	**1368**	**113**	16	1308	118	1292	119	**1293**	**119**
403.gcc	16	920	140	**904**	**142**	887	145	16	**893**	**144**	877	147	902	143
429.mcf	16	740	197	**734**	**199**	728	200	8	342	214	343	213	**342**	**214**
445.gobmk	16	1013	166	**1010**	**166**	1009	166	16	931	180	922	182	**923**	**182**
456.hmmer	16	656	228	652	229	**652**	**229**	16	517	289	525	284	**518**	**288**
458.sjeng	16	**1255**	**154**	1254	154	1259	154	16	1148	169	1146	169	**1148**	**169**
462.libquantum	16	666	497	**666**	**498**	665	498	16	666	497	**666**	**498**	665	498
464.h264ref	16	1619	219	1723	205	**1645**	**215**	16	1555	228	**1559**	**227**	1580	224
471.omnetpp	16	**718**	**139**	719	139	717	139	16	663	151	663	151	**663**	**151**
473.astar	16	1045	107	**1039**	**108**	1038	108	16	**927**	**121**	927	121	926	121
483.xalancbmk	16	**573**	**193**	573	193	575	192	16	**573**	**193**	573	193	575	192

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
    The config file option 'submit' was used.
    '/usr/bin/numactl' used to bind processes to CPUs
```

Operating System Notes

```
    'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
```

Base Compiler Invocation

C benchmarks:
```
      icc -m32
```

C++ benchmarks:
```
      icpc -m32
```

Base Portability Flags

```
    400.perlbench: -DSPEC_CPU_LINUX_IA32
    462.libquantum: -DSPEC_CPU_LINUX
    483.xalancbmk: -DSPEC_CPU_LINUX
```

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Base Optimization Flags

C benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch
```

C++ benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/home/bsc/smartheap/lib -lsmartheap
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc -m32
```

401.bzip2: `icc -m64`

456.hmmer: `icc -m64`

458.sjeng: `icc -m64`

C++ benchmarks (except as noted below):
```
icpc -m32
```

473.astar: `icpc -m64`

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
401.bzip2: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
458.sjeng: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LINUX`
473.astar: `-DSPEC_CPU_LP64`
483.xalancbmk: `-DSPEC_CPU_LINUX`

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Peak Optimization Flags

C benchmarks:

    400.perlbench: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
                `-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)`
                `-prof-use(pass 2) -ansi-alias`

    401.bzip2: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
                `-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)`
                `-prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32`

    403.gcc: `-xSSE4.2 -ipo -O3 -no-prec-div -static`

    429.mcf: `-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch`

    445.gobmk: `-xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2`
                `-ipo -no-prec-div -ansi-alias`

    456.hmmer: `-xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2`
                `-ansi-alias -auto-ilp32`

    458.sjeng: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
                `-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)`
                `-prof-use(pass 2) -unroll4 -auto-ilp32`

    462.libquantum: `basepeak = yes`

    464.h264ref: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
                `-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)`
                `-prof-use(pass 2) -unroll2 -ansi-alias`

C++ benchmarks:

    471.omnetpp: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
                `-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`
                `-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs`
                `-L/home/bsc/smartheap/lib -lsmartheap`

    473.astar: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
                `-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`
                `-ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs`
                `-L/home/bsc/smartheap/lib -lsmartheap64`

    483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

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Peak Other Flags (Continued)

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100929.03.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100929.03.xml

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