



SPEC[®] CFP2006 Result

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IBM Corporation

SPECfp[®]2006 = 34.7

IBM System x3690 X5 (Intel Xeon X6550)

SPECfp_base2006 = 32.4

CPU2006 license: 11

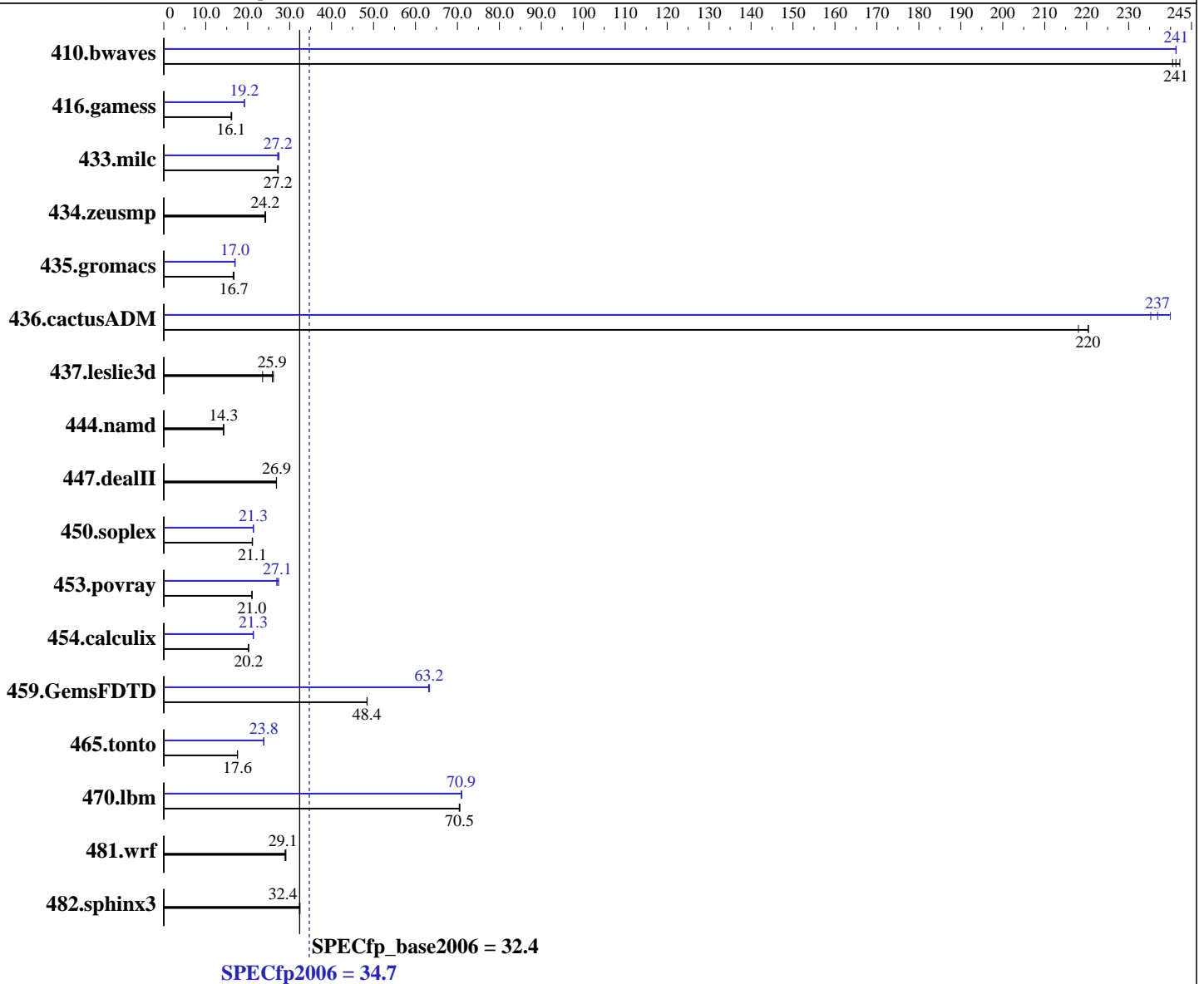
Test date: Aug-2010

Test sponsor: IBM Corporation

Hardware Availability: Aug-2010

Tested by: IBM Corporation

Software Availability: Jan-2010



Hardware

CPU Name: Intel Xeon X6550
 CPU Characteristics: Intel Turbo Boost Technology up to 2.40 GHz
 CPU MHz: 2000
 FPU: Integrated
 CPU(s) enabled: 16 cores, 2 chips, 8 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: SuSE Linux Enterprise Server 11 (x86_64), Kernel 2.6.27.19-5-default
 Compiler: Intel C++ and Fortran Professional Compiler for IA32 and Intel 64, Version 11.1
 Build 20091130 Package ID: l_cprof_p_11.1.064, l_cprof_p_11.1.064
 Auto Parallel: Yes
 File System: ext3
 System State: Run level 3 (multi-user)

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L3 Cache: 18 MB I+D on chip per chip
Other Cache: None
Memory: 128 GB (32 x 4 GB PC3-8500R CL7, Quad Rank)
Disk Subsystem: 1 x 146 GB SAS, 15000 RPM
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	56.5	240	56.1	242	56.3	241	56.3	241	56.3	241	56.3	241
416.gamess	1208	16.2	1218	16.1	1218	16.1	1019	19.2	1019	19.2	1019	19.2
433.milc	337	27.2	338	27.1	336	27.3	334	27.5	338	27.2	337	27.2
434.zeusmp	376	24.2	377	24.1	375	24.3	376	24.2	377	24.1	375	24.3
435.gromacs	426	16.8	427	16.7	432	16.5	420	17.0	421	17.0	422	16.9
436.cactusADM	54.8	218	54.2	220	54.2	220	50.8	235	49.8	240	50.4	237
437.leslie3d	399	23.6	359	26.2	363	25.9	399	23.6	359	26.2	363	25.9
444.namd	563	14.3	562	14.3	563	14.3	563	14.3	562	14.3	563	14.3
447.dealII	426	26.9	426	26.9	426	26.8	426	26.9	426	26.9	426	26.8
450.soplex	396	21.1	395	21.1	394	21.2	391	21.3	391	21.3	390	21.4
453.povray	253	21.0	253	21.0	254	21.0	196	27.1	194	27.4	198	26.9
454.calculix	409	20.2	408	20.2	409	20.2	386	21.3	386	21.4	387	21.3
459.GemsFDTD	219	48.4	219	48.5	219	48.4	167	63.4	168	63.2	168	63.1
465.tonto	560	17.6	560	17.6	560	17.6	413	23.9	413	23.8	413	23.8
470.lbm	195	70.5	195	70.6	195	70.4	194	70.9	194	70.9	193	71.1
481.wrf	384	29.1	386	28.9	384	29.1	384	29.1	386	28.9	384	29.1
482.sphinx3	602	32.4	602	32.4	600	32.5	602	32.4	602	32.4	600	32.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
echo 1 > /proc/sys/vm/zone_reclaim_mode
```

Platform Notes

Turbo Boost set to Traditional

General Notes

```
'ulimit -s unlimited' was used to set the stack size to unlimited prior to run
Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to granularity=fine,scatter
KMP_STACKSIZE set to 200M
```



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Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
 416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
 434.zeusmp: -DSPEC_CPU_LP64
 435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
 437.lelie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
 447.dealII: -DSPEC_CPU_LP64
 450.soplex: -DSPEC_CPU_LP64
 453.povray: -DSPEC_CPU_LP64
 454.calculix: -DSPEC_CPU_LP64 -nofor_main
 459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
 482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Fortran benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch



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Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-ansi-alias

470.lbm: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-parallel -ansi-alias -auto-ilp32

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: basepeak = yes

447.dealII: basepeak = yes

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-malloc-options=3 -auto-ilp32

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll4 -ansi-alias

Fortran benchmarks:

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Peak Optimization Flags (Continued)

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch
-parallel

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -Ob0 -opt-prefetch -parallel

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-inline-calloc -opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32

436.cactusADM: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -opt-prefetch -parallel -auto-ilp32

454.calculix: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100929.00.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100929.00.xml>



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Tested with SPEC CPU2006 v1.1.
Report generated on Wed Jul 23 12:46:37 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 28 September 2010.