



# SPEC<sup>®</sup> CINT2006 Result

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Itaotec

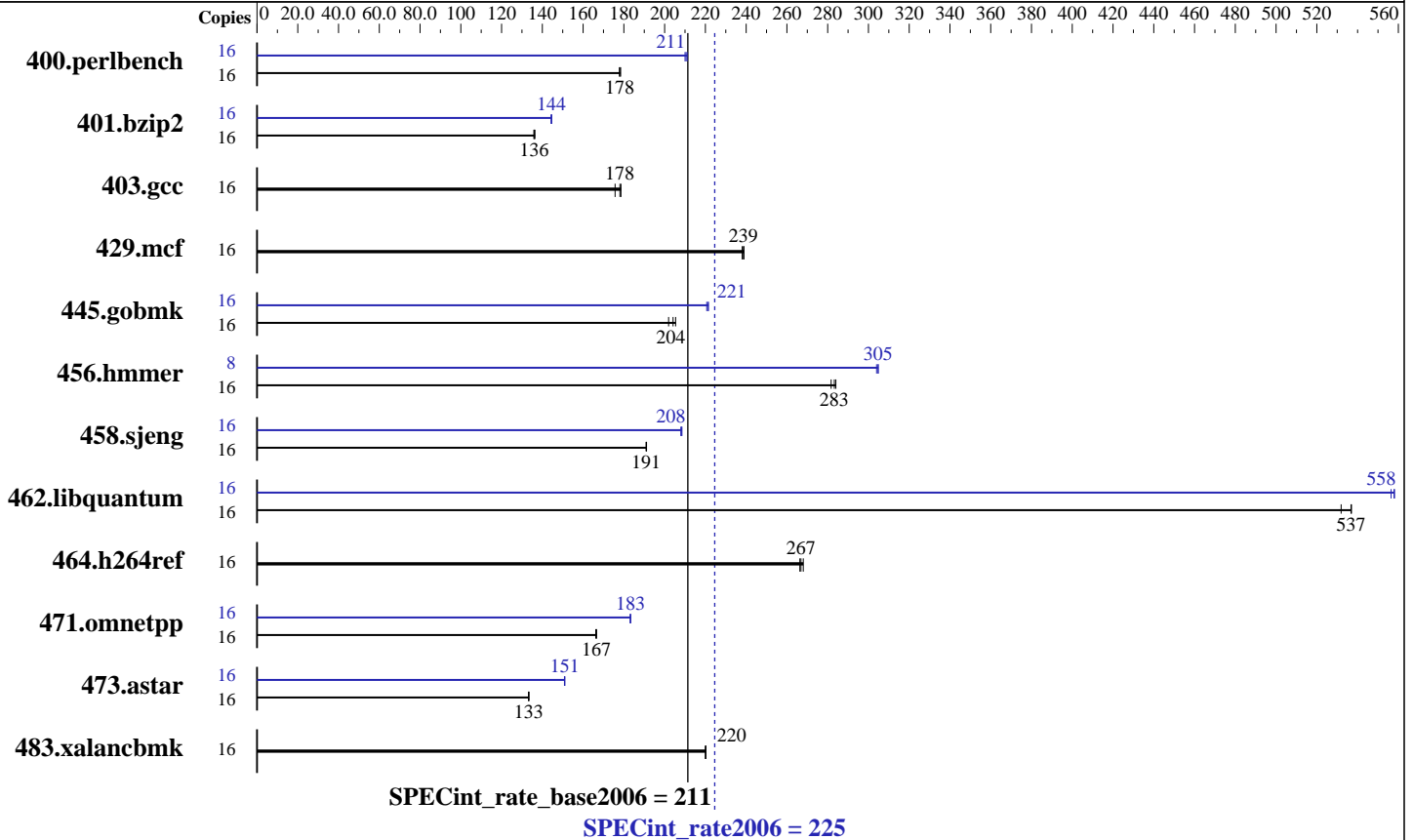
SPECint<sup>®</sup>\_rate2006 = 225

Servidor Itaotec MX223 (Intel Xeon E5620)

SPECint\_rate\_base2006 = 211

CPU2006 license: 9001  
Test sponsor: Itaotec  
Tested by: Itaotec

Test date: Jul-2010  
Hardware Availability: Apr-2010  
Software Availability: Jan-2010



## Hardware

CPU Name: Intel Xeon E5620  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.66 GHz  
 CPU MHz: 2400  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core  
 CPU(s) orderable: 1, 2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 12 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 48 GB (12 x 4GB, DDR3-1066, Dual Rank, CL 7, ECC)  
 Disk Subsystem: 1 x 160 GB SATA-2, 7200 RPM  
 Other Hardware: None

## Software

Operating System: SUSE Linux Enterprise Server 11 (x86\_64), Kernel 2.6.27.19-5-smp  
 Compiler: Intel C++ Professional Compiler 11.1 for Linux Build 20091130 Package ID: l\_cproc\_p\_11.1.064  
 Auto Parallel: No  
 File System: ReiserFS  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	<b>878</b>	<b>178</b>	876	178	879	178	16	<b>743</b>	<b>211</b>	741	211	744	210
401.bzip2	16	1136	136	<b>1135</b>	<b>136</b>	1132	136	16	1070	144	<b>1069</b>	<b>144</b>	1067	145
403.gcc	16	733	176	<b>723</b>	<b>178</b>	721	179	16	733	176	<b>723</b>	<b>178</b>	721	179
429.mcf	16	613	238	611	239	<b>611</b>	<b>239</b>	16	613	238	611	239	<b>611</b>	<b>239</b>
445.gobmk	16	831	202	817	205	<b>822</b>	<b>204</b>	16	758	222	761	221	<b>759</b>	<b>221</b>
456.hammer	16	<b>527</b>	<b>283</b>	530	282	526	284	8	245	305	<b>245</b>	<b>305</b>	246	304
458.sjeng	16	1013	191	<b>1014</b>	<b>191</b>	1014	191	16	929	208	<b>929</b>	<b>208</b>	931	208
462.libquantum	16	617	537	<b>618</b>	<b>537</b>	623	532	16	<b>594</b>	<b>558</b>	596	556	594	558
464.h264ref	16	1329	266	<b>1327</b>	<b>267</b>	1321	268	16	1329	266	<b>1327</b>	<b>267</b>	1321	268
471.omnetpp	16	<b>600</b>	<b>167</b>	602	166	600	167	16	545	183	<b>546</b>	<b>183</b>	546	183
473.astar	16	842	133	<b>843</b>	<b>133</b>	843	133	16	744	151	745	151	<b>744</b>	<b>151</b>
483.xalancbmk	16	<b>501</b>	<b>220</b>	502	220	501	220	16	<b>501</b>	<b>220</b>	502	220	501	220

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.

## General Notes

This result was measured on the Servidor Itautec MX223.  
The Servidor Itautec MX203 and the Servidor Itautec MX223 are electronically equivalent.

## Base Compiler Invocation

C benchmarks:  
icc -m32

C++ benchmarks:  
icpc -m32



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## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

C++ benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/home/richard/sh/SmartHeap\_8.1/lib -lsmarheap

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc -m32

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

462.libquantum: icc -m64

C++ benchmarks (except as noted below):  
icpc -m32

473.astar: icpc -m64

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64

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## Peak Portability Flags (Continued)

456.hmmcr: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -ansi-alias  
401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32  
403.gcc: basepeak = yes  
429.mcf: basepeak = yes  
445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2  
-ipo -no-prec-div -ansi-alias  
456.hmmcr: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2  
-ansi-alias -auto-ilp32  
458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll4 -auto-ilp32  
462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32  
-opt-prefetch  
464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/home/richard/sh/SmartHeap\_8.1/lib -lsmarheap  
473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs  
-L/home/richard/sh/SmartHeap\_8/lib -lsmarheap64

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## Peak Optimization Flags (Continued)

483.xalanbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Itautec-Intel-ic11.1-linux64-revE.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Itautec-Intel-ic11.1-linux64-revE.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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