



# SPEC® CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Fujitsu

SPECfp®2006 = 48.3

PRIMERGY RX200 S6, Intel Xeon X5680, 3.33 GHz

SPECfp\_base2006 = 45.0

CPU2006 license: 19

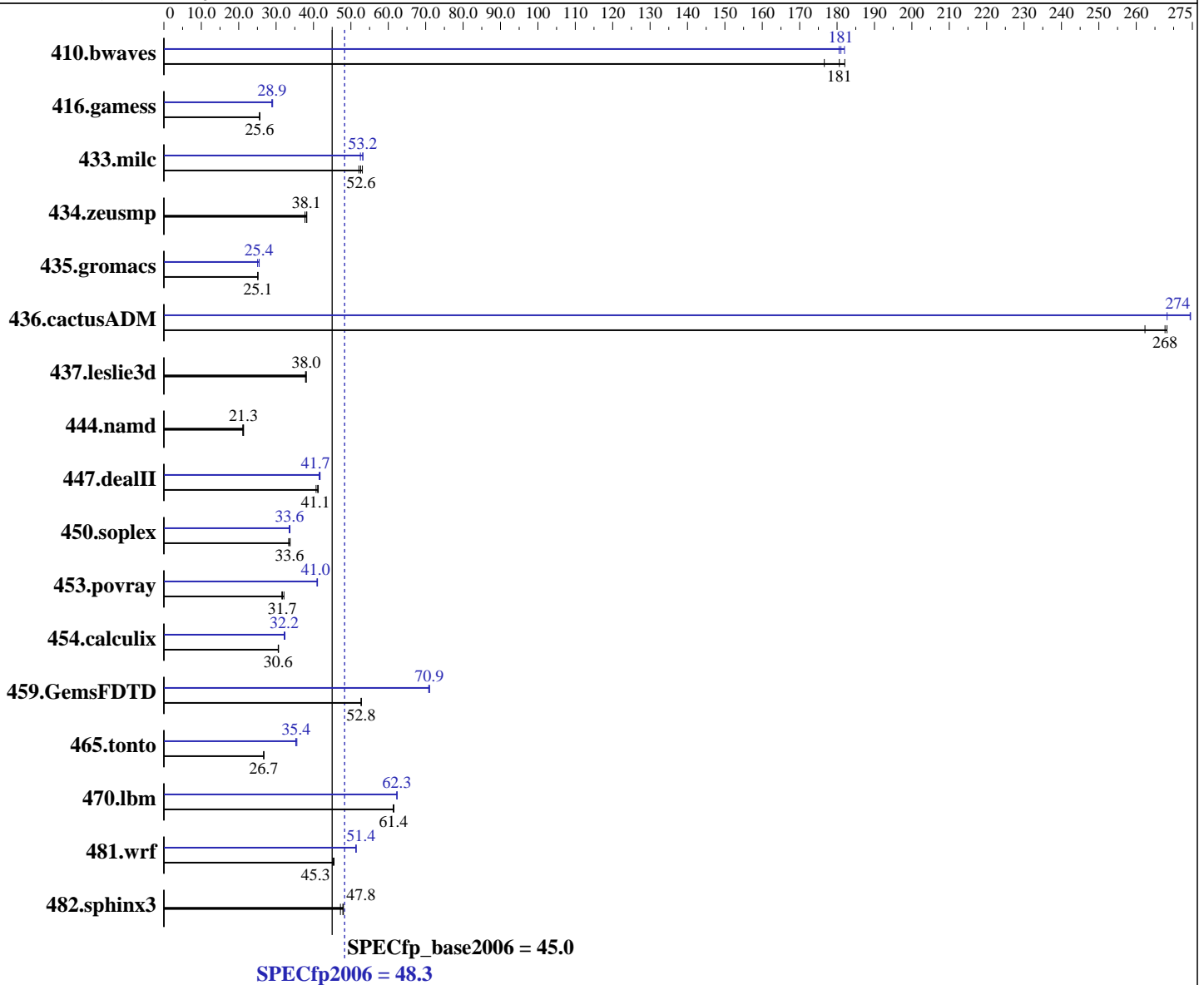
Test date: Jun-2010

Test sponsor: Fujitsu

Hardware Availability: May-2010

Tested by: Fujitsu

Software Availability: Jan-2010



### Hardware

CPU Name: Intel Xeon X5680  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.60 GHz  
 CPU MHz: 3333  
 FPU: Integrated  
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

### Software

Operating System: SUSE Linux Enterprise Server 11 (x86\_64), Kernel 2.6.27.19-5-default  
 Compiler: Intel C++ and Fortran Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l\_cproc\_p\_11.1.064, l\_cprof\_p\_11.1.064  
 Auto Parallel: Yes  
 File System: ext3  
 System State: Multi-User Run Level 3

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Fujitsu

SPECfp2006 = **48.3**

PRIMERGY RX200 S6, Intel Xeon X5680, 3.33 GHz

SPECfp\_base2006 = **45.0**

CPU2006 license: 19

Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Jun-2010

Hardware Availability: May-2010

Software Availability: Jan-2010

L3 Cache: 12 MB I+D on chip per chip  
Other Cache: None  
Memory: 48 GB (12x4 GB PC3-10600R, 2 rank, CL9-9-9, ECC, see add'l detail in notes)  
Disk Subsystem: 1 x SATA, 160 GB, 5400 RPM  
Other Hardware: None

Base Pointers: 64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	77.0	177	74.6	182	<b>75.2</b>	<b>181</b>	74.7	182	75.3	181	<b>75.1</b>	<b>181</b>
416.gamess	764	25.6	<b>765</b>	<b>25.6</b>	766	25.5	673	29.1	<b>677</b>	<b>28.9</b>	678	28.9
433.milc	<b>174</b>	<b>52.6</b>	176	52.1	173	53.1	175	52.5	<b>173</b>	<b>53.2</b>	173	53.2
434.zeusmp	<b>239</b>	<b>38.1</b>	241	37.7	238	38.2	<b>239</b>	<b>38.1</b>	241	37.7	238	38.2
435.gromacs	284	25.1	284	25.1	<b>284</b>	<b>25.1</b>	285	25.0	<b>281</b>	<b>25.4</b>	280	25.5
436.cactusADM	44.6	268	<b>44.6</b>	<b>268</b>	45.6	262	44.5	268	<b>43.5</b>	<b>274</b>	43.5	275
437.leslie3d	247	38.1	248	37.9	<b>247</b>	<b>38.0</b>	247	38.1	248	37.9	<b>247</b>	<b>38.0</b>
444.namd	381	21.1	377	21.3	<b>377</b>	<b>21.3</b>	381	21.1	377	21.3	<b>377</b>	<b>21.3</b>
447.dealII	<b>278</b>	<b>41.1</b>	277	41.3	281	40.7	276	41.5	274	41.7	<b>274</b>	<b>41.7</b>
450.soplex	<b>248</b>	<b>33.6</b>	250	33.4	247	33.8	247	33.7	<b>248</b>	<b>33.6</b>	249	33.5
453.povray	169	31.5	<b>168</b>	<b>31.7</b>	166	32.1	130	41.1	130	41.0	<b>130</b>	<b>41.0</b>
454.calculix	<b>270</b>	<b>30.6</b>	270	30.6	269	30.7	<b>256</b>	<b>32.2</b>	255	32.4	256	32.2
459.GemsFDTD	201	52.8	<b>201</b>	<b>52.8</b>	201	52.7	<b>150</b>	<b>70.9</b>	150	70.8	149	71.0
465.tonto	368	26.7	<b>368</b>	<b>26.7</b>	369	26.7	<b>278</b>	<b>35.4</b>	277	35.6	279	35.3
470.lbm	223	61.5	<b>224</b>	<b>61.4</b>	224	61.3	221	62.3	220	62.4	<b>221</b>	<b>62.3</b>
481.wrf	<b>247</b>	<b>45.3</b>	245	45.5	247	45.2	<b>217</b>	<b>51.4</b>	217	51.4	218	51.3
482.sphinx3	406	48.0	413	47.2	<b>407</b>	<b>47.8</b>	406	48.0	413	47.2	<b>407</b>	<b>47.8</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

## Platform Notes

BIOS configuration:  
Intel HT Technology = Disable  
Data Reuse Optimization = Disable

## General Notes

OMP\_NUM\_THREADS set to number of cores  
KMP\_AFFINITY set to granularity=fine,scatter  
KMP\_STACKSIZE set to 200M

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

**Fujitsu**

**SPECfp2006 = 48.3**

PRIMERGY RX200 S6, Intel Xeon X5680, 3.33 GHz

**SPECfp\_base2006 = 45.0**

**CPU2006 license:** 19  
**Test sponsor:** Fujitsu  
**Tested by:** Fujitsu

**Test date:** Jun-2010  
**Hardware Availability:** May-2010  
**Software Availability:** Jan-2010

## General Notes (Continued)

For information about Fujitsu please visit: <http://www.fujitsu.com>

## Base Compiler Invocation

C benchmarks:  
icc -m64

C++ benchmarks:  
icpc -m64

Fortran benchmarks:  
ifort -m64

Benchmarks using both Fortran and C:  
icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

C++ benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Fortran benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

**Fujitsu**

**SPECfp2006 = 48.3**

PRIMERGY RX200 S6, Intel Xeon X5680, 3.33 GHz

**SPECfp\_base2006 = 45.0**

CPU2006 license: 19

Test date: Jun-2010

Test sponsor: Fujitsu

Hardware Availability: May-2010

Tested by: Fujitsu

Software Availability: Jan-2010

## Base Optimization Flags (Continued)

Benchmarks using both Fortran and C:

`-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch`

## Peak Compiler Invocation

C benchmarks:

`icc -m64`

C++ benchmarks:

`icpc -m64`

Fortran benchmarks:

`ifort -m64`

Benchmarks using both Fortran and C:

`icc -m64 ifort -m64`

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

433.milc: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-ansi-alias`

470.lbm: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-parallel -ansi-alias -auto-ilp32`

482.sphinx3: `basepeak = yes`

C++ benchmarks:

444.namd: `basepeak = yes`

447.dealIII: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-unroll2 -ansi-alias -scalar-rep- -auto-ilp32`

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

**Fujitsu**

**SPECfp2006 = 48.3**

PRIMERGY RX200 S6, Intel Xeon X5680, 3.33 GHz

**SPECfp\_base2006 = 45.0**

**CPU2006 license:** 19

**Test sponsor:** Fujitsu

**Tested by:** Fujitsu

**Test date:** Jun-2010

**Hardware Availability:** May-2010

**Software Availability:** Jan-2010

## Peak Optimization Flags (Continued)

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-opt-malloc-options=3 -auto-ilp32

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-unroll4 -ansi-alias

### Fortran benchmarks:

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch  
-parallel

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-unroll2 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-unroll2 -Ob0 -opt-prefetch -parallel

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-inline-calloc -opt-malloc-options=3 -auto -unroll4

### Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-opt-prefetch -auto-ilp32

436.cactusADM: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-unroll2 -opt-prefetch -parallel -auto-ilp32

454.calculix: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32

481.wrf: Same as 454.calculix

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100330.01.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100330.01.xml>



# SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

SPECfp2006 = 48.3

PRIMERGY RX200 S6, Intel Xeon X5680, 3.33 GHz

SPECfp\_base2006 = 45.0

CPU2006 license: 19

Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Jun-2010

Hardware Availability: May-2010

Software Availability: Jan-2010

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.1.  
Report generated on Wed Jul 23 11:30:05 2014 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 20 July 2010.