



SPEC[®] CINT2006 Result

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Fujitsu

SPECint[®]_rate2006 = 510

PRIMERGY RX600 S5, Intel Xeon L7545, 1.87 GHz

SPECint_rate_base2006 = 474

CPU2006 license: 19

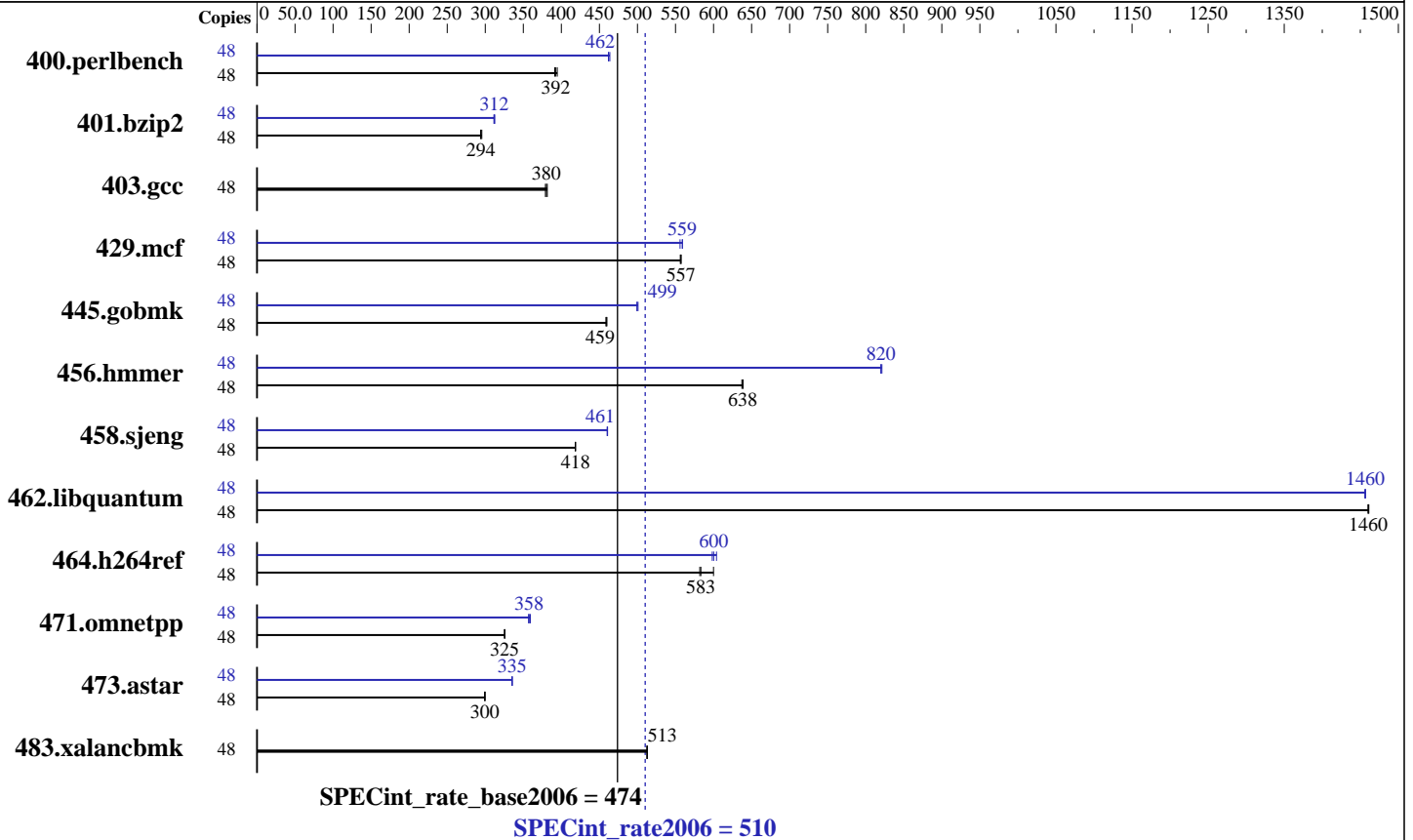
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Jun-2010

Hardware Availability: Jun-2010

Software Availability: Jan-2010



Hardware

CPU Name: Intel Xeon L7545
 CPU Characteristics: Intel Turbo Boost Technology up to 2.53 GHz
 CPU MHz: 1867
 FPU: Integrated
 CPU(s) enabled: 24 cores, 4 chips, 6 cores/chip, 2 threads/core
 CPU(s) orderable: 2,3,4 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 18 MB I+D on chip per chip
 Other Cache: None
 Memory: 512 GB (64x8 GB PC3-10600R, 2 rank, CL9-9-9, ECC, see add'l detail in notes)
 Disk Subsystem: 1 x SAS, 300 GB, 10000 RPM
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64), Kernel 2.6.27.19-5-default
 Compiler: Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l_cproc_p_11.1.064
 Auto Parallel: No
 File System: ext3
 System State: Multi-User Run Level 3
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1



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Results Table

| Benchmark | Base | | | | | | | | Peak | | | | | | | |
|----------------|--------|-------------|------------|-------------|------------|-------------|-------------|--------|-------------|------------|-------------|------------|-------------|-------------|--|--|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | | |
| 400.perlbench | 48 | 1198 | 391 | 1188 | 395 | <u>1195</u> | <u>392</u> | 48 | <u>1015</u> | <u>462</u> | 1011 | 464 | 1015 | 462 | | |
| 401.bzip2 | 48 | <u>1573</u> | <u>294</u> | 1568 | 295 | 1576 | 294 | 48 | <u>1485</u> | <u>312</u> | 1488 | 311 | 1482 | 313 | | |
| 403.gcc | 48 | 1013 | 382 | <u>1016</u> | <u>380</u> | 1019 | 379 | 48 | 1013 | 382 | <u>1016</u> | <u>380</u> | 1019 | 379 | | |
| 429.mcf | 48 | 785 | 557 | <u>786</u> | <u>557</u> | 787 | 556 | 48 | <u>783</u> | <u>559</u> | 783 | 559 | 787 | 556 | | |
| 445.gobmk | 48 | 1098 | 459 | 1095 | 460 | <u>1097</u> | <u>459</u> | 48 | 1006 | 501 | 1008 | 499 | <u>1008</u> | <u>499</u> | | |
| 456.hammer | 48 | 703 | 637 | <u>702</u> | <u>638</u> | 701 | 639 | 48 | 545 | 821 | 546 | 820 | <u>546</u> | <u>820</u> | | |
| 458.sjeng | 48 | <u>1388</u> | <u>418</u> | 1389 | 418 | 1387 | 419 | 48 | 1262 | 460 | <u>1261</u> | <u>461</u> | 1261 | 461 | | |
| 462.libquantum | 48 | 681 | 1460 | 681 | 1460 | <u>681</u> | <u>1460</u> | 48 | 683 | 1460 | 683 | 1460 | <u>683</u> | <u>1460</u> | | |
| 464.h264ref | 48 | 1825 | 582 | 1771 | 600 | <u>1821</u> | <u>583</u> | 48 | <u>1770</u> | <u>600</u> | 1775 | 598 | 1759 | 604 | | |
| 471.omnetpp | 48 | 921 | 326 | 922 | 325 | <u>922</u> | <u>325</u> | 48 | 841 | 357 | <u>839</u> | <u>358</u> | 835 | 359 | | |
| 473.astar | 48 | 1127 | 299 | 1124 | 300 | <u>1125</u> | <u>300</u> | 48 | <u>1005</u> | <u>335</u> | 1005 | 335 | 1005 | 335 | | |
| 483.xalancbmk | 48 | 646 | 512 | <u>646</u> | <u>513</u> | 646 | 513 | 48 | 646 | 512 | <u>646</u> | <u>513</u> | 646 | 513 | | |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

The system automatically configures the memory to run at 978 MHz.

General Notes

For information about Fujitsu please visit: <http://www.fujitsu.com>
Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502

Base Compiler Invocation

C benchmarks:
icc -m32

C++ benchmarks:
icpc -m32



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Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.icl1.1/libic11.1-32bit -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

462.libquantum: icc -m64

C++ benchmarks (except as noted below):

icpc -m32

473.astar: icpc -m64

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64

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Peak Portability Flags (Continued)

456.hmmcr: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -ansi-alias
401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32
403.gcc: basepeak = yes
429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch
445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
-ipo -no-prec-div -ansi-alias
456.hmmcr: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
-ansi-alias -auto-ilp32
458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -unroll4 -auto-ilp32
462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32
-opt-prefetch
464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap
473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs

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Peak Optimization Flags (Continued)

473.astar (continued):

-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100708.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100708.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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