



# SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

SPECint®\_rate2006 = 241

PowerEdge R910 (Intel Xeon E7530, 1.87 GHz)

SPECint\_rate\_base2006 = 224

CPU2006 license: 55

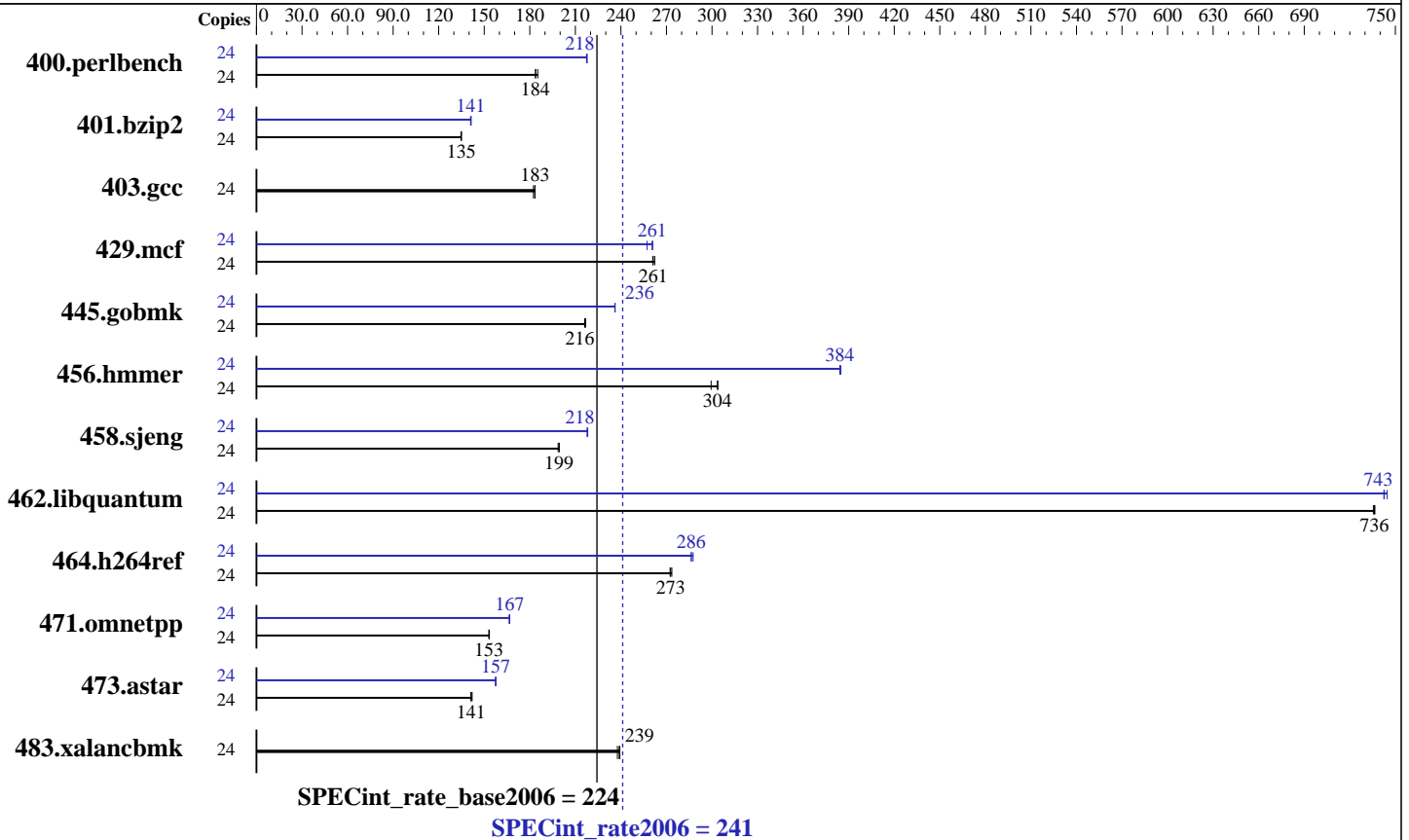
Test date: Mar-2010

Test sponsor: Dell Inc.

Hardware Availability: Mar-2010

Tested by: Dell Inc.

Software Availability: Dec-2009



## Hardware

CPU Name: Intel Xeon E7530  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.13 GHz  
 CPU MHz: 1867  
 FPU: Integrated  
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core  
 CPU(s) orderable: 2,4 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 12 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 128 GB (32 x 4 GB DDR3-1066 QR RDIMM, CL7, ECC, downclocked to 978 MHz)  
 Disk Subsystem: 1 x 300 GB 10000 RPM SAS 6Gb  
 Other Hardware: None

## Software

Operating System: Red Hat Linux Enterprise Linux 5 (x86\_64) Update 4 errata kernel (RHEL 5.4.z) kernel-2.6.18-164.9.1.el5.x86\_64  
 Compiler: Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l\_cproc\_p\_11.1.064  
 Auto Parallel: No  
 File System: ext3  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	24	<b><u>1276</u></b>	<b><u>184</u></b>	1266	185	1276	184	24	1077	218	1079	217	<b><u>1078</u></b>	<b><u>218</u></b>
401.bzip2	24	<b><u>1717</u></b>	<b><u>135</u></b>	1716	135	1720	135	24	1643	141	<b><u>1640</u></b>	<b><u>141</u></b>	1639	141
403.gcc	24	1053	183	1059	182	<b><u>1054</u></b>	<b><u>183</u></b>	24	1053	183	1059	182	<b><u>1054</u></b>	<b><u>183</u></b>
429.mcf	24	839	261	<b><u>838</u></b>	<b><u>261</u></b>	835	262	24	839	261	851	257	<b><u>840</u></b>	<b><u>261</u></b>
445.gobmk	24	1162	217	<b><u>1164</u></b>	<b><u>216</u></b>	1165	216	24	1067	236	<b><u>1066</u></b>	<b><u>236</u></b>	1066	236
456.hammer	24	748	299	737	304	<b><u>738</u></b>	<b><u>304</u></b>	24	582	385	<b><u>583</u></b>	<b><u>384</u></b>	583	384
458.sjeng	24	1457	199	<b><u>1457</u></b>	<b><u>199</u></b>	1462	199	24	1331	218	1333	218	<b><u>1333</u></b>	<b><u>218</u></b>
462.libquantum	24	675	736	676	736	<b><u>675</u></b>	<b><u>736</u></b>	24	670	742	668	745	<b><u>670</u></b>	<b><u>743</u></b>
464.h264ref	24	1950	272	<b><u>1947</u></b>	<b><u>273</u></b>	1942	274	24	1847	288	1855	286	<b><u>1855</u></b>	<b><u>286</u></b>
471.omnetpp	24	978	153	980	153	<b><u>979</u></b>	<b><u>153</u></b>	24	902	166	899	167	<b><u>900</u></b>	<b><u>167</u></b>
473.astar	24	<b><u>1191</u></b>	<b><u>141</u></b>	1186	142	1193	141	24	1069	158	1070	157	<b><u>1070</u></b>	<b><u>157</u></b>
483.xalancbmk	24	692	239	<b><u>693</u></b>	<b><u>239</u></b>	697	238	24	692	239	<b><u>693</u></b>	<b><u>239</u></b>	697	238

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

## Platform Notes

vm.zone\_reclaim\_mode = 1 in /etc/sysctl.conf file  
BIOS Settings:  
Power Management = Maximum Performance (Default = Active Power Controller)

## General Notes

The Dell PowerEdge R910 and  
the Bull NovaScale R480 F2 models are electronically equivalent.  
The results have been measured on a Dell PowerEdge R910 model.

## Base Compiler Invocation

C benchmarks:  
icc -m32

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## Base Compiler Invocation (Continued)

C++ benchmarks:  
icpc -m32

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

C++ benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc -m32

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

462.libquantum: icc -m64

C++ benchmarks (except as noted below):  
icpc -m32

473.astar: icpc -m64



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## Peak Portability Flags

```

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

```

## Peak Optimization Flags

C benchmarks:

```

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
               -prof-use(pass 2) -ansi-alias

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
            -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
            -prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

403.gcc: basepeak = yes

429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
            -ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
            -ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
            -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
            -prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32
                -opt-prefetch

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
              -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
              -prof-use(pass 2) -unroll2 -ansi-alias

```

C++ benchmarks:

```

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
              -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
              -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
              -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

```

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## Peak Optimization Flags (Continued)

```
473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
           -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
           -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs
           -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64
```

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100316.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100316.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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