



SPEC® CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

SPECfp®_rate2006 = 123

IBM System x3620 M3 (Intel Xeon E5507)

SPECfp_rate_base2006 = 120

CPU2006 license: 11

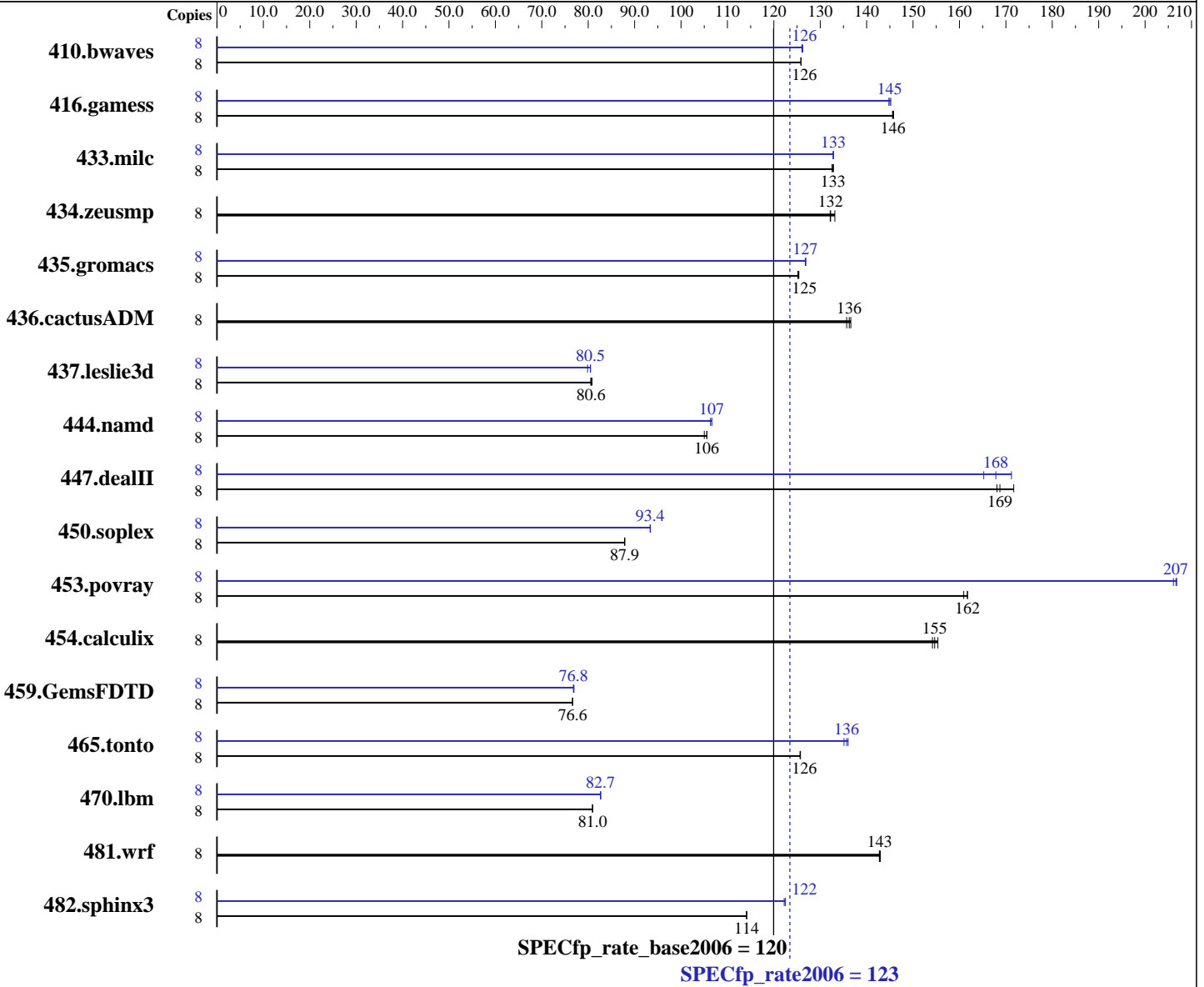
Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: Jun-2010

Hardware Availability: May-2010

Software Availability: Jan-2010



Hardware

CPU Name: Intel Xeon E5507
 CPU Characteristics:
 CPU MHz: 2267
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

Software

Operating System: SuSe Linux Enterprise Server 11 (x86_64),
 Kernel 2.6.27.19-5-default
 Compiler: Intel C++ and Fortran Professional Compiler for
 IA32 and Intel 64, Version 11.1
 Build 20091130 Package ID: l_cproc_p_11.1.064,
 l_cprof_p_11.1.064
 Auto Parallel: No
 File System: ext3
 System State: Run level 3 (multi-user)

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

SPECfp_rate2006 = 123

IBM System x3620 M3 (Intel Xeon E5507)

SPECfp_rate_base2006 = 120

CPU2006 license: 11

Test date: Jun-2010

Test sponsor: IBM Corporation

Hardware Availability: May-2010

Tested by: IBM Corporation

Software Availability: Jan-2010

L3 Cache: 4 MB I+D on chip per chip
Other Cache: None
Memory: 48 GB (12 x 4 GB PC3-10600R CL9, 2 Rank)
Disk Subsystem: 1 x 250 GB SATA, 7200 RPM
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	864	126	864	126	864	126	8	862	126	861	126	863	126
416.gamess	8	1075	146	1074	146	1076	146	8	1080	145	1082	145	1079	145
433.milc	8	554	133	553	133	553	133	8	553	133	553	133	553	133
434.zeusmp	8	547	133	551	132	551	132	8	547	133	551	132	551	132
435.gromacs	8	456	125	456	125	456	125	8	450	127	451	127	451	127
436.cactusADM	8	700	137	705	136	702	136	8	700	137	705	136	702	136
437.leslie3d	8	934	80.5	933	80.6	930	80.8	8	934	80.5	934	80.5	942	79.9
444.namd	8	608	106	608	106	611	105	8	602	107	602	107	603	106
447.dealII	8	542	169	533	172	544	168	8	554	165	545	168	535	171
450.soplex	8	759	87.9	759	87.9	760	87.8	8	714	93.4	715	93.3	715	93.4
453.povray	8	263	162	264	161	263	162	8	207	206	206	207	206	207
454.calculix	8	425	155	428	154	427	155	8	425	155	428	154	427	155
459.GemsFDTD	8	1108	76.6	1107	76.6	1108	76.6	8	1105	76.8	1105	76.8	1103	76.9
465.tonto	8	626	126	626	126	626	126	8	583	135	579	136	580	136
470.lbm	8	1358	81.0	1358	81.0	1358	80.9	8	1329	82.7	1330	82.7	1330	82.7
481.wrf	8	626	143	626	143	625	143	8	626	143	626	143	625	143
482.sphinx3	8	1365	114	1366	114	1366	114	8	1273	122	1276	122	1273	122

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Platform Notes

Turbo Mode Enable
Turbo Boost set to Traditional
CPU C State Enable
Data Reuse Disable



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

SPECfp_rate2006 = 123

IBM System x3620 M3 (Intel Xeon E5507)

SPECfp_rate_base2006 = 120

CPU2006 license: 11

Test date: Jun-2010

Test sponsor: IBM Corporation

Hardware Availability: May-2010

Tested by: IBM Corporation

Software Availability: Jan-2010

General Notes

Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502
'ulimit -s unlimited' was used to set the stack size to unlimited prior to run

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

SPECfp_rate2006 = 123

IBM System x3620 M3 (Intel Xeon E5507)

SPECfp_rate_base2006 = 120

CPU2006 license: 11

Test date: Jun-2010

Test sponsor: IBM Corporation

Hardware Availability: May-2010

Tested by: IBM Corporation

Software Availability: Jan-2010

Base Optimization Flags (Continued)

Fortran benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static

Benchmarks using both Fortran and C:

-xSSE4.2 -ipo -O3 -no-prec-div -static

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
 416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
 434.zeusmp: -DSPEC_CPU_LP64
 435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
 437.leslie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
 447.dealII: -DSPEC_CPU_LP64
 453.povray: -DSPEC_CPU_LP64
 454.calculix: -DSPEC_CPU_LP64 -nofor_main
 459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

SPECfp_rate2006 = 123

IBM System x3620 M3 (Intel Xeon E5507)

SPECfp_rate_base2006 = 120

CPU2006 license: 11

Test date: Jun-2010

Test sponsor: IBM Corporation

Hardware Availability: May-2010

Tested by: IBM Corporation

Software Availability: Jan-2010

Peak Optimization Flags

C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-fno-alias -opt-prefetch

470.lbm: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-malloc-options=3 -ansi-alias -auto-ilp32

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-fno-alias -auto-ilp32

447.dealIII: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias -scalar-rep-

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-malloc-options=3

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: -xSSE4.2 -ipo -O3 -no-prec-div -static

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -Ob0

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll4 -auto -inline-calloc -opt-malloc-options=3

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

SPECfp_rate2006 = 123

IBM System x3620 M3 (Intel Xeon E5507)

SPECfp_rate_base2006 = 120

CPU2006 license: 11

Test date: Jun-2010

Test sponsor: IBM Corporation

Hardware Availability: May-2010

Tested by: IBM Corporation

Software Availability: Jan-2010

Peak Optimization Flags (Continued)

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100601.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100601.xml>

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Wed Jul 23 12:41:23 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 9 July 2010.