



SPEC® CFP2006 Result

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Acer Incorporated

SPECfp®2006 = 24.6

Acer AB460 F1 (Intel Xeon E5502, 1.86 GHz)

SPECfp_base2006 = 23.0

CPU2006 license: 97

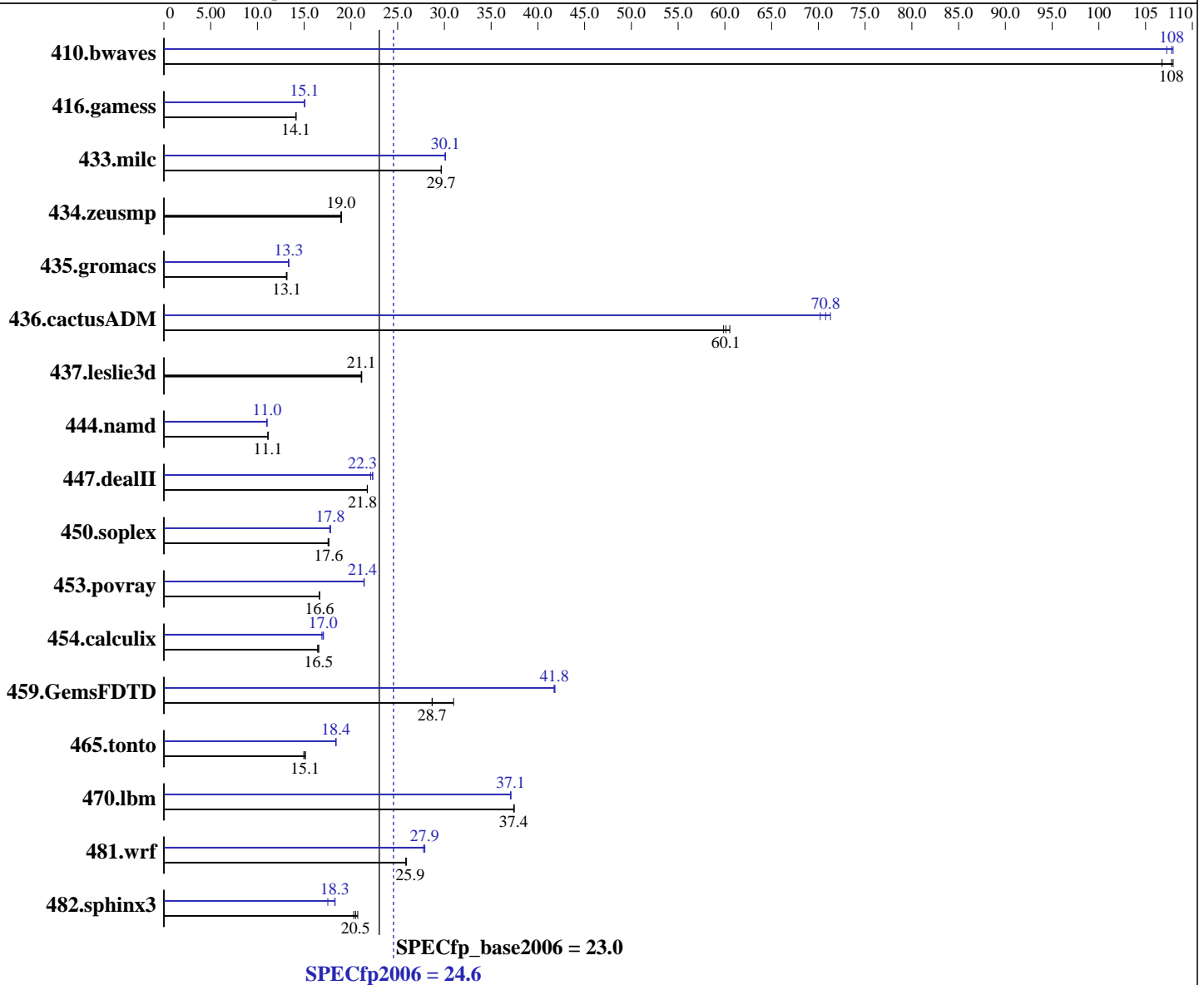
Test date: May-2010

Test sponsor: Acer Incorporated

Hardware Availability: Jul-2010

Tested by: Acer Incorporated

Software Availability: Jan-2010



Hardware

CPU Name: Intel Xeon E5502
 CPU Characteristics:
 CPU MHz: 1866
 FPU: Integrated
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64)
 Kernel 2.6.27.19-5-default
 Compiler: Intel C++ and Fortran Professional Compiler for IA32 and Intel 64, Version 11.1
 Build 20091130 Package ID: l_cproc_p_11.1.064, l_cprof_p_11.1.064
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Run level 3 (multi-user)

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L3 Cache: 4 MB I+D on chip per chip
Other Cache: None
Memory: 24 GB (6 x 4 GB DDR3-1333 RDIMM, ECC, CL9 running at 800MHz)
Disk Subsystem: 1 x 300 GB SATA II, 10000 RPM
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

| Benchmark | Base | | | | | | Peak | | | | | |
|---------------|------------|-------------|------------|-------------|-------------|-------------|-------------|-------------|------------|-------------|-------------|-------------|
| | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 410.bwaves | 127 | 107 | 126 | 108 | <u>126</u> | <u>108</u> | 127 | 107 | <u>126</u> | <u>108</u> | 126 | 108 |
| 416.gamess | 1387 | 14.1 | 1386 | 14.1 | <u>1386</u> | <u>14.1</u> | 1301 | 15.1 | 1302 | 15.0 | <u>1301</u> | <u>15.1</u> |
| 433.milc | 309 | 29.7 | <u>309</u> | <u>29.7</u> | 309 | 29.7 | 305 | 30.1 | <u>305</u> | <u>30.1</u> | 305 | 30.1 |
| 434.zeusmp | 481 | 18.9 | <u>480</u> | <u>19.0</u> | 479 | 19.0 | 481 | 18.9 | <u>480</u> | <u>19.0</u> | 479 | 19.0 |
| 435.gromacs | <u>543</u> | <u>13.1</u> | 543 | 13.2 | 545 | 13.1 | <u>535</u> | <u>13.3</u> | 536 | 13.3 | 535 | 13.4 |
| 436.cactusADM | 200 | 59.9 | <u>199</u> | <u>60.1</u> | 197 | 60.5 | <u>169</u> | <u>70.8</u> | 168 | 71.3 | 170 | 70.2 |
| 437.leslie3d | <u>445</u> | <u>21.1</u> | 444 | 21.2 | 445 | 21.1 | <u>445</u> | <u>21.1</u> | 444 | 21.2 | 445 | 21.1 |
| 444.namd | <u>720</u> | <u>11.1</u> | 720 | 11.1 | 721 | 11.1 | 727 | 11.0 | 728 | 11.0 | <u>728</u> | <u>11.0</u> |
| 447.dealII | 526 | 21.7 | 525 | 21.8 | <u>526</u> | <u>21.8</u> | 512 | 22.3 | <u>512</u> | <u>22.3</u> | 517 | 22.1 |
| 450.soplex | 475 | 17.6 | 473 | 17.6 | <u>473</u> | <u>17.6</u> | 470 | 17.7 | <u>468</u> | <u>17.8</u> | 468 | 17.8 |
| 453.povray | 320 | 16.6 | <u>320</u> | <u>16.6</u> | 320 | 16.6 | 248 | 21.4 | 249 | 21.4 | <u>249</u> | <u>21.4</u> |
| 454.calculix | 499 | 16.5 | <u>499</u> | <u>16.5</u> | 502 | 16.4 | <u>485</u> | <u>17.0</u> | 483 | 17.1 | 488 | 16.9 |
| 459.GemsFDTD | 370 | 28.7 | <u>370</u> | <u>28.7</u> | 342 | 31.0 | <u>254</u> | <u>41.8</u> | 254 | 41.7 | 254 | 41.8 |
| 465.tonto | 650 | 15.1 | <u>652</u> | <u>15.1</u> | 658 | 15.0 | 534 | 18.4 | <u>535</u> | <u>18.4</u> | 536 | 18.4 |
| 470.lbm | 367 | 37.5 | <u>367</u> | <u>37.4</u> | 367 | 37.4 | 370 | 37.1 | 370 | 37.1 | <u>370</u> | <u>37.1</u> |
| 481.wrf | 432 | 25.9 | 431 | 25.9 | <u>431</u> | <u>25.9</u> | <u>401</u> | <u>27.9</u> | 400 | 27.9 | 402 | 27.8 |
| 482.sphinx3 | <u>951</u> | <u>20.5</u> | 940 | 20.7 | 960 | 20.3 | <u>1067</u> | <u>18.3</u> | 1064 | 18.3 | 1112 | 17.5 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stack size to unlimited prior to run
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to granularity=fine,scatter
KMP_STACKSIZE set to 200M

Platform Notes

Fan speed set to full Speed (ie. Enterprise Blade mode) with Smart Blade Console through CMM (Chassis Management Module)



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General Notes

Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502

The Acer AB460 F1, and Gateway GB460 F1 are electronically equivalent.
This result was measured on Gateway GB460 F1.

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
 416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
 434.zeusmp: -DSPEC_CPU_LP64
 435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
 437.leslie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
 447.dealII: -DSPEC_CPU_LP64
 450.soplex: -DSPEC_CPU_LP64
 453.povray: -DSPEC_CPU_LP64
 454.calculix: -DSPEC_CPU_LP64 -nofor_main
 459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
 482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

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Base Optimization Flags (Continued)

Fortran benchmarks:

`-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch`

Benchmarks using both Fortran and C:

`-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch`

Peak Compiler Invocation

C benchmarks:

`icc -m64`

C++ benchmarks:

`icpc -m64`

Fortran benchmarks:

`ifort -m64`

Benchmarks using both Fortran and C:

`icc -m64 ifort -m64`

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-ansi-alias`

470.lbm: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-parallel -ansi-alias -auto-ilp32`

482.sphinx3: `-xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32
-unroll2`

C++ benchmarks:

444.namd: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-fno-alias -auto-ilp32`

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Peak Optimization Flags (Continued)

447.dealIII: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias -scalar-rep- -auto-ilp32

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-malloc-options=3 -auto-ilp32

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch
-parallel

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -Ob0 -opt-prefetch -parallel

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-inline-calloc -opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32

436.cactusADM: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -opt-prefetch -parallel -auto-ilp32

454.calculix: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32

481.wrf: Same as 454.calculix



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The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100316.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100316.xml>

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