SPEC® CINT2006 Result

Dell Inc.

PowerEdge M910 (Intel Xeon L7545, 1.87 GHz)

SPECint®_rate2006 =   250

SPECint_rate_base2006 =   232

CPU2006 license: 55

Test sponsor: Dell Inc.

Tested by: Dell Inc.

Test date: May-2010

Hardware Availability: Mar-2010

Software Availability: Dec-2009

SPECint_rate_base2006 = 232

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Hardware

CPU Name:	Intel Xeon L7545
CPU Characteristics:	Intel Turbo Boost Technology up to 2.53 GHz
CPU MHz:	1867
FPU:	Integrated
CPU(s) enabled:	12 cores, 2 chips, 6 cores/chip, 2 threads/core
CPU(s) orderable:	2,4 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	18 MB I+D on chip per chip
Other Cache:	None
Memory:	128 GB (32 x 4 GB DDR3-1066 QR RDIMM, CL7, ECC, downclocked to 978 MHz)
Disk Subsystem:	1 x 146 GB 15000 RPM SAS
Other Hardware:	None

Software

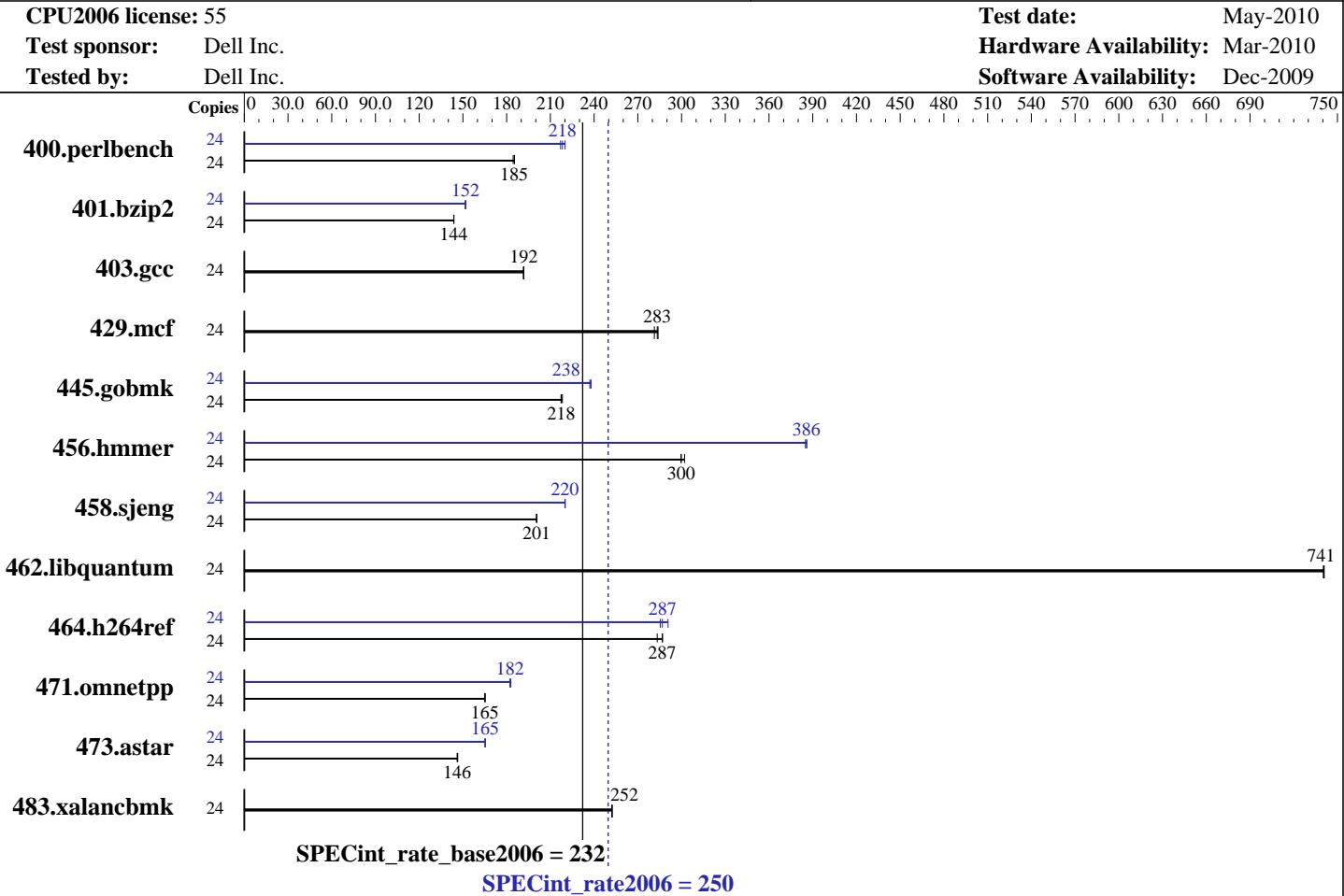|	
Operating System:	SUSE Linux Enterprise Server 11 (x86_64), Kernel 2.6.27.19-5-smp
Compiler:	Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l_cproc_p_11.1.064
Auto Parallel:	No
File System:	ext3
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V8.1

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Results Table

Benchmark	Copies	Base Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Peak Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	24	1265	185	**1266**	**185**	1271	184	24	1081	217	1065	220	**1074**	**218**
401.bzip2	24	1610	144	1612	144	**1612**	**144**	24	1524	152	1528	152	**1526**	**152**
403.gcc	24	1007	192	1009	191	**1008**	**192**	24	1007	192	1009	191	**1008**	**192**
429.mcf	24	771	284	**773**	**283**	778	281	24	771	284	**773**	**283**	778	281
445.gobmk	24	1159	217	1154	218	**1157**	**218**	24	1058	238	**1059**	**238**	1062	237
456.hmmer	24	747	300	741	302	**747**	**300**	24	**580**	**386**	582	385	580	386
458.sjeng	24	1447	201	**1447**	**201**	1449	200	24	**1320**	**220**	1319	220	1320	220
462.libquantum	24	671	741	672	740	**671**	**741**	24	671	741	672	740	**671**	**741**
464.h264ref	24	**1852**	**287**	1875	283	1851	287	24	1861	285	**1852**	**287**	1827	291
471.omnetpp	24	908	165	909	165	**908**	**165**	24	**823**	**182**	820	183	823	182
473.astar	24	**1153**	**146**	1150	146	1153	146	24	1019	165	**1020**	**165**	1020	165
483.xalancbmk	24	**656**	**252**	657	252	656	252	24	**656**	**252**	657	252	656	252

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
    The config file option 'submit' was used.
     numactl was used to bind copies to the cores
```

Operating System Notes

```
    'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
```

Platform Notes

```
    vm.zone_reclaim_mode = 1 in /etc/sysctl.conf file
    BIOS Settings:
    Power Management = Maximum Performance (Default = Active Power Controller)
```

General Notes

```
    Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502
```

Base Compiler Invocation

C benchmarks:
```
      icc  -m32
```

C++ benchmarks:
```
      icpc -m32
```

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Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:
`-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch`

C++ benchmarks:
`-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs`
`-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap`

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
`icc  -m32`

401.bzip2: `icc -m64`

456.hmmer: `icc -m64`

458.sjeng: `icc -m64`

C++ benchmarks (except as noted below):
`icpc -m32`

473.astar: `icpc -m64`

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
401.bzip2: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
458.sjeng: `-DSPEC_CPU_LP64`

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Peak Portability Flags (Continued)

```
462.libquantum: -DSPEC_CPU_LINUX
     473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
               -prof-use(pass 2) -ansi-alias

    401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
               -prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

      403.gcc: basepeak = yes

      429.mcf: basepeak = yes

    445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
               -ipo -no-prec-div -ansi-alias

    456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
               -ansi-alias -auto-ilp32

    458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
               -prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

   464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
               -prof-use(pass 2) -unroll2 -ansi-alias
```

C++ benchmarks:

```
   471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
               -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

     473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs
               -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64
```

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Peak Optimization Flags (Continued)

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100330.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100330.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Wed Jul 23 13:27:39 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 8 July 2010.