



# SPEC® CINT2006 Result

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## Fujitsu

SPECint®2006 = 35.4

## CELSIUS W380, Intel Core i5-680

SPECint\_base2006 = 32.2

CPU2006 license: 19

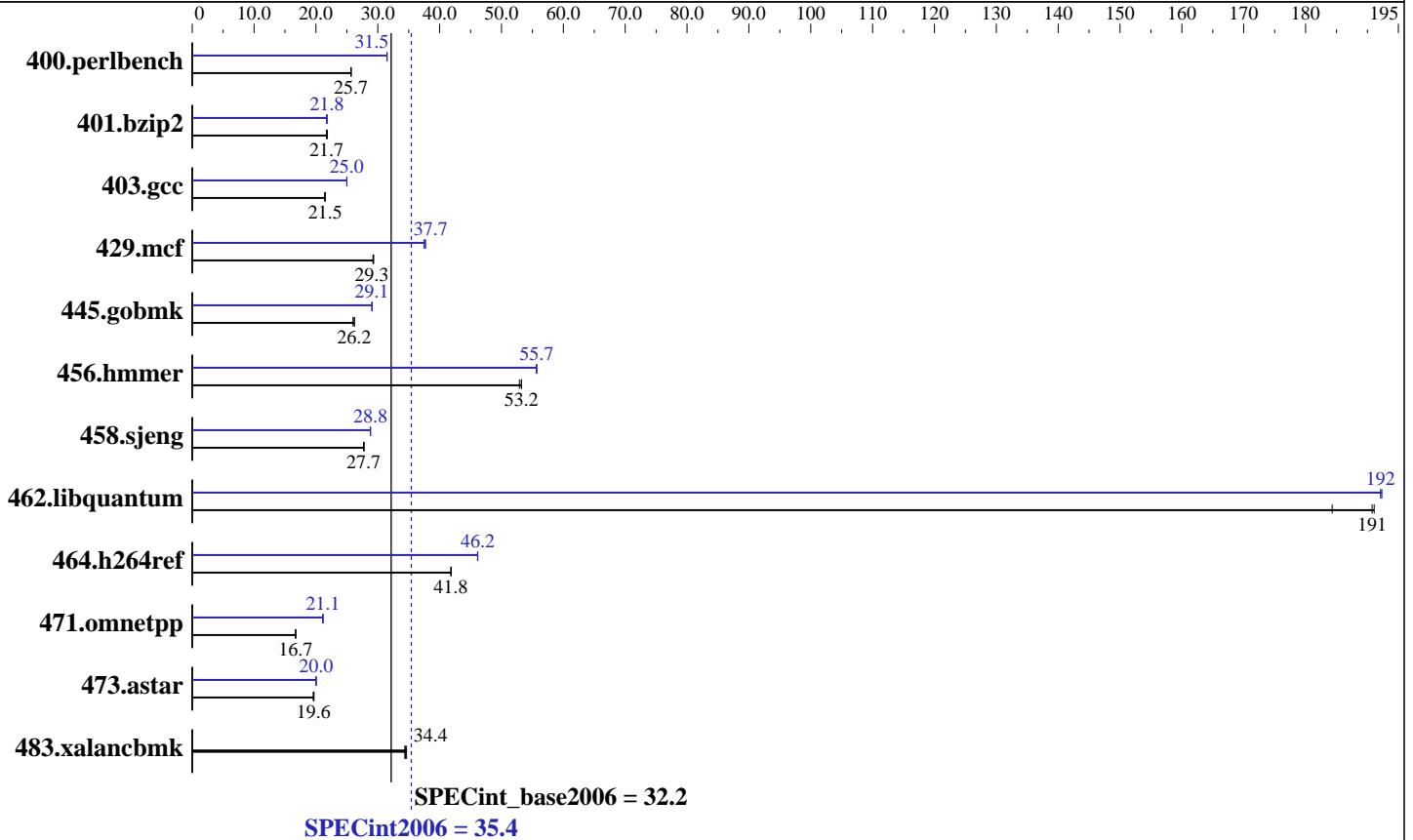
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Apr-2010

Hardware Availability: May-2010

Software Availability: Oct-2009



### Hardware

CPU Name: Intel Core i5-680  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.87 GHz  
 CPU MHz: 3600  
 FPU: Integrated  
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 4 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 8 GB (2x4 GB PC3-10600U, 2 rank, CL9)  
 Disk Subsystem: 1 x SATA II, 400 GB, 7200 rpm  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 11 (x86\_64), kernel 2.6.27.19-5-default  
 Compiler: Intel C++ Compiler for IA32 and Intel 64, Version 11.1 Build 20091012 Package ID: 1\_cproc\_p\_11.1.059  
 Auto Parallel: Yes  
 File System: ext3  
 System State: Multi-User Run Level 3  
 Base Pointers: 64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	<b><u>381</u></b>	<b><u>25.7</u></b>	380	25.7	381	25.7	310	31.5	310	31.5	<b><u>310</u></b>	<b><u>31.5</u></b>
401.bzip2	442	21.9	<b><u>444</u></b>	<b><u>21.7</u></b>	445	21.7	<b><u>443</u></b>	<b><u>21.8</u></b>	442	21.8	445	21.7
403.gcc	<b><u>374</u></b>	<b><u>21.5</u></b>	374	21.5	376	21.4	322	25.0	322	25.0	<b><u>322</u></b>	<b><u>25.0</u></b>
429.mcf	311	29.3	<b><u>311</u></b>	<b><u>29.3</u></b>	312	29.2	243	37.5	242	37.7	<b><u>242</u></b>	<b><u>37.7</u></b>
445.gobmk	<b><u>400</u></b>	<b><u>26.2</u></b>	404	26.0	400	26.2	<b><u>361</u></b>	<b><u>29.1</u></b>	361	29.1	361	29.0
456.hammer	176	52.9	<b><u>175</u></b>	<b><u>53.2</u></b>	175	53.2	168	55.6	<b><u>168</u></b>	<b><u>55.7</u></b>	168	55.7
458.sjeng	437	27.7	435	27.8	<b><u>436</u></b>	<b><u>27.7</u></b>	420	28.8	420	28.8	<b><u>420</u></b>	<b><u>28.8</u></b>
462.libquantum	112	184	<b><u>109</u></b>	<b><u>191</u></b>	108	191	108	192	<b><u>108</u></b>	<b><u>192</u></b>	108	192
464.h264ref	529	41.9	529	41.8	<b><u>529</u></b>	<b><u>41.8</u></b>	<b><u>479</u></b>	<b><u>46.2</u></b>	479	46.2	480	46.1
471.omnetpp	<b><u>374</u></b>	<b><u>16.7</u></b>	373	16.7	374	16.7	296	21.1	<b><u>296</u></b>	<b><u>21.1</u></b>	295	21.2
473.astar	356	19.7	<b><u>358</u></b>	<b><u>19.6</u></b>	359	19.6	350	20.1	351	20.0	<b><u>351</u></b>	<b><u>20.0</u></b>
483.xalancbmk	199	34.7	<b><u>200</u></b>	<b><u>34.4</u></b>	201	34.4	199	34.7	<b><u>200</u></b>	<b><u>34.4</u></b>	201	34.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

## Platform Notes

Hyper-Threading Technology = Disabled

## General Notes

OMP\_NUM\_THREADS set to number of cores  
KMP\_AFFINITY set to granularity=fine,scatter  
Binaries were compiled on SLES10 with Binutils 2.18.50.0.7.20080502

## Base Compiler Invocation

C benchmarks:  
icc -m64

C++ benchmarks:  
icpc -m64



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## Base Portability Flags

```

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

```

## Base Optimization Flags

C benchmarks:

```

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel
-par-runtime-control -opt-prefetch

```

C++ benchmarks:

```

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64

```

## Base Other Flags

C benchmarks:

```

403.gcc: -Dalloca=_alloca

```

## Peak Compiler Invocation

C benchmarks (except as noted below):

```

icc -m64

```

```

400.perlbench: icc -m32

```

```

429.mcf: icc -m32

```

```

445.gobmk: icc -m32

```

```

464.h264ref: icc -m32

```

C++ benchmarks (except as noted below):

```

icpc -m64

```

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## Peak Compiler Invocation (Continued)

471.omnetpp: icpc -m32

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64  
403.gcc: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -ansi-alias -opt-prefetch  
  
401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div -static(pass 2) -prof-use(pass 2)  
-auto-ilp32 -opt-prefetch -ansi-alias  
  
403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-alloc  
-opt-malloc-options=3 -auto-ilp32  
  
429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch  
  
445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2  
-ipo -no-prec-div -ansi-alias  
  
456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2  
-ansi-alias -auto-ilp32  
  
458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll4  
  
462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static -parallel  
-par-runtime-control -opt-prefetch  
-par-schedule-static=32768 -ansi-alias  
  
464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll2 -ansi-alias

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## Peak Optimization Flags (Continued)

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revD.20100511.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revD.20100511.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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