



# SPEC® CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Itaotec

### SPECfp®\_rate2006 = 242

### Servidor Itaotec MX203 (Intel Xeon X5680)

### SPECfp\_rate\_base2006 = 234

CPU2006 license: 9001

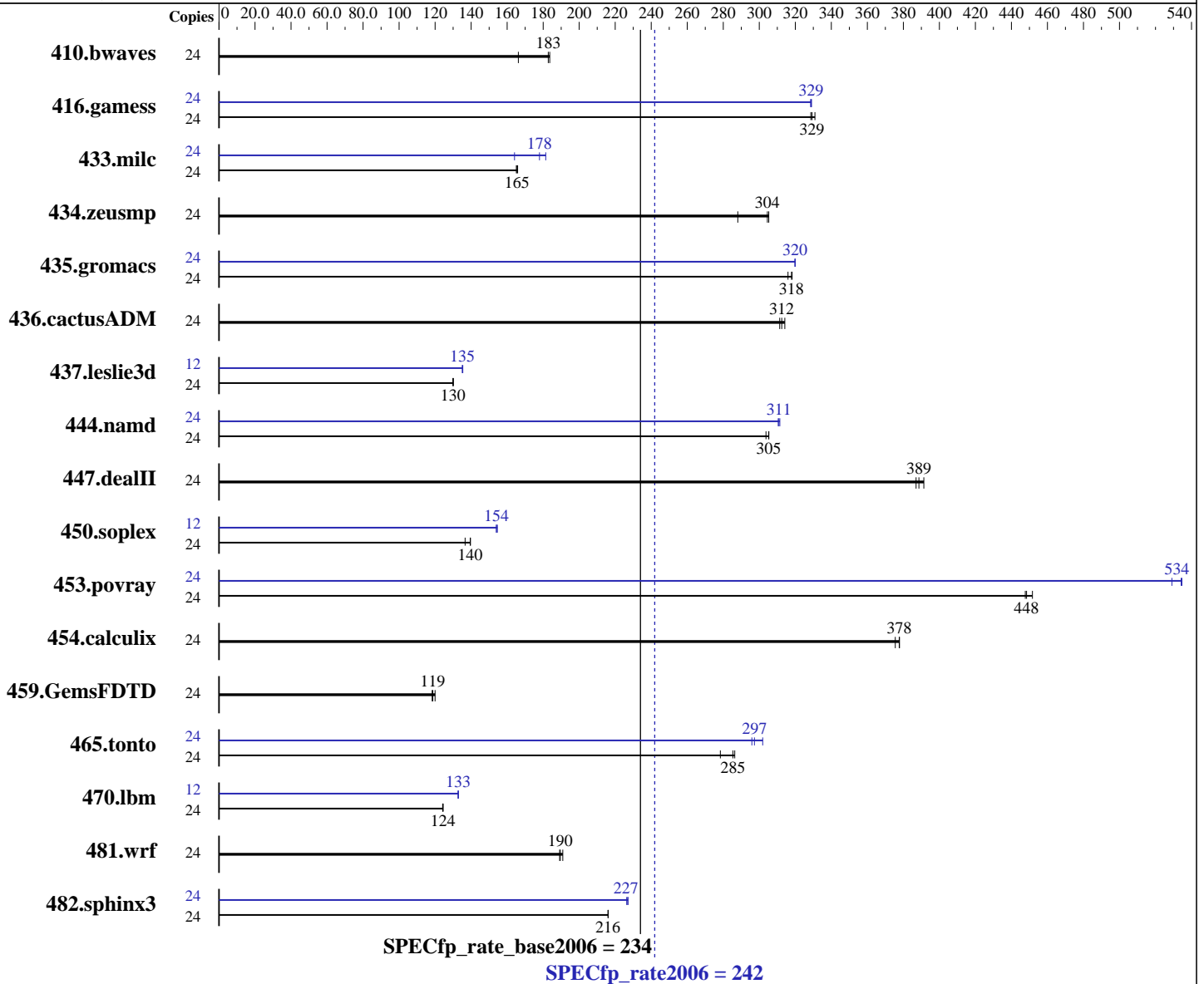
Test sponsor: Itaotec

Tested by: Itaotec

Test date: Mar-2010

Hardware Availability: Apr-2010

Software Availability: Feb-2010



#### Hardware

CPU Name: Intel Xeon X5680  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.60 GHz  
 CPU MHz: 3330  
 FPU: Integrated  
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core  
 CPU(s) orderable: 1, 2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

#### Software

Operating System: SUSE Linux Enterprise Server 11 (x86\_64), Kernel 2.6.27.19-5-smp  
 Compiler: Intel C++ and Fortran Compiler 11.1 for Linux Build 20100203 Package ID: l\_cproc\_p\_11.1.069, l\_cprof\_p\_11.1.069  
 Auto Parallel: No  
 File System: ReiserFS  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itaotec

SPECfp\_rate2006 = 242

Servidor Itaotec MX203 (Intel Xeon X5680)

SPECfp\_rate\_base2006 = 234

CPU2006 license: 9001  
Test sponsor: Itaotec  
Tested by: Itaotec

Test date: Mar-2010  
Hardware Availability: Apr-2010  
Software Availability: Feb-2010

L3 Cache: 12 MB I+D on chip per chip  
Other Cache: None  
Memory: 24 GB (6 x 4GB DDR3-1333, CL 9, ECC)  
Disk Subsystem: 1 x 160 GB SATA-2, 7200RPM  
Other Hardware: None

Peak Pointers: 32/64-bit  
Other Software: Binutils 2.18.50.0.7.20080502

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	24	1962	166	1776	184	<b>1784</b>	<b>183</b>	24	1962	166	1776	184	<b>1784</b>	<b>183</b>
416.gamess	24	1420	331	<b>1427</b>	<b>329</b>	1430	329	24	<b>1429</b>	<b>329</b>	1431	328	1428	329
433.milc	24	1329	166	1334	165	<b>1332</b>	<b>165</b>	24	1343	164	1214	181	<b>1238</b>	<b>178</b>
434.zeusmp	24	715	305	758	288	<b>717</b>	<b>304</b>	24	715	305	758	288	<b>717</b>	<b>304</b>
435.gromacs	24	538	318	<b>539</b>	<b>318</b>	542	316	24	536	320	536	320	<b>536</b>	<b>320</b>
436.cactusADM	24	913	314	921	311	<b>918</b>	<b>312</b>	24	913	314	921	311	<b>918</b>	<b>312</b>
437.leslie3d	24	1734	130	<b>1736</b>	<b>130</b>	1737	130	12	835	135	<b>835</b>	<b>135</b>	833	135
444.namd	24	630	305	634	304	<b>631</b>	<b>305</b>	24	618	311	620	311	<b>619</b>	<b>311</b>
447.dealII	24	709	387	702	391	<b>706</b>	<b>389</b>	24	709	387	702	391	<b>706</b>	<b>389</b>
450.soplex	24	1434	140	<b>1435</b>	<b>140</b>	1465	137	12	<b>650</b>	<b>154</b>	647	155	650	154
453.povray	24	<b>285</b>	<b>448</b>	283	452	285	448	24	241	529	<b>239</b>	<b>534</b>	239	535
454.calculix	24	524	378	527	376	<b>524</b>	<b>378</b>	24	524	378	527	376	<b>524</b>	<b>378</b>
459.GemsFDTD	24	2122	120	<b>2145</b>	<b>119</b>	2151	118	24	2122	120	<b>2145</b>	<b>119</b>	2151	118
465.tonto	24	825	286	848	278	<b>828</b>	<b>285</b>	24	<b>794</b>	<b>297</b>	798	296	782	302
470.lbm	24	<b>2651</b>	<b>124</b>	2655	124	2650	124	12	1241	133	<b>1241</b>	<b>133</b>	1242	133
481.wrf	24	1404	191	1417	189	<b>1415</b>	<b>190</b>	24	1404	191	1417	189	<b>1415</b>	<b>190</b>
482.sphinx3	24	2164	216	<b>2165</b>	<b>216</b>	2165	216	24	2066	226	2060	227	<b>2061</b>	<b>227</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.

## General Notes

This result was measured on the Servidor Itaotec MX203.  
The Servidor Itaotec MX223 and the Servidor Itaotec MX203 are electronically equivalent.



# SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itaotec

SPECfp\_rate2006 = 242

Servidor Itaotec MX203 (Intel Xeon X5680)

SPECfp\_rate\_base2006 = 234

CPU2006 license: 9001  
Test sponsor: Itaotec  
Tested by: Itaotec

Test date: Mar-2010  
Hardware Availability: Apr-2010  
Software Availability: Feb-2010

## Base Compiler Invocation

C benchmarks:  
icc -m64

C++ benchmarks:  
icpc -m64

Fortran benchmarks:  
ifort -m64

Benchmarks using both Fortran and C:  
icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -static

C++ benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -static

Fortran benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -static

Benchmarks using both Fortran and C:  
-xSSE4.2 -ipo -O3 -no-prec-div -static



# SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itaotec

SPECfp\_rate2006 = 242

Servidor Itaotec MX203 (Intel Xeon X5680)

SPECfp\_rate\_base2006 = 234

CPU2006 license: 9001  
Test sponsor: Itaotec  
Tested by: Itaotec

Test date: Mar-2010  
Hardware Availability: Apr-2010  
Software Availability: Feb-2010

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-fno-alias -opt-prefetch

470.lbm: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-opt-malloc-options=3 -ansi-alias -auto-ilp32

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itautec

SPECfp\_rate2006 = 242

Servidor Itautec MX203 (Intel Xeon X5680)

SPECfp\_rate\_base2006 = 234

CPU2006 license: 9001  
Test sponsor: Itautec  
Tested by: Itautec

Test date: Mar-2010  
Hardware Availability: Apr-2010  
Software Availability: Feb-2010

## Peak Optimization Flags (Continued)

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2

### C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-fno-alias -auto-ilp32

447.deallI: basepeak = yes

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-opt-malloc-options=3

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-unroll4 -ansi-alias

### Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-unroll2 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: -xSSE4.2 -ipo -O3 -no-prec-div -static

459.GemsFDTD: basepeak = yes

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-unroll4 -auto -inline-calloc -opt-malloc-options=3

### Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes



# SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itautec

SPECfp\_rate2006 = 242

Servidor Itautec MX203 (Intel Xeon X5680)

SPECfp\_rate\_base2006 = 234

CPU2006 license: 9001

Test date: Mar-2010

Test sponsor: Itautec

Hardware Availability: Apr-2010

Tested by: Itautec

Software Availability: Feb-2010

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Itautec-Intel-ic11.1-linux64-revE.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Itautec-Intel-ic11.1-linux64-revE.xml>

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.1.  
Report generated on Wed Jul 23 09:53:41 2014 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 14 April 2010.