



SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

SPECint®2006 = 43.3

IBM System x3650 M3 (Intel Xeon X5677)

SPECint_base2006 = 40.0

CPU2006 license: 11

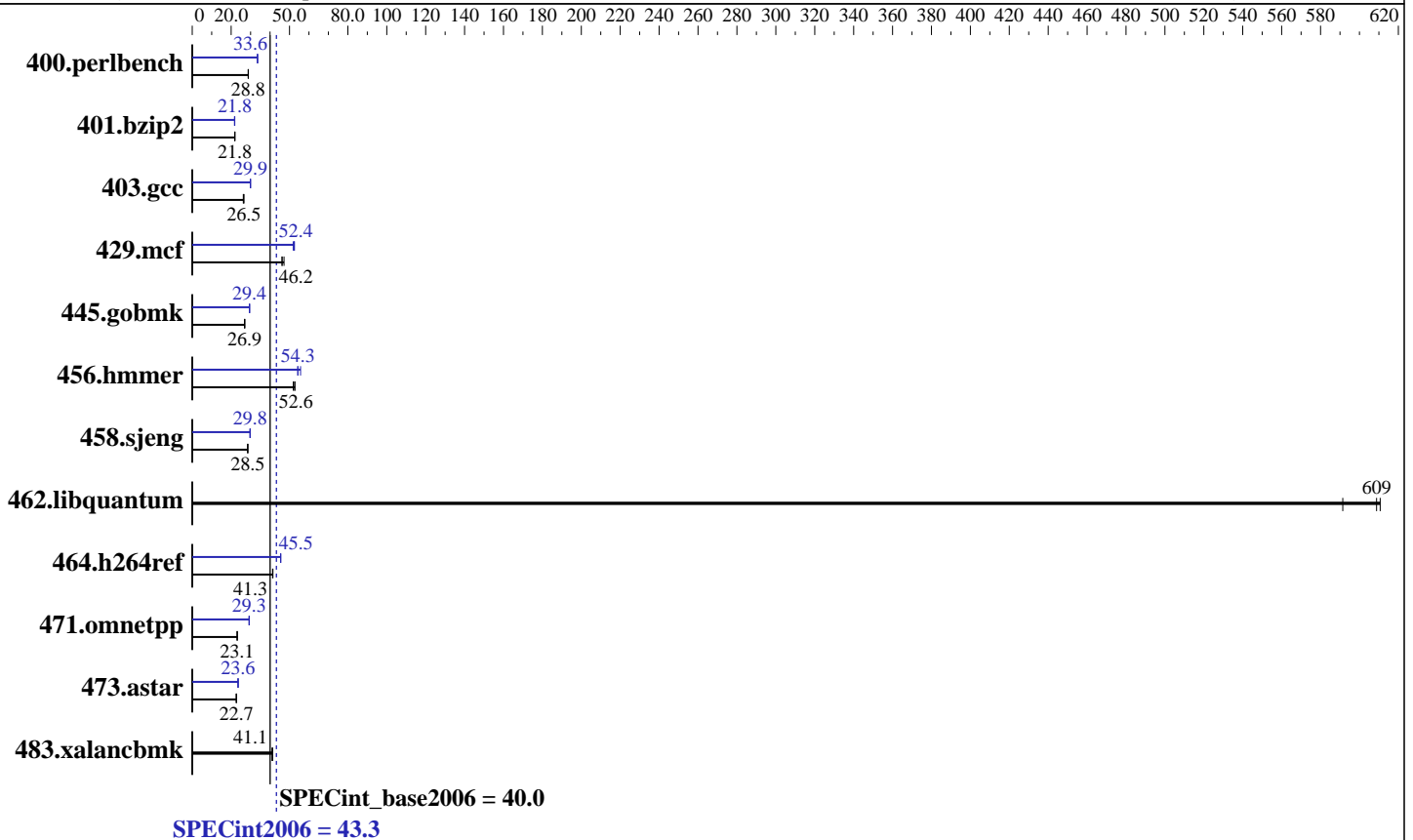
Test date: Mar-2010

Test sponsor: IBM Corporation

Hardware Availability: Apr-2010

Tested by: IBM Corporation

Software Availability: Jan-2010



Hardware

CPU Name: Intel Xeon X5677
 CPU Characteristics: Intel Turbo Boost Technology up to 3.73 GHz
 CPU MHz: 3467
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 12 MB I+D on chip per chip
 Other Cache: None
 Memory: 48 GB (12 x 4 GB PC3-10600R-ECC)
 Disk Subsystem: 1 x 73 GB SAS, 15000RPM
 Other Hardware: None

Software

Operating System: SuSe Linux Enterprise Server 11 (x86_64)
 Kernel 2.6.27.19-5-default
 Compiler: Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1
 Build 20091130 Package ID: l_cproc_p_11.1.064
 Auto Parallel: Yes
 File System: ext3
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1
 Binutils 2.18.50.0.7.20080502



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

SPECint2006 = 43.3

IBM System x3650 M3 (Intel Xeon X5677)

SPECint_base2006 = 40.0

CPU2006 license: 11

Test date: Mar-2010

Test sponsor: IBM Corporation

Hardware Availability: Apr-2010

Tested by: IBM Corporation

Software Availability: Jan-2010

Results Table

| Benchmark | Base | | | | | | Peak | | | | | |
|----------------|------------|-------------|-------------|-------------|------------|-------------|------------|-------------|-------------|-------------|------------|-------------|
| | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 339 | 28.9 | <u>339</u> | <u>28.8</u> | 340 | 28.7 | <u>291</u> | <u>33.6</u> | 292 | 33.5 | 291 | 33.6 |
| 401.bzip2 | <u>442</u> | <u>21.8</u> | 438 | 22.0 | 442 | 21.8 | 442 | 21.9 | <u>442</u> | <u>21.8</u> | 445 | 21.7 |
| 403.gcc | 303 | 26.5 | <u>304</u> | <u>26.5</u> | 307 | 26.3 | 269 | 29.9 | 269 | 30.0 | <u>269</u> | <u>29.9</u> |
| 429.mcf | 193 | 47.2 | 198 | 46.1 | <u>197</u> | <u>46.2</u> | 174 | 52.5 | 176 | 51.9 | <u>174</u> | <u>52.4</u> |
| 445.gobmk | <u>390</u> | <u>26.9</u> | 387 | 27.1 | 390 | 26.9 | <u>356</u> | <u>29.4</u> | 356 | 29.4 | 356 | 29.5 |
| 456.hammer | <u>177</u> | <u>52.6</u> | 177 | 52.8 | 180 | 51.9 | 168 | 55.7 | <u>172</u> | <u>54.3</u> | 172 | 54.2 |
| 458.sjeng | <u>424</u> | <u>28.5</u> | 425 | 28.5 | 423 | 28.6 | 406 | 29.8 | <u>407</u> | <u>29.8</u> | 407 | 29.7 |
| 462.libquantum | 33.9 | 611 | <u>34.0</u> | <u>609</u> | 35.0 | 591 | 33.9 | 611 | <u>34.0</u> | <u>609</u> | 35.0 | 591 |
| 464.h264ref | 535 | 41.3 | <u>535</u> | <u>41.3</u> | 533 | 41.5 | <u>487</u> | <u>45.5</u> | 487 | 45.5 | 487 | 45.5 |
| 471.omnetpp | 271 | 23.1 | <u>270</u> | <u>23.1</u> | 270 | 23.2 | <u>213</u> | <u>29.3</u> | 213 | 29.3 | 214 | 29.2 |
| 473.astar | 310 | 22.6 | <u>309</u> | <u>22.7</u> | 309 | 22.7 | 299 | 23.5 | <u>298</u> | <u>23.6</u> | 297 | 23.7 |
| 483.xalancbmk | 168 | 41.1 | 167 | 41.3 | <u>168</u> | <u>41.1</u> | 168 | 41.1 | 167 | 41.3 | <u>168</u> | <u>41.1</u> |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Platform Notes

Turbo Mode Enable

General Notes

'ulimit -s unlimited' was used to set the stack size to unlimited prior to run
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to granularity=fine,scatter

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64

401.bzip2: -DSPEC_CPU_LP64

403.gcc: -DSPEC_CPU_LP64

429.mcf: -DSPEC_CPU_LP64

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

SPECint2006 = 43.3

IBM System x3650 M3 (Intel Xeon X5677)

SPECint_base2006 = 40.0

CPU2006 license: 11

Test date: Mar-2010

Test sponsor: IBM Corporation

Hardware Availability: Apr-2010

Tested by: IBM Corporation

Software Availability: Jan-2010

Base Portability Flags (Continued)

445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

400.perlbench: icc -m32

429.mcf: icc -m32

445.gobmk: icc -m32

464.h264ref: icc -m32

C++ benchmarks (except as noted below):

icpc -m64

471.omnetpp: icpc -m32



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

SPECint2006 = 43.3

IBM System x3650 M3 (Intel Xeon X5677)

SPECint_base2006 = 40.0

CPU2006 license: 11

Test date: Mar-2010

Test sponsor: IBM Corporation

Hardware Availability: Apr-2010

Tested by: IBM Corporation

Software Availability: Jan-2010

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
 401.bzip2: -DSPEC_CPU_LP64
 403.gcc: -DSPEC_CPU_LP64
 456.hmmer: -DSPEC_CPU_LP64
 458.sjeng: -DSPEC_CPU_LP64
 462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
 473.astar: -DSPEC_CPU_LP64
 483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
 -prof-use(pass 2) -ansi-alias -opt-prefetch

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div -static(pass 2) -prof-use(pass 2)
 -auto-ilp32 -opt-prefetch -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc
 -opt-malloc-options=3 -auto-ilp32

429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
 -ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
 -ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
 -prof-use(pass 2) -unroll4

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
 -prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
 -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

SPECint2006 = 43.3

IBM System x3650 M3 (Intel Xeon X5677)

SPECint_base2006 = 40.0

CPU2006 license: 11

Test date: Mar-2010

Test sponsor: IBM Corporation

Hardware Availability: Apr-2010

Tested by: IBM Corporation

Software Availability: Jan-2010

Peak Optimization Flags (Continued)

```
473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
           -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
           -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs
           -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64
```

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100330.00.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100330.00.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Wed Jul 23 05:22:31 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 30 March 2010.