



# SPEC® CINT2006 Result

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## Acer Incorporated

**SPECint®2006 = 38.6**

## Acer AT350 F1 (Intel Xeon X5570)

**SPECint\_base2006 = 34.5**

CPU2006 license: 97

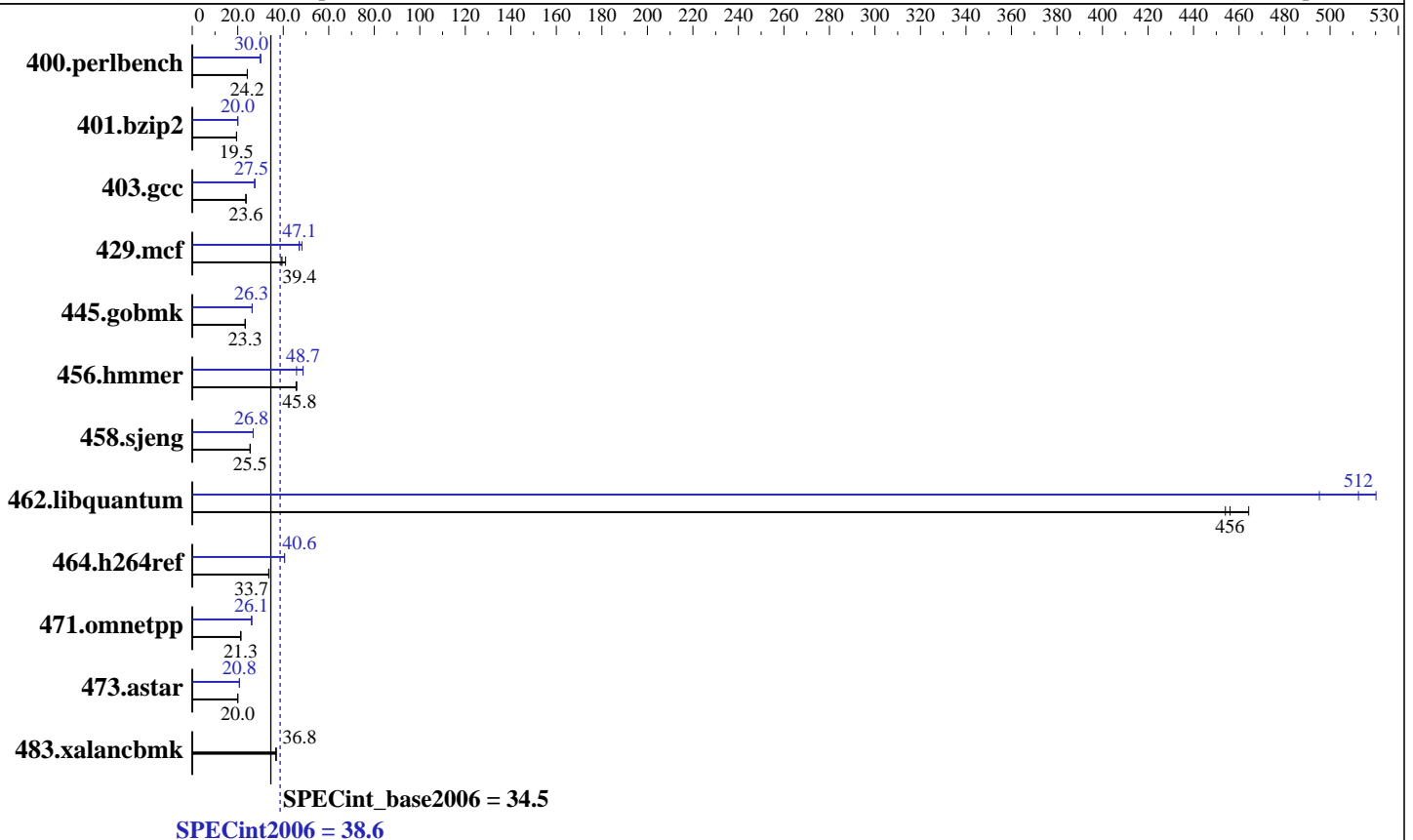
Test sponsor: Acer Incorporated

Tested by: Acer Incorporated

Test date: Jan-2010

Hardware Availability: Jan-2010

Software Availability: Sep-2009



### Hardware

CPU Name: Intel Xeon X5570  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.33 GHz  
 CPU MHz: 2933  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core  
 CPU(s) orderable: 1, 2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 8 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 24 GB (6 x 4GB DDR3-1333 RDIMM)  
 Disk Subsystem: 500 GB SATA II, 7200 RPM  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 11 (x86\_64)  
 Kernel 2.6.27.19-5  
 Compiler: Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1  
 Build 20091012 Package ID: l\_cproc\_p\_11.1.059  
 Auto Parallel: Yes  
 File System: ReiserFS  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1  
 Binutils 2.18.50.0.7.20080502



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	404	24.2	<b>404</b>	<b>24.2</b>	403	24.2	325	30.0	326	30.0	<b>325</b>	<b>30.0</b>
401.bzip2	<b>495</b>	<b>19.5</b>	494	19.5	496	19.4	<b>482</b>	<b>20.0</b>	482	20.0	483	20.0
403.gcc	340	23.7	341	23.6	<b>341</b>	<b>23.6</b>	293	27.5	<b>293</b>	<b>27.5</b>	293	27.5
429.mcf	222	41.0	<b>231</b>	<b>39.4</b>	234	39.0	194	47.0	<b>194</b>	<b>47.1</b>	189	48.3
445.gobmk	450	23.3	451	23.3	<b>451</b>	<b>23.3</b>	398	26.3	<b>398</b>	<b>26.3</b>	398	26.3
456.hammer	<b>204</b>	<b>45.8</b>	204	45.6	203	45.9	192	48.7	204	45.8	<b>192</b>	<b>48.7</b>
458.sjeng	<b>475</b>	<b>25.5</b>	475	25.5	476	25.4	<b>452</b>	<b>26.8</b>	452	26.8	451	26.8
462.libquantum	<b>45.4</b>	<b>456</b>	44.6	464	45.6	454	41.8	495	<b>40.4</b>	<b>512</b>	39.8	520
464.h264ref	<b>657</b>	<b>33.7</b>	659	33.6	656	33.7	<b>546</b>	<b>40.6</b>	546	40.5	545	40.6
471.omnetpp	<b>293</b>	<b>21.3</b>	293	21.3	293	21.3	240	26.0	239	26.1	<b>240</b>	<b>26.1</b>
473.astar	350	20.0	<b>351</b>	<b>20.0</b>	352	19.9	339	20.7	<b>338</b>	<b>20.8</b>	337	20.8
483.xalancbmk	188	36.8	<b>187</b>	<b>36.8</b>	187	36.9	188	36.8	<b>187</b>	<b>36.8</b>	187	36.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

'ulimit -s unlimited' was set for stacksize unlimited

## General Notes

OMP\_NUM\_THREADS set to number of cores  
KMP\_AFFINITY set to granularity=fine,scatter  
This result was measured on the Gateway GT350 F1.  
The Acer AT350 F1 and Gateway GT350 F1 are electronically equivalent.

## Base Compiler Invocation

C benchmarks:  
icc -m64  
C++ benchmarks:  
icpc -m64

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64  
403.gcc: -DSPEC\_CPU\_LP64

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## Base Portability Flags (Continued)

```

429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

```

## Base Optimization Flags

C benchmarks:

```

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel
-par-runtime-control -opt-prefetch

```

C++ benchmarks:

```

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.icl1.1/libicl1.1-64bit -lsmartheap64

```

## Base Other Flags

C benchmarks:

```

403.gcc: -Dalloca=_alloca

```

## Peak Compiler Invocation

C benchmarks (except as noted below):

```

icc -m64

```

```

400.perlbench: icc -m32

```

```

429.mcf: icc -m32

```

```

445.gobmk: icc -m32

```

```

464.h264ref: icc -m32

```

C++ benchmarks (except as noted below):

```

icpc -m64

```

```

471.omnetpp: icpc -m32

```



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## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_X64  
 401.bzip2: -DSPEC\_CPU\_LP64  
 403.gcc: -DSPEC\_CPU\_LP64  
 456.hmmer: -DSPEC\_CPU\_LP64  
 458.sjeng: -DSPEC\_CPU\_LP64  
 462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
 473.astar: -DSPEC\_CPU\_LP64  
 483.xalancbmk: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
 -prof-use(pass 2) -ansi-alias -opt-prefetch

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div -static(pass 2) -prof-use(pass 2)  
 -auto-ilp32 -opt-prefetch -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-alloc  
 -opt-malloc-options=3 -auto-ilp32

429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2  
 -ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2  
 -ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
 -prof-use(pass 2) -unroll4

462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static -parallel  
 -par-runtime-control -opt-prefetch  
 -par-schedule-static=32768 -ansi-alias

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
 -prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs

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## Peak Optimization Flags (Continued)

471.omnetpp (continued):  
 -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs  
 -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:  
 403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at  
<http://www.spec.org/cpu2006/flags/Intel-ic11.1-int-linux64-revH.html>

You can also download the XML flags source by saving the following link:  
<http://www.spec.org/cpu2006/flags/Intel-ic11.1-int-linux64-revH.xml>

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For questions about this result, please contact the tester.  
 For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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