



SPEC® CFP2006 Result

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Intel Corporation

SPECfp®_rate2006 = 51.9

Intel DH57JG motherboard (Intel Core i5-661)

SPECfp_rate_base2006 = 50.7

CPU2006 license: 13

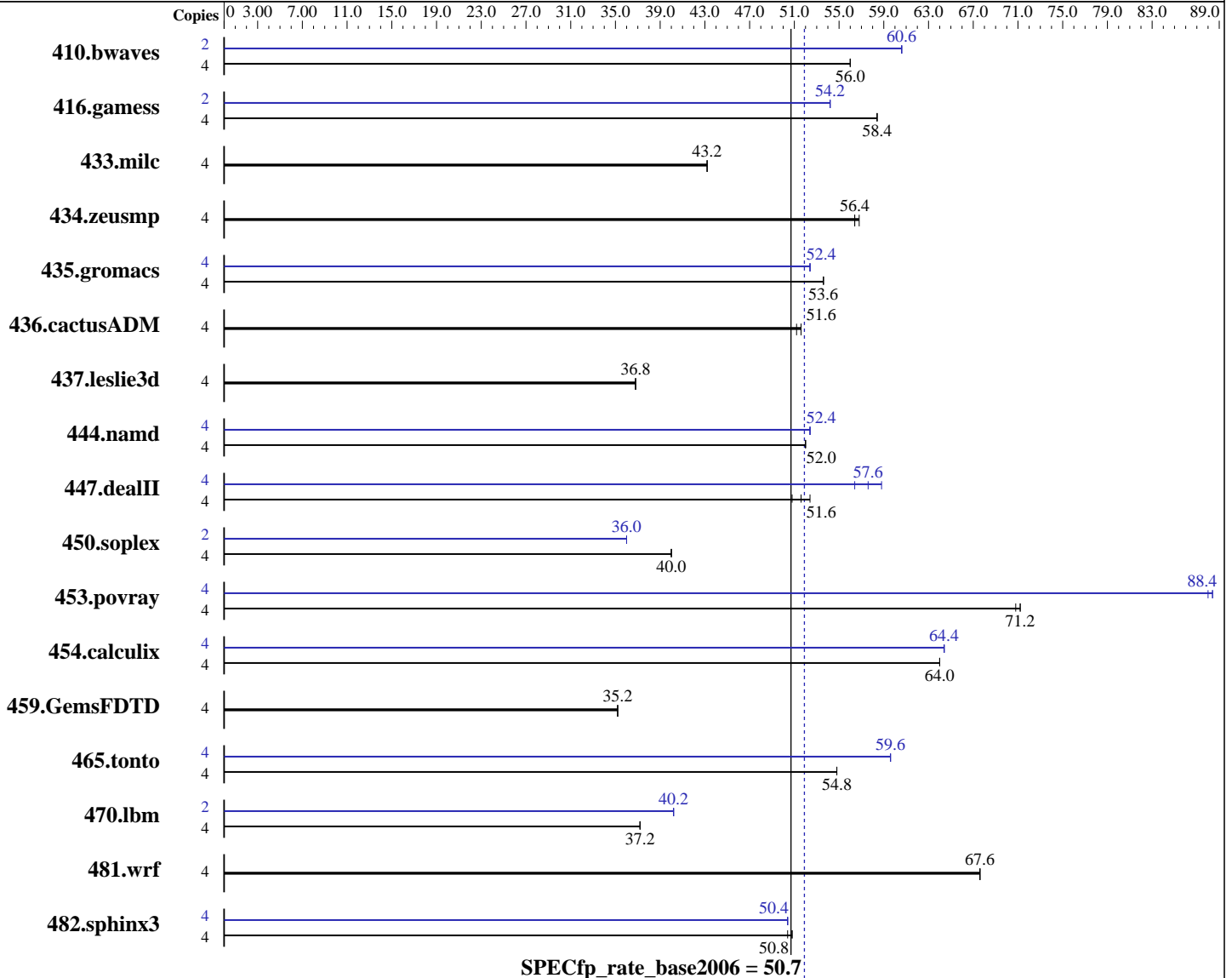
Test sponsor: Intel Corporation

Tested by: Intel Corporation

Test date: Jan-2009

Hardware Availability: Mar-2010

Software Availability: Oct-2009



Hardware

CPU Name: Intel Core i5-661
 CPU Characteristics: Intel Turbo Boost Technology up to 3.6 GHz
 CPU MHz: 3333
 FPU: Integrated
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip, 2 threads/core
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: Windows Vista Ultimate w/ SP1 (64-bit)
 Compiler: Intel C++ Compiler Professional 11.1 for Intel 64 Build 20090903 Package ID: w_cproc_p_11.1.045
 Intel Visual Fortran Compiler Professional 11.1 for Intel 64 Build 20090903 Package ID: w_cproc_p_11.1.045, w_cprof_p_11.1.045
 Microsoft Visual Studio 2008 Professional SP1 (for libraries)
 Auto Parallel: No

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L3 Cache: 4 MB I+D on chip per chip
 Other Cache: None
 Memory: 4 GB (2x2GB Micron MT16JTF25664AZ-1G4 DDR3-1333 CL9)
 Disk Subsystem: Intel X25-M 80GB SSD
 Other Hardware: None

File System: NTFS
 System State: Default
 Base Pointers: 64-bit
 Peak Pointers: 64-bit
 Other Software: None
 SmartHeap Library Version 8.1 from <http://www.microquill.com/>

Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio		
410.bwaves	4	973	56.0	968	56.0	969	56.0	2	449	60.6	449	60.6	449	60.6		
416.gamess	4	1346	58.4	1343	58.4	1344	58.4	2	723	54.2	723	54.2	723	54.2		
433.milc	4	852	43.2	848	43.2	847	43.2	4	852	43.2	848	43.2	847	43.2		
434.zeusmp	4	645	56.4	644	56.4	643	56.8	4	645	56.4	644	56.4	643	56.8		
435.gromacs	4	533	53.6	532	53.6	532	53.6	4	545	52.4	546	52.4	545	52.4		
436.cactusADM	4	934	51.2	928	51.6	924	51.6	4	934	51.2	928	51.6	924	51.6		
437.leslie3d	4	1021	36.8	1019	36.8	1018	36.8	4	1021	36.8	1019	36.8	1018	36.8		
444.namd	4	617	52.0	619	52.0	617	52.0	4	614	52.4	614	52.4	614	52.4		
447.dealII	4	876	52.4	897	50.8	890	51.6	4	778	58.8	794	57.6	809	56.4		
450.soplex	4	834	40.0	835	40.0	835	40.0	2	464	36.0	464	36.0	464	36.0		
453.povray	4	301	70.8	299	71.2	300	71.2	4	241	88.4	242	88.0	241	88.4		
454.calculix	4	516	64.0	516	64.0	517	64.0	4	511	64.4	512	64.4	513	64.4		
459.GemsFDTD	4	1204	35.2	1202	35.2	1203	35.2	4	1204	35.2	1202	35.2	1203	35.2		
465.tonto	4	716	54.8	718	54.8	716	54.8	4	659	59.6	660	59.6	658	59.6		
470.lbm	4	1477	37.2	1477	37.2	1476	37.2	2	684	40.2	683	40.2	684	40.2		
481.wrf	4	662	67.6	661	67.6	663	67.6	4	662	67.6	661	67.6	663	67.6		
482.sphinx3	4	1541	50.8	1540	50.8	1542	50.4	4	1542	50.4	1544	50.4	1545	50.4		

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
Windows start command was used to bind copies to processors

General Notes

Tested systems can be used with Shin-G ATX case,
PC Power and Cooling 1200W power supply
System was configured with nVidia GTX 280 discrete graphics card



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Base Compiler Invocation

C benchmarks:

icl -Qvc9 -Qstd=c99

C++ benchmarks:

icl -Qvc9

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icl -Qvc9 -Qstd=c99 ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_P64 /Qlowercase
 416.gamess: -DSPEC_CPU_P64
 433.milc: -DSPEC_CPU_P64
 434.zeusmp: -DSPEC_CPU_P64
 435.gromacs: -DSPEC_CPU_P64
 436.cactusADM: -DSPEC_CPU_P64 -Qlowercase /assume:underscore
 437.lelie3d: -DSPEC_CPU_P64
 444.namd: -DSPEC_CPU_P64 /TP
 447.dealII: -DSPEC_CPU_P64 -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
 450.soplex: -DSPEC_CPU_P64
 453.povray: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
 454.calculix: -DSPEC_CPU_P64 -DSPEC_CPU_NOZMODIFIER -Qlowercase
 459.GemsFDTD: -DSPEC_CPU_P64
 465.tonto: -DSPEC_CPU_P64
 470.lbm: -DSPEC_CPU_P64
 481.wrf: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
 482.sphinx3: -DSPEC_CPU_P64

Base Optimization Flags

C benchmarks:

-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qauto-ilp32
/F1000000000 -link /FORCE:MULTIPLE

C++ benchmarks:

-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qcxx-features
-Qauto-ilp32 /F1000000000 shlw64M.lib -link /FORCE:MULTIPLE

Fortran benchmarks:

-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qopt-prefetch /F1000000000
-link /FORCE:MULTIPLE

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Base Optimization Flags (Continued)

Benchmarks using both Fortran and C:

```
-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qauto-ilp32
/F1000000000 -link /FORCE:MULTIPLE
```

Peak Compiler Invocation

C benchmarks:

```
icl -Qvc9 -Qstd=c99
```

C++ benchmarks:

```
icl -Qvc9
```

Fortran benchmarks:

```
ifort
```

Benchmarks using both Fortran and C:

```
icl -Qvc9 -Qstd=c99 ifort
```

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

```
433.milc: basepeak = yes
```

```
470.lbm: -QxSSE4.2 -Qipo -O3 -Qprec-div- -Qopt-prefetch
-Qauto-ilp32 /F1000000000 -link /FORCE:MULTIPLE
```

```
482.sphinx3: -QxSSE4.2 -Qipo -O3 -Qprec-div- -Qunroll2 -Qauto-ilp32
/F1000000000 -link /FORCE:MULTIPLE
```

C++ benchmarks:

```
444.namd: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Oa -Qauto-ilp32 /F1000000000
sh1W64M.lib -link /FORCE:MULTIPLE
```

```
447.dealII: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias
-Qscalar-rep- -Qauto-ilp32 /F1000000000 sh1W64M.lib
-link /FORCE:MULTIPLE
```

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Peak Optimization Flags (Continued)

450.soplex: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qauto-ilp32 /F1000000000 sh1W64M.lib
-link /FORCE:MULTIPLE

453.povray: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qunroll4 -Qansi-alias -Qauto-ilp32
/F1000000000 sh1W64M.lib -link /FORCE:MULTIPLE

Fortran benchmarks:

410.bwaves: -QxSSE4.2 -Qipo -O3 -Qprec-div- -Qopt-prefetch
/F1000000000 -link /FORCE:MULTIPLE

416.gamess: Same as 410.bwaves

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qunroll4 -Qauto /F1000000000
-link /FORCE:MULTIPLE

Benchmarks using both Fortran and C:

435.gromacs: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qopt-prefetch -Qauto-ilp32
/F1000000000 -link /FORCE:MULTIPLE

436.cactusADM: basepeak = yes

454.calculix: -QxSSE4.2 -Qipo -O3 -Qprec-div- -Qauto-ilp32 /F1000000000
-link /FORCE:MULTIPLE

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-winx64-revA.20100302.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-winx64-revA.20100302.xml>



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