



# SPEC® CINT2006 Result

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## Bull SAS

SPECint®2006 = 30.1

BL265  
(Intel Xeon E5540, 2.53 GHz)

SPECint\_base2006 = 26.9

CPU2006 license: 20

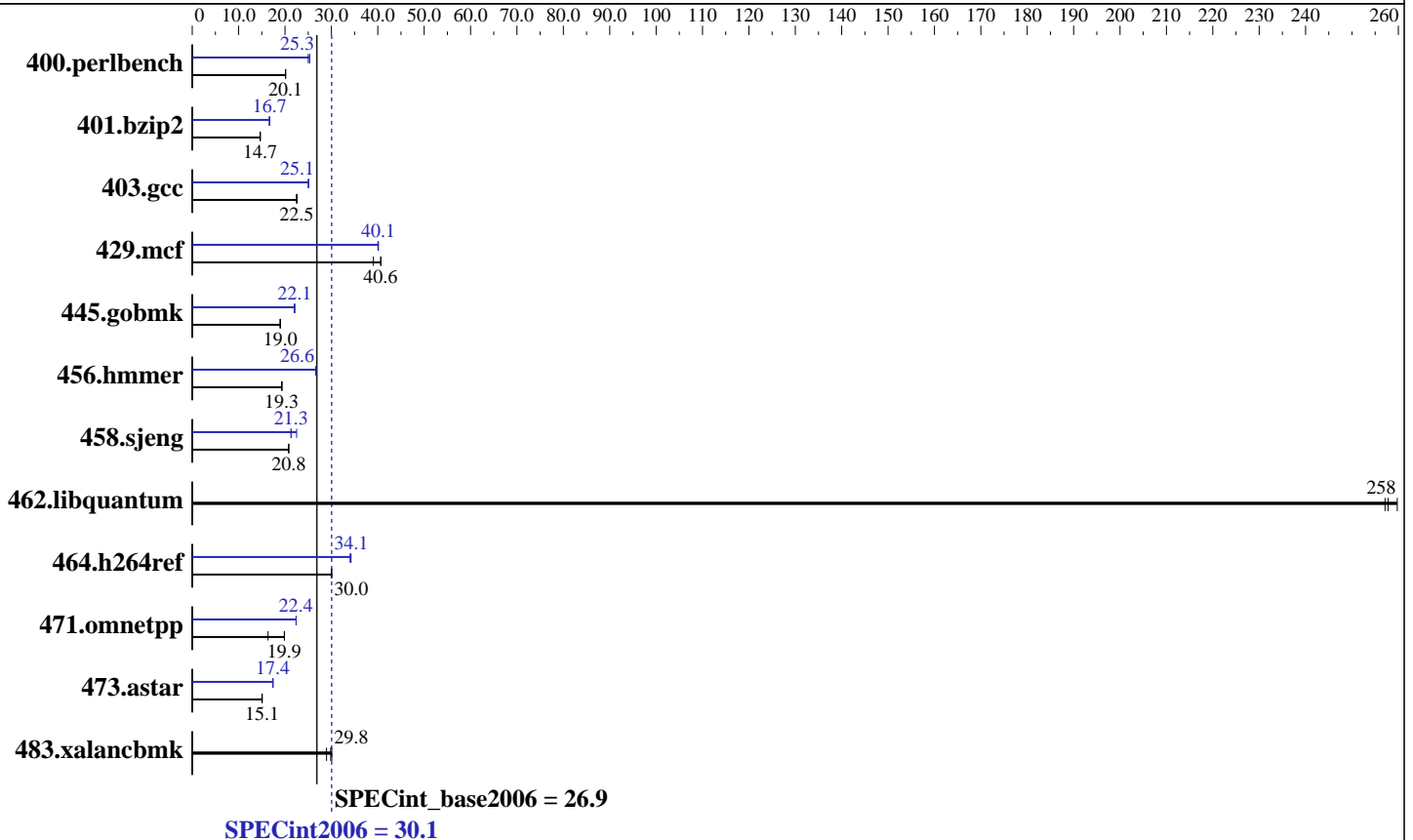
Test date: Jan-2010

Test sponsor: Bull SAS

Hardware Availability: May-2009

Tested by: Bull SAS

Software Availability: Feb-2009



### Hardware

CPU Name: Intel Xeon E5540  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.80 GHz  
 CPU MHz: 2533  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 8 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 24 GB (6 x 4 GB PC3-10600R, 2 Rank, CL9-9-9, ECC, running at 1066 MHz)  
 Disk Subsystem: 1 x 73 GB SAS, 10000 RPM  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 10 (x86\_64) SP2, SP2 with patch Linux kernel 20090119, Kernel 2.6.16.60-0.34-smp  
 Compiler: Intel C++ Compiler 11.0 for Linux Build 20090131 Package ID: l\_cproc\_p\_11.0.080  
 Auto Parallel: Yes  
 File System: ReiserFS  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	485	20.2	486	20.1	<b>486</b>	<b>20.1</b>	<b>387</b>	<b>25.3</b>	386	25.3	391	25.0
401.bzip2	660	14.6	658	14.7	<b>658</b>	<b>14.7</b>	580	16.6	<b>579</b>	<b>16.7</b>	579	16.7
403.gcc	357	22.5	<b>357</b>	<b>22.5</b>	357	22.5	321	25.1	322	25.0	<b>321</b>	<b>25.1</b>
429.mcf	234	39.0	224	40.7	<b>225</b>	<b>40.6</b>	227	40.1	228	40.0	<b>227</b>	<b>40.1</b>
445.gobmk	553	19.0	<b>553</b>	<b>19.0</b>	553	19.0	474	22.1	477	22.0	<b>475</b>	<b>22.1</b>
456.hammer	483	19.3	<b>483</b>	<b>19.3</b>	483	19.3	349	26.7	<b>350</b>	<b>26.6</b>	351	26.6
458.sjeng	583	20.8	582	20.8	<b>582</b>	<b>20.8</b>	538	22.5	568	21.3	<b>568</b>	<b>21.3</b>
462.libquantum	<b>80.4</b>	<b>258</b>	80.6	257	79.8	260	<b>80.4</b>	<b>258</b>	80.6	257	79.8	260
464.h264ref	736	30.1	<b>737</b>	<b>30.0</b>	737	30.0	<b>648</b>	<b>34.1</b>	646	34.2	650	34.0
471.omnetpp	<b>315</b>	<b>19.9</b>	315	19.9	382	16.3	279	22.4	279	22.4	<b>279</b>	<b>22.4</b>
473.astar	465	15.1	467	15.0	<b>466</b>	<b>15.1</b>	<b>404</b>	<b>17.4</b>	403	17.4	405	17.3
483.xalancbmk	<b>231</b>	<b>29.8</b>	238	29.0	229	30.1	<b>231</b>	<b>29.8</b>	238	29.0	229	30.1

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

## General Notes

OMP\_NUM\_THREADS set to number of cores  
KMP\_AFFINITY set to granularity=fine,scatter

## Base Compiler Invocation

C benchmarks:  
icc

C++ benchmarks:  
icpc

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX



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## Base Optimization Flags

C benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel  
-par-runtime-control -opt-prefetch
```

C++ benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/spec/cpu2006.1.1/lib -lsmartheap
```

## Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc

```
401.bzip2: /opt/intel/Compiler/11.0/080/bin/intel64/icc
```

```
456.hmmer: /opt/intel/Compiler/11.0/080/bin/intel64/icc
```

```
458.sjeng: /opt/intel/Compiler/11.0/080/bin/intel64/icc
```

C++ benchmarks (except as noted below):

icpc

```
473.astar: /opt/intel/Compiler/11.0/080/bin/intel64/icpc
```

## Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32  
401.bzip2: -DSPEC_CPU_LP64  
456.hmmer: -DSPEC_CPU_LP64  
458.sjeng: -DSPEC_CPU_LP64  
462.libquantum: -DSPEC_CPU_LINUX  
473.astar: -DSPEC_CPU_LP64  
483.xalancbmk: -DSPEC_CPU_LINUX
```



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## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -ansi-alias -opt-prefetch

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -auto-ilp32 -opt-prefetch -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc  
-opt-malloc-options=3

429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2  
-ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2  
-ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/spec/cpu2006.1.1/lib -lsmartheap

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=routine -auto-ilp32  
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap64

483.xalancbmk: basepeak = yes



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## Peak Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20100202.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20100202.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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