



# SPEC® CINT2006 Result

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## Acer Incorporated

**SPECint®2006 = 19.3**

## Gateway GR160 F1 (Intel Xeon E5502)

**SPECint\_base2006 = 17.2**

CPU2006 license: 97

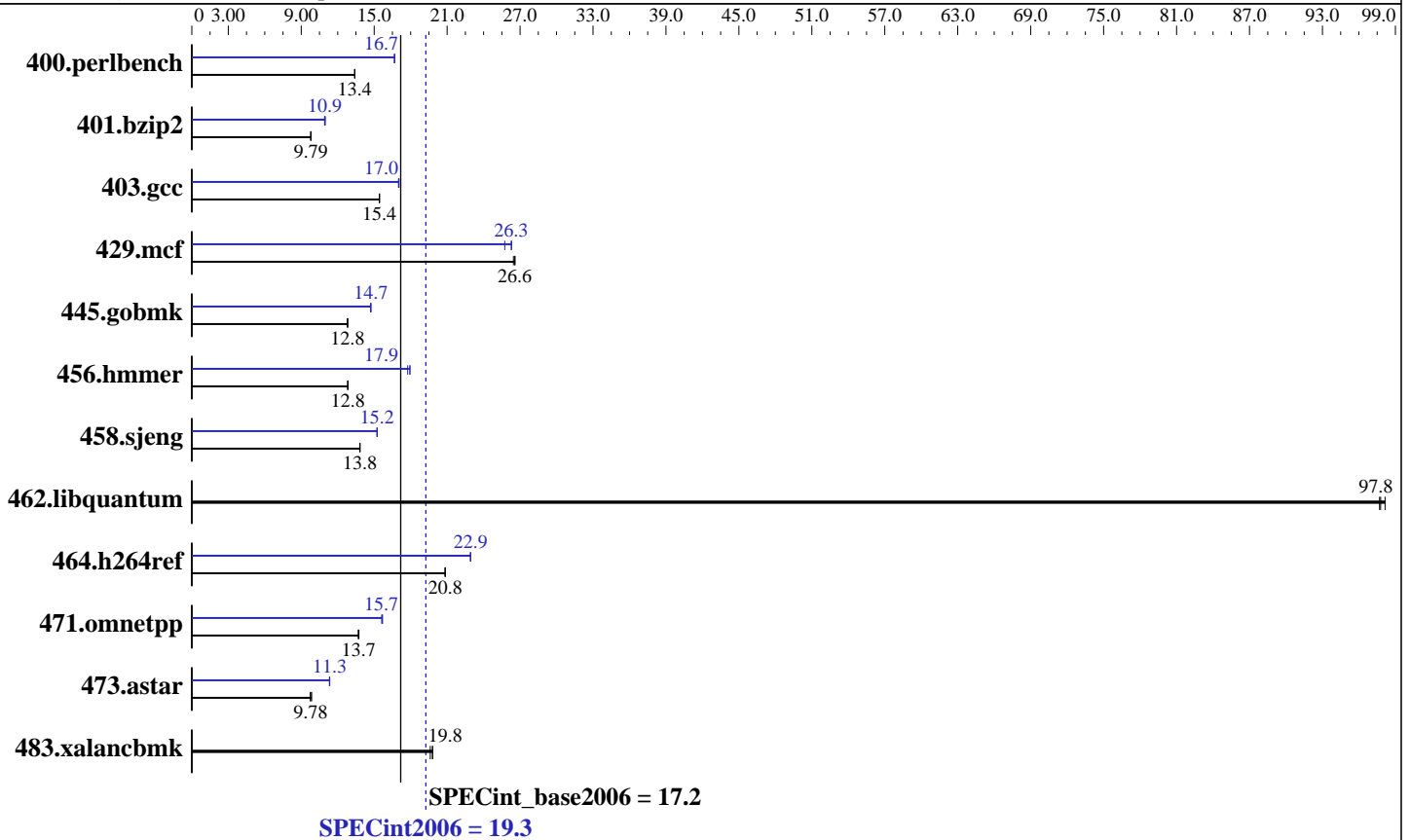
Test sponsor: Acer Incorporated

Tested by: Acer Incorporated

Test date: Dec-2009

Hardware Availability: Jan-2010

Software Availability: Feb-2009



### Hardware

CPU Name: Intel Xeon E5502  
 CPU Characteristics:  
 CPU MHz: 1867  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip  
 CPU(s) orderable: 1, 2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 4 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 24 GB (6 x 4 GB DDR3-1333 RDIMM)  
 Disk Subsystem: 300 GB SATA II, 10000 RPM  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 11 (x86\_64)  
 Kernel 2.6.27.19-5  
 Compiler: Intel C++ Compiler Professional 11.0 for Linux  
 Build 20090131 Package ID: l\_cproc\_p\_11.0.080  
 Auto Parallel: Yes  
 File System: ReiserFS  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1  
 Binutils 2.18.50.0.7.20080502



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	730	13.4	<b><u>729</u></b>	<b><u>13.4</u></b>	729	13.4	585	16.7	586	16.7	<b><u>586</u></b>	<b><u>16.7</u></b>
401.bzip2	987	9.78	984	9.81	<b><u>986</u></b>	<b><u>9.79</u></b>	884	10.9	<b><u>882</u></b>	<b><u>10.9</u></b>	879	11.0
403.gcc	522	15.4	<b><u>522</u></b>	<b><u>15.4</u></b>	521	15.4	474	17.0	<b><u>473</u></b>	<b><u>17.0</u></b>	472	17.0
429.mcf	345	26.5	343	26.6	<b><u>343</u></b>	<b><u>26.6</u></b>	347	26.3	354	25.7	<b><u>347</u></b>	<b><u>26.3</u></b>
445.gobmk	<b><u>819</u></b>	<b><u>12.8</u></b>	819	12.8	819	12.8	712	14.7	<b><u>712</u></b>	<b><u>14.7</u></b>	714	14.7
456.hammer	727	12.8	728	12.8	<b><u>727</u></b>	<b><u>12.8</u></b>	<b><u>520</u></b>	<b><u>17.9</u></b>	520	17.9	526	17.8
458.sjeng	875	13.8	<b><u>875</u></b>	<b><u>13.8</u></b>	876	13.8	<b><u>794</u></b>	<b><u>15.2</u></b>	794	15.2	794	15.2
462.libquantum	<b><u>212</u></b>	<b><u>97.8</u></b>	211	98.2	212	97.7	<b><u>212</u></b>	<b><u>97.8</u></b>	211	98.2	212	97.7
464.h264ref	1061	20.9	<b><u>1062</u></b>	<b><u>20.8</u></b>	1062	20.8	<b><u>965</u></b>	<b><u>22.9</u></b>	966	22.9	965	22.9
471.omnetpp	455	13.7	<b><u>456</u></b>	<b><u>13.7</u></b>	456	13.7	400	15.6	<b><u>399</u></b>	<b><u>15.7</u></b>	399	15.7
473.astar	712	9.86	<b><u>718</u></b>	<b><u>9.78</u></b>	720	9.75	620	11.3	<b><u>619</u></b>	<b><u>11.3</u></b>	619	11.3
483.xalancbmk	352	19.6	348	19.8	<b><u>349</u></b>	<b><u>19.8</u></b>	352	19.6	348	19.8	<b><u>349</u></b>	<b><u>19.8</u></b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## General Notes

OMP\_NUM\_THREADS set to number of cores  
KMP\_AFFINITY set to granularity=fine,scatter

This result was measured on the Gateway GR160 F1.  
The Acer AR160 F1 and Gateway GR160 F1 are electronically equivalent.

## Base Compiler Invocation

C benchmarks:  
icc

C++ benchmarks:  
icpc

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX



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## Base Optimization Flags

C benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel  
-par-runtime-control -opt-prefetch
```

C++ benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/spec/cpu2006.1.1/lib -lsmartheap
```

## Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc

```
401.bzip2: /opt/intel/Compiler/11.0/080/bin/intel64/icc
```

```
456.hmmer: /opt/intel/Compiler/11.0/080/bin/intel64/icc
```

```
458.sjeng: /opt/intel/Compiler/11.0/080/bin/intel64/icc
```

C++ benchmarks (except as noted below):

icpc

```
473.astar: /opt/intel/Compiler/11.0/080/bin/intel64/icpc
```

## Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
```

```
401.bzip2: -DSPEC_CPU_LP64
```

```
456.hmmer: -DSPEC_CPU_LP64
```

```
458.sjeng: -DSPEC_CPU_LP64
```

```
462.libquantum: -DSPEC_CPU_LINUX
```

```
473.astar: -DSPEC_CPU_LP64
```

```
483.xalancbmk: -DSPEC_CPU_LINUX
```



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## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -ansi-alias -opt-prefetch

401.bzp2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -auto-ilp32 -opt-prefetch -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-alloc  
-opt-malloc-options=3

429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2  
-ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2  
-ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/spec/cpu2006.1.1/lib -lsmartheap

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=routine -auto-ilp32  
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap64

483.xalancbmk: basepeak = yes



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## Peak Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revE.20100216.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revE.20100216.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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