



SPEC® CINT2006 Result

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Acer Incorporated

SPECint®2006 = 29.7

Gateway GT110 F1 (Intel Xeon X3380)

SPECint_base2006 = 26.5

CPU2006 license: 97

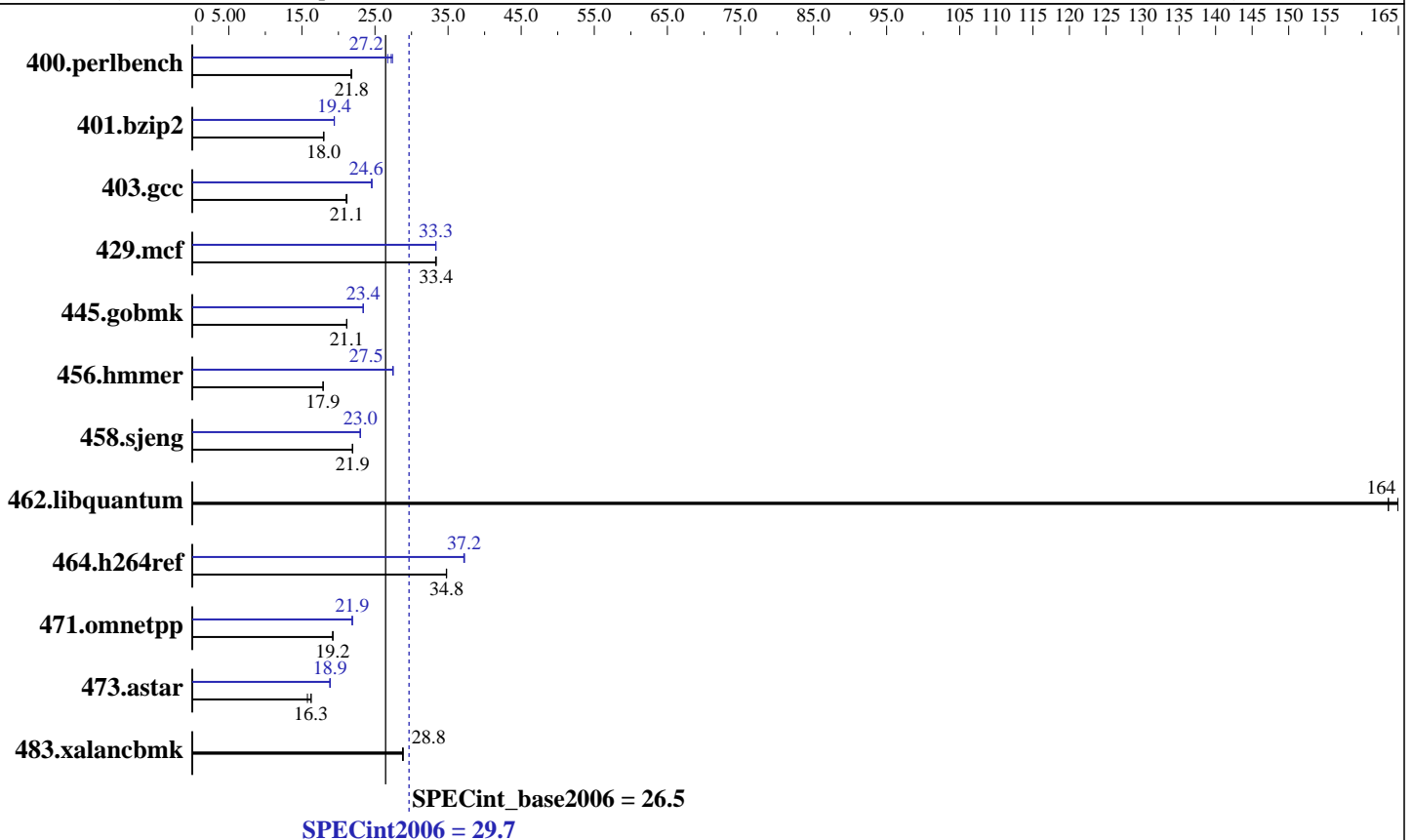
Test date: Nov-2009

Test sponsor: Acer Incorporated

Hardware Availability: Dec-2009

Tested by: Acer Incorporated

Software Availability: Nov-2008



Hardware

CPU Name: Intel Xeon X3380
 CPU Characteristics: 1333 MHz system bus
 CPU MHz: 3167
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 8 GB (4 x 2048 MB ECC Unbuffered DDR2-800 CL5)
 Disk Subsystem: 1 x 640 GB 7200RPM SATA HDD
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64)
 Kernel 2.6.27.19-5
 Compiler: Intel C++ Compiler 11.0 for Linux
 Build 20080930 Package ID: l_cproc_p_11.0.066
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Multi-user, level 3
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1
 Binutils 2.18.50.0.7.20080502



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	448	21.8	448	21.8	450	21.7	359	27.2	365	26.7	357	27.4
401.bzip2	538	17.9	535	18.0	537	18.0	497	19.4	496	19.4	497	19.4
403.gcc	380	21.2	381	21.1	382	21.1	328	24.5	328	24.6	328	24.6
429.mcf	274	33.3	273	33.4	273	33.4	274	33.3	274	33.3	273	33.3
445.gobmk	496	21.1	496	21.1	496	21.1	448	23.4	449	23.4	448	23.4
456.hmmmer	521	17.9	521	17.9	521	17.9	339	27.5	340	27.4	340	27.5
458.sjeng	552	21.9	552	21.9	551	22.0	526	23.0	527	23.0	526	23.0
462.libquantum	127	164	126	165	127	164	127	164	126	165	127	164
464.h264ref	636	34.8	637	34.8	636	34.8	595	37.2	594	37.2	596	37.2
471.omnetpp	325	19.2	325	19.2	325	19.2	285	21.9	286	21.9	285	21.9
473.astar	431	16.3	432	16.3	445	15.8	372	18.9	373	18.8	372	18.9
483.xalancbmk	239	28.9	239	28.8	240	28.8	239	28.9	239	28.8	240	28.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was set for stacksize unlimited

General Notes

OMP_NUM_THREADS set to number of processors
KMP_AFFINITY set to "physical,0"

This result was measured on the Gateway GT110 F1.
The Acer AT110 F1 and Gateway GT110 F1 are electronically equivalent.

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX

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Base Portability Flags (Continued)

483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -static -parallel
-par-runtime-control -opt-prefetch

C++ benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/Compiler/11.0/066/bin/intel64/icc

456.hmmer: /opt/intel/Compiler/11.0/066/bin/intel64/icc

C++ benchmarks:

icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32

401.bzip2: -DSPEC_CPU_LP64

456.hmmer: -DSPEC_CPU_LP64

462.libquantum: -DSPEC_CPU_LINUX

483.xalancbmk: -DSPEC_CPU_LINUX



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Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -ansi-alias -opt-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -auto-ilp32 -opt-prefetch
-ansi-alias

403.gcc: -xSSE4.1 -ipo -O3 -no-prec-div -static -inline-alloc
-opt-malloc-options=3

429.mcf: -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -O2 -ipo
-no-prec-div -ansi-alias

456.hmmer: -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2
-ansi-alias -auto-ilp32

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll4

462.libquantum: basepeak = yes

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca



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The flags file that was used to format this result can be browsed at
<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revE.20100119.html>

You can also download the XML flags source by saving the following link:
<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revE.20100119.xml>

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For other inquiries, please contact webmaster@spec.org.

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