



# SPEC® CINT2006 Result

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## Fujitsu

**SPECint®2006 = 35.7**

PRIMERGY BX620 S5, Intel Xeon X5570, 2.93 GHz

**SPECint\_base2006 = 31.6**

CPU2006 license: 19

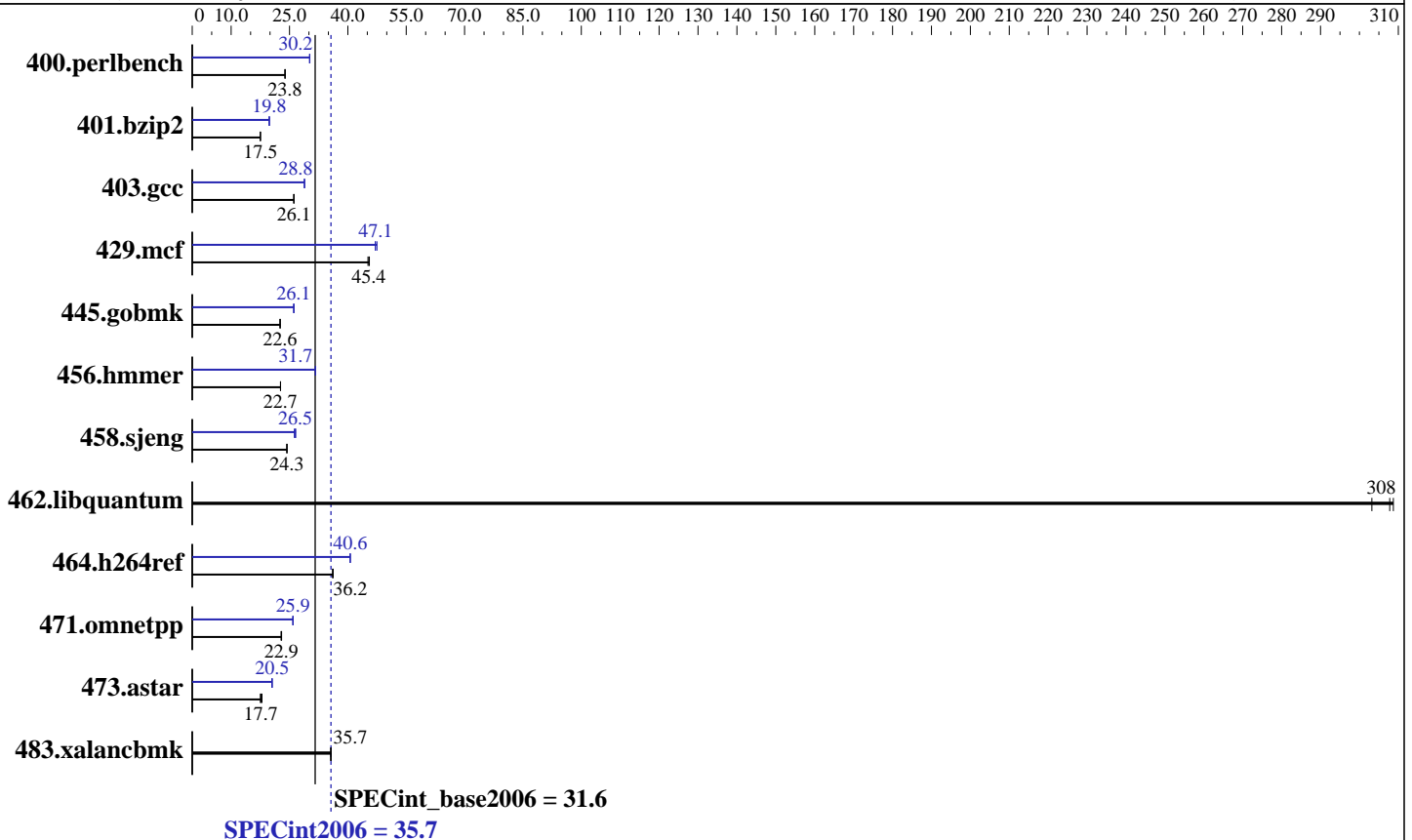
Test date: Oct-2009

Test sponsor: Fujitsu

Hardware Availability: Apr-2009

Tested by: Fujitsu

Software Availability: Feb-2009



### Hardware

CPU Name: Intel Xeon X5570  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.33 GHz  
 CPU MHz: 2933  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 8 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 48 GB (6x8 GB PC3-10600R, 2 rank, CL9-9-9, ECC)  
 Disk Subsystem: 1 x SATA, 250 GB, 7200 RPM  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 10 (x86\_64) SP2, Kernel 2.6.16.60-0.21-smp  
 Compiler: Intel C++ Compiler 11.0 for Linux Build 20090131 Package ID: l\_cproc\_p\_11.0.080  
 Auto Parallel: Yes  
 File System: ext3  
 System State: Multi-User Run Level 3  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	410	23.8	409	23.9	<b>410</b>	<b>23.8</b>	323	30.2	<b>324</b>	<b>30.2</b>	324	30.2
401.bzip2	552	17.5	<b>551</b>	<b>17.5</b>	550	17.5	<b>488</b>	<b>19.8</b>	488	19.8	486	19.9
403.gcc	308	26.1	308	26.1	<b>308</b>	<b>26.1</b>	<b>279</b>	<b>28.8</b>	279	28.9	279	28.8
429.mcf	200	45.5	<b>201</b>	<b>45.4</b>	202	45.2	<b>194</b>	<b>47.1</b>	194	47.1	192	47.5
445.gobmk	465	22.6	464	22.6	<b>464</b>	<b>22.6</b>	403	26.1	<b>402</b>	<b>26.1</b>	401	26.2
456.hammer	411	22.7	411	22.7	<b>411</b>	<b>22.7</b>	294	31.7	<b>294</b>	<b>31.7</b>	295	31.7
458.sjeng	<b>497</b>	<b>24.3</b>	497	24.4	498	24.3	<b>457</b>	<b>26.5</b>	461	26.2	455	26.6
462.libquantum	67.1	309	68.3	303	<b>67.3</b>	<b>308</b>	67.1	309	68.3	303	<b>67.3</b>	<b>308</b>
464.h264ref	611	36.2	<b>612</b>	<b>36.2</b>	614	36.1	<b>545</b>	<b>40.6</b>	545	40.6	545	40.6
471.omnetpp	<b>273</b>	<b>22.9</b>	273	22.9	272	23.0	241	25.9	242	25.8	<b>241</b>	<b>25.9</b>
473.astar	<b>397</b>	<b>17.7</b>	401	17.5	391	17.9	341	20.6	<b>342</b>	<b>20.5</b>	343	20.5
483.xalancbmk	<b>193</b>	<b>35.7</b>	193	35.7	194	35.6	<b>193</b>	<b>35.7</b>	193	35.7	194	35.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

## General Notes

OMP\_NUM\_THREADS set to number of cores  
KMP\_AFFINITY set to granularity=fine,scatter  
For information about Fujitsu please visit: <http://www.fujitsu.com>

## Base Compiler Invocation

C benchmarks:  
icc  
  
C++ benchmarks:  
icpc

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX



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## Base Optimization Flags

C benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel  
-par-runtime-control -opt-prefetch
```

C++ benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/spec/cpu2006.1.1/lib -lsmartheap
```

## Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc

```
401.bzip2: /opt/intel/Compiler/11.0/080/bin/intel64/icc
```

```
456.hmmer: /opt/intel/Compiler/11.0/080/bin/intel64/icc
```

```
458.sjeng: /opt/intel/Compiler/11.0/080/bin/intel64/icc
```

C++ benchmarks (except as noted below):

icpc

```
473.astar: /opt/intel/Compiler/11.0/080/bin/intel64/icpc
```

## Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
```

```
401.bzip2: -DSPEC_CPU_LP64
```

```
456.hmmer: -DSPEC_CPU_LP64
```

```
458.sjeng: -DSPEC_CPU_LP64
```

```
462.libquantum: -DSPEC_CPU_LINUX
```

```
473.astar: -DSPEC_CPU_LP64
```

```
483.xalancbmk: -DSPEC_CPU_LINUX
```



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## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -ansi-alias -opt-prefetch

401.bzp2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -auto-ilp32 -opt-prefetch -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc  
-opt-malloc-options=3

429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2  
-ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2  
-ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/spec/cpu2006.1.1/lib -lsmartheap

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=routine -auto-ilp32  
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap64

483.xalancbmk: basepeak = yes



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## Peak Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20091013.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20091013.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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