



SPEC® CINT2006 Result

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NTT System S. A.

SPECint®_rate2006 = Not Run

NTT Tytan S32 series (Intel Xeon E5520)

SPECint_rate_base2006 = 185

CPU2006 license: 9013

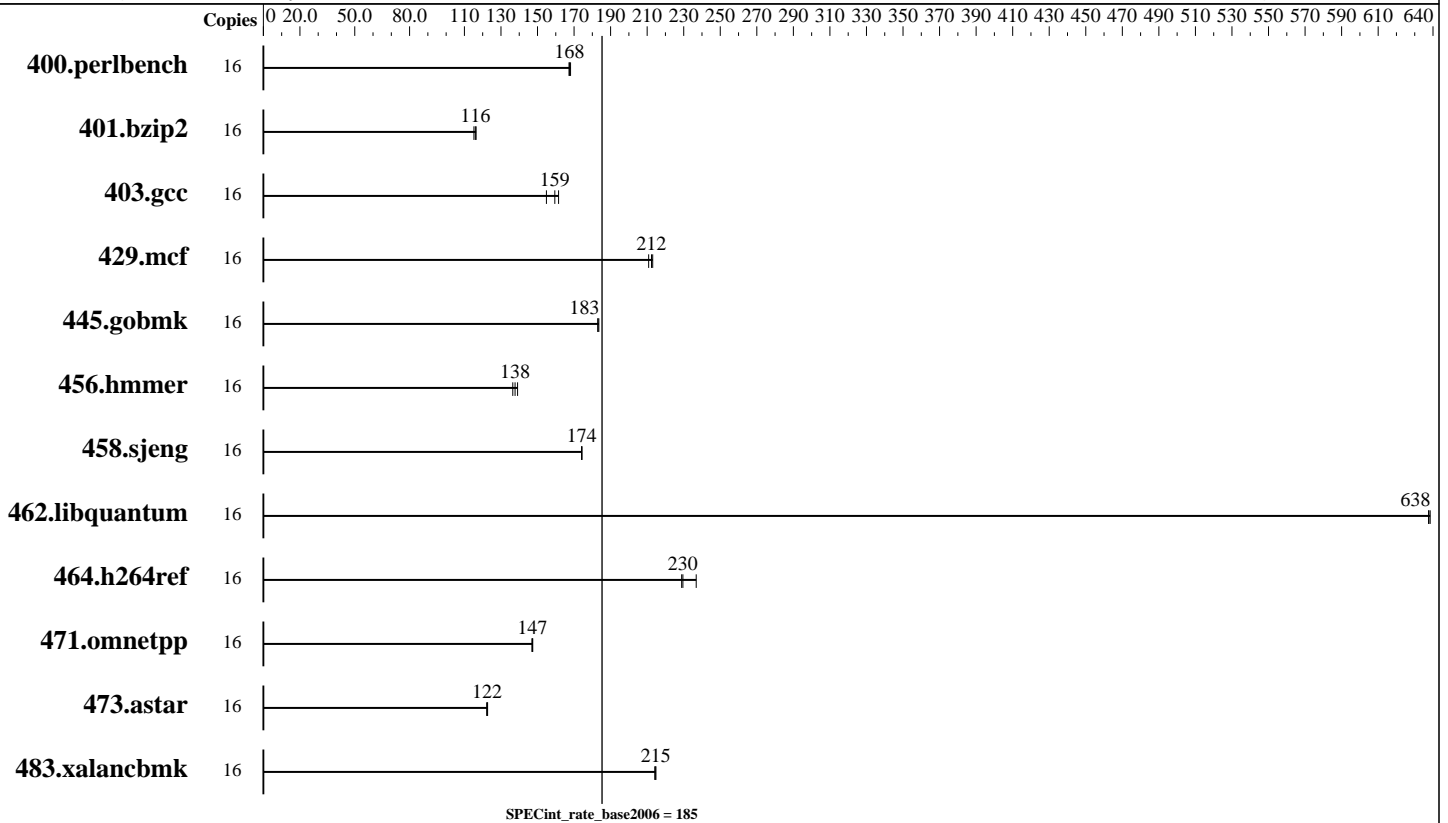
Test date: Aug-2009

Test sponsor: NTT System S. A.

Hardware Availability: May-2009

Tested by: NTT System S. A.

Software Availability: Nov-2008



Hardware

CPU Name: Intel Xeon E5520
 CPU Characteristics: Intel Turbo Boost Technology up to 2.53 GHz
 CPU MHz: 2267
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 8 MB I+D on chip per chip
 Other Cache: None
 Memory: 24 GB (6 x 4 GB 1333 MHz ECC Registered runnig at 1066 MHz)
 Disk Subsystem: 147 GB SAS, 10000 RPM
 Other Hardware: None

Software

Operating System: SuSe Linux Enterprise Server 10 (x86_64) SP2, kernel 2.6.16.60-0.21-smp
 Compiler: Intel C++ Compiler 11.0 for Linux Build 20080930 Package ID: l_cproc_p_11.0.066
 Auto Parallel: No
 File System: ReiserFS
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: Not Applicable
 Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	<u>933</u>	<u>168</u>	930	168	935	167							
401.bzip2	16	<u>1331</u>	<u>116</u>	1326	116	1340	115							
403.gcc	16	832	155	797	162	<u>808</u>	<u>159</u>							
429.mcf	16	<u>687</u>	<u>212</u>	685	213	692	211							
445.gobmk	16	917	183	914	184	<u>917</u>	<u>183</u>							
456.hammer	16	1094	136	<u>1084</u>	<u>138</u>	1073	139							
458.sjeng	16	1111	174	1112	174	<u>1112</u>	<u>174</u>							
462.libquantum	16	<u>520</u>	<u>638</u>	519	638	520	638							
464.h264ref	16	1547	229	<u>1542</u>	<u>230</u>	1495	237							
471.omnetpp	16	679	147	681	147	<u>679</u>	<u>147</u>							
473.astar	16	<u>917</u>	<u>122</u>	919	122	915	123							
483.xalanbmk	16	<u>514</u>	<u>215</u>	516	214	514	215							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalanbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -inline-alloc
-opt-malloc-options=3 -opt-prefetch

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Base Optimization Flags (Continued)

C++ benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/spec/cpu2006.1.1/lib -lsmartheap
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090901.00.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090901.00.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.

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