



SPEC® CFP2006 Result

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NTT System S. A.
NTT Business W 907G

SPECfp®2006 = 17.4

SPECfp_base2006 = 16.8

CPU2006 license: 9013

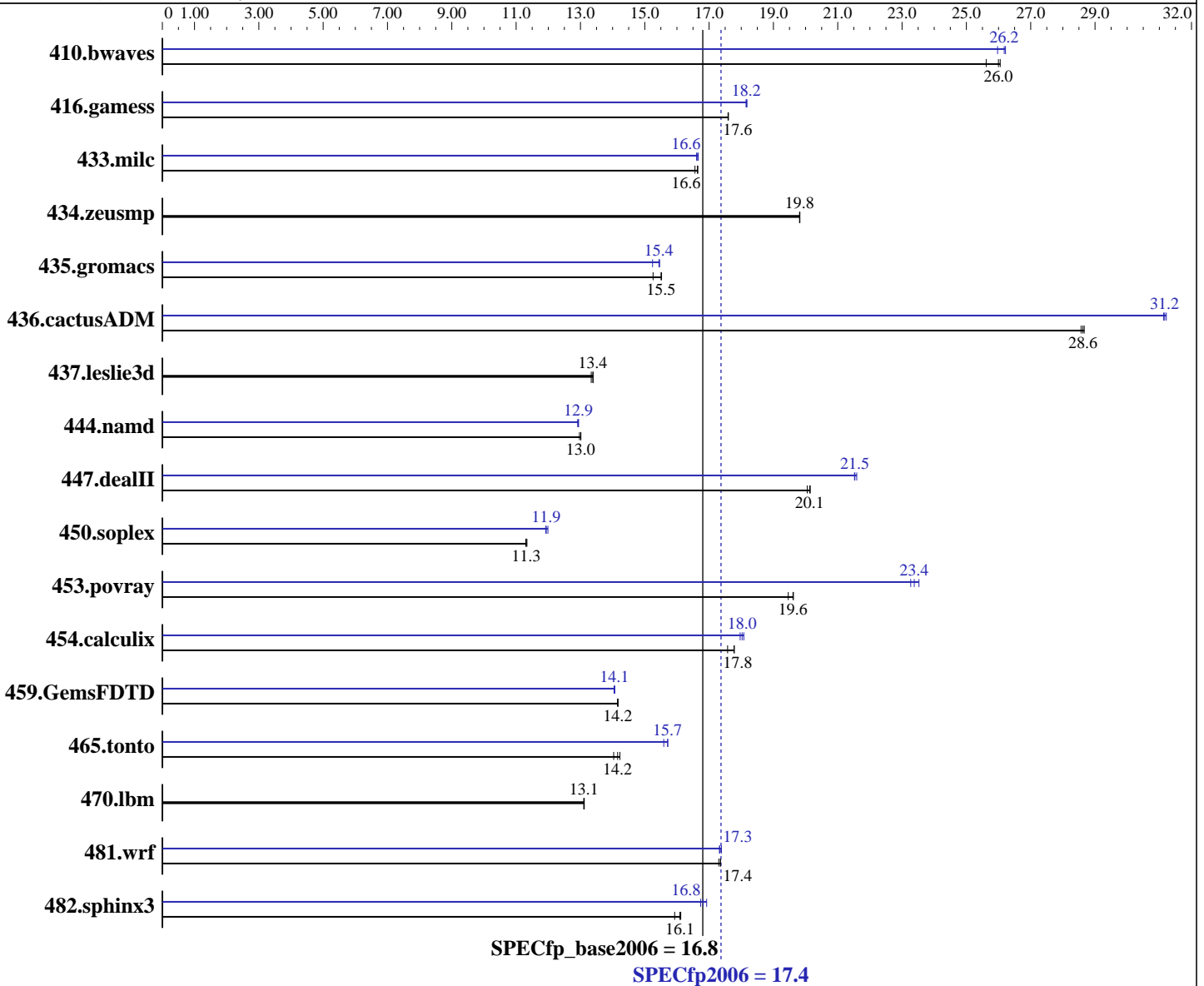
Test sponsor: NTT System S. A.

Tested by: NTT System S. A.

Test date: Jul-2009

Hardware Availability: Jul-2009

Software Availability: Nov-2008



Hardware

CPU Name: Intel Celeron E3200
 CPU Characteristics:
 CPU MHz: 2400
 FPU: Integrated
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 1 MB I+D on chip per chip

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Software

Operating System: SuSe Linux SLES10 SP2, Kernel 2.6.16.60-0.21-smp
 Compiler: Intel C++ and Fortran Compiler 11.0 for Linux
 Build 20080930 Package ID: l_cproc_p_11.0.066,
 l_cprof_p_11.0.066
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: 32/64-bit

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L3 Cache: None
Other Cache: None
Memory: 4 GB (2x2GB) DDR2 800Mhz
Disk Subsystem: 250 GB SATA, 7200RPM
Other Hardware: None

Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	531	25.6	522	26.1	<u>523</u>	<u>26.0</u>	523	26.0	518	26.2	<u>519</u>	<u>26.2</u>
416.gamess	1113	17.6	1113	17.6	<u>1113</u>	<u>17.6</u>	<u>1078</u>	<u>18.2</u>	1077	18.2	1079	18.1
433.milc	554	16.6	<u>552</u>	<u>16.6</u>	551	16.6	<u>552</u>	<u>16.6</u>	551	16.7	553	16.6
434.zeusmp	<u>459</u>	<u>19.8</u>	459	19.8	459	19.8	<u>459</u>	<u>19.8</u>	459	19.8	459	19.8
435.gromacs	<u>460</u>	<u>15.5</u>	468	15.3	460	15.5	468	15.2	<u>462</u>	<u>15.4</u>	462	15.5
436.cactusADM	417	28.7	<u>418</u>	<u>28.6</u>	418	28.6	383	31.2	<u>383</u>	<u>31.2</u>	384	31.1
437.leslie3d	<u>702</u>	<u>13.4</u>	705	13.3	702	13.4	<u>702</u>	<u>13.4</u>	705	13.3	702	13.4
444.namd	<u>616</u>	<u>13.0</u>	616	13.0	618	13.0	620	12.9	621	12.9	<u>620</u>	<u>12.9</u>
447.dealII	571	20.1	568	20.1	<u>568</u>	<u>20.1</u>	532	21.5	<u>531</u>	<u>21.5</u>	530	21.6
450.soplex	<u>737</u>	<u>11.3</u>	738	11.3	736	11.3	696	12.0	<u>699</u>	<u>11.9</u>	700	11.9
453.povray	271	19.6	<u>271</u>	<u>19.6</u>	273	19.5	226	23.5	229	23.3	<u>228</u>	<u>23.4</u>
454.calculix	<u>464</u>	<u>17.8</u>	464	17.8	469	17.6	456	18.1	<u>458</u>	<u>18.0</u>	459	18.0
459.GemsFDTD	749	14.2	750	14.2	<u>749</u>	<u>14.2</u>	755	14.0	<u>755</u>	<u>14.1</u>	754	14.1
465.tonto	701	14.0	<u>695</u>	<u>14.2</u>	692	14.2	631	15.6	<u>626</u>	<u>15.7</u>	626	15.7
470.lbm	<u>1048</u>	<u>13.1</u>	1047	13.1	1049	13.1	<u>1048</u>	<u>13.1</u>	1047	13.1	1049	13.1
481.wrf	<u>644</u>	<u>17.4</u>	646	17.3	644	17.4	<u>645</u>	<u>17.3</u>	645	17.3	643	17.4
482.sphinx3	1223	15.9	1209	16.1	<u>1211</u>	<u>16.1</u>	1164	16.7	<u>1159</u>	<u>16.8</u>	1152	16.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

OMP_NUM_THREADS set to number of processors
KMP_AFFINITY set to "physical,0"
KMP_STACKSIZE set to 200M

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

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Base Compiler Invocation (Continued)

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xSSSE3 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
C++ benchmarks:
-xSSSE3 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
Fortran benchmarks:
-xSSSE3 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
Benchmarks using both Fortran and C:
-xSSSE3 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

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Peak Compiler Invocation (Continued)

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):

icpc

450.soplex: icpc -m32

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icc ifort

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

433.milc: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -static -fno-alias

470.lbm: basepeak = yes

482.sphinx3: -xSSSE3 -ipo -O3 -no-prec-div -static -unroll2

C++ benchmarks:

444.namd: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -static -fno-alias -auto-ilp32

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Peak Optimization Flags (Continued)

447.deallI: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -static -unroll2 -ansi-alias -scalar-rep-
-opt-prefetch

450.soplex: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -static -opt-malloc-options=3

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -static -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xSSSE3 -ipo -O3 -no-prec-div -static -opt-prefetch
-parallel

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -static -unroll2 -Ob0 -ansi-alias
-scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -static -unroll2 -Ob0 -opt-prefetch
-parallel

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -static -unroll4 -auto

Benchmarks using both Fortran and C:

435.gromacs: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -static -opt-prefetch -auto-ilp32

436.cactusADM: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -static -unroll2 -opt-prefetch -parallel
-auto-ilp32

454.calculix: -xSSSE3 -ipo -O3 -no-prec-div -static -auto-ilp32

481.wrf: -xSSSE3 -ipo -O3 -no-prec-div -static -opt-prefetch
-parallel -auto-ilp32

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20090827.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20090827.xml>



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