



SPEC® CINT2006 Result

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Intel Corporation

SPECint®2006 = 26.3

Intel DQ45CB motherboard (Intel Core 2 Duo E8400)

SPECint_base2006 = 24.2

CPU2006 license: 13

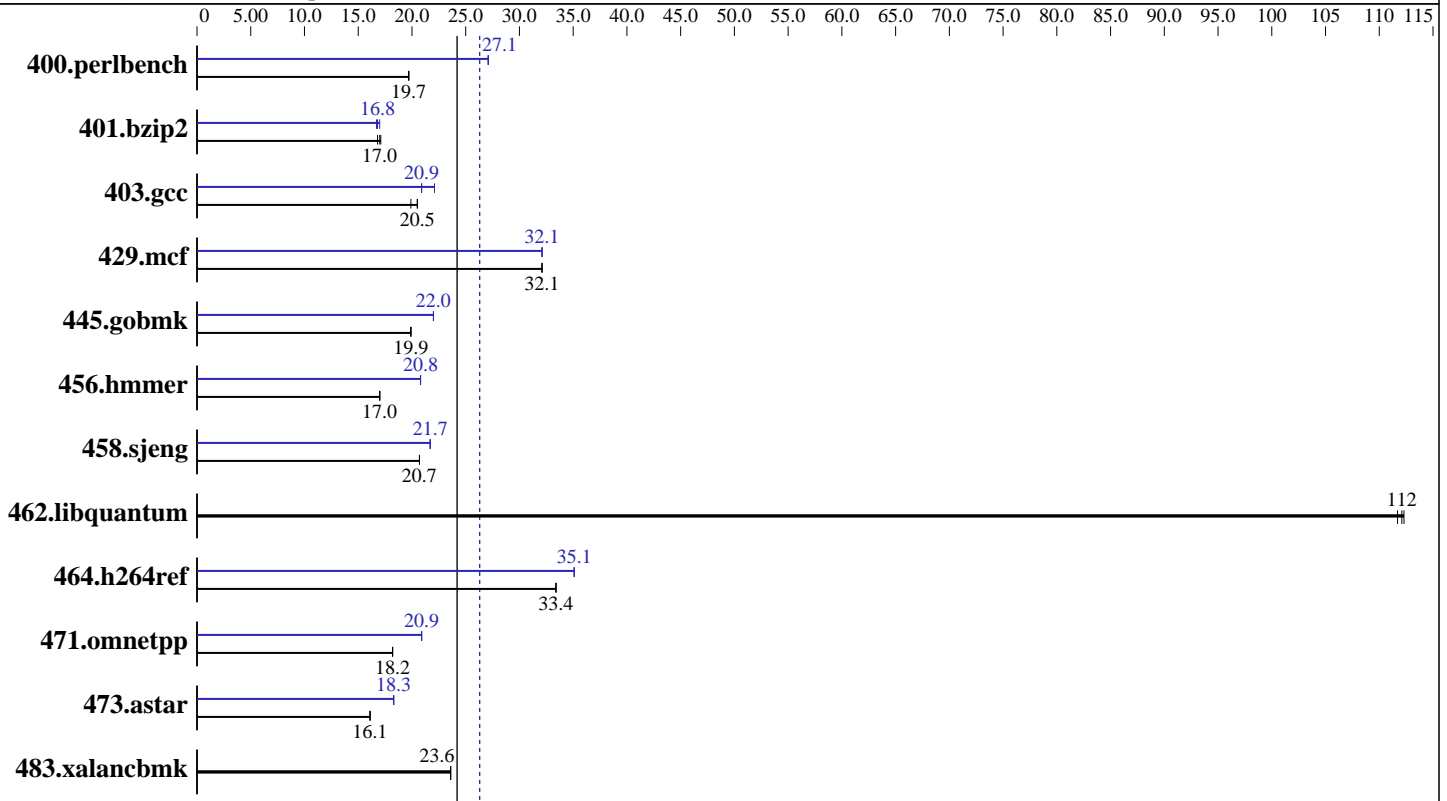
Test date: Apr-2009

Test sponsor: Intel Corporation

Hardware Availability: May-2009

Tested by: Intel Corporation

Software Availability: Nov-2008



Hardware

CPU Name: Intel Core 2 Duo E8400
 CPU Characteristics:
 CPU MHz: 3000
 FPU: Integrated
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 6 MB I+D on chip per chip
 L3 Cache: None
 Other Cache: None
 Memory: 4 GB (4x1GB DDR2-800 CL5)
 Disk Subsystem: Seagate 320 GB SATA, 7200RPM
 Other Hardware: None

Software

Operating System: Windows Vista Ultimate w/ SP1 (64-bit)
 Compiler: Intel C++ Compiler Professional 11.0 for IA32
 Build 20080930 Package ID: w_cproc_p_11.0.054
 Microsoft Visual Studio 2008 (for libraries)
 Auto Parallel: Yes
 File System: NTFS
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: 32-bit
 Other Software: SmartHeap Library Version 8.1 from
<http://www.microquill.com/>



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	<u>496</u>	<u>19.7</u>	496	19.7	496	19.7	<u>360</u>	<u>27.1</u>	360	27.1	360	27.1
401.bzip2	564	17.1	<u>569</u>	<u>17.0</u>	573	16.8	578	16.7	568	17.0	<u>574</u>	<u>16.8</u>
403.gcc	393	20.5	404	19.9	<u>393</u>	<u>20.5</u>	364	22.1	<u>385</u>	<u>20.9</u>	385	20.9
429.mcf	284	32.1	<u>284</u>	<u>32.1</u>	284	32.1	<u>284</u>	<u>32.1</u>	284	32.1	284	32.1
445.gobmk	526	19.9	<u>526</u>	<u>19.9</u>	527	19.9	476	22.0	476	22.0	<u>476</u>	<u>22.0</u>
456.hammer	548	17.0	549	17.0	<u>549</u>	<u>17.0</u>	449	20.8	<u>449</u>	<u>20.8</u>	449	20.8
458.sjeng	586	20.7	<u>586</u>	<u>20.7</u>	586	20.7	559	21.7	559	21.7	<u>559</u>	<u>21.7</u>
462.libquantum	186	112	<u>185</u>	<u>112</u>	185	112	186	112	<u>185</u>	<u>112</u>	185	112
464.h264ref	663	33.4	<u>663</u>	<u>33.4</u>	663	33.4	<u>631</u>	<u>35.1</u>	631	35.1	631	35.1
471.omnetpp	344	18.2	<u>343</u>	<u>18.2</u>	343	18.2	<u>299</u>	<u>20.9</u>	299	20.9	299	20.9
473.astar	435	16.1	435	16.1	<u>435</u>	<u>16.1</u>	<u>383</u>	<u>18.3</u>	383	18.3	383	18.3
483.xalancbmk	<u>293</u>	<u>23.6</u>	293	23.6	293	23.6	<u>293</u>	<u>23.6</u>	293	23.6	293	23.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

Tested systems can be used with Shin-G ATX case,
 Antec Truepower Trio power supply TP3-650
 Binaries were built on Windows Vista Ultimate (32-bit)
 Binaries were built on Windows Vista Ultimate (32-bit)
 OMP_NUM_THREADS set to number of logical processors as seen by the OS
 KMP_AFFINITY set to physical,0

Base Compiler Invocation

C benchmarks:
 icl -Qvc9 -Qc99

C++ benchmarks:
 icl -Qvc9

Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32
 464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
 483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword



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Base Optimization Flags

C benchmarks:

-QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qparallel
-Qpar-runtime-control -Qvec-guard-write /F512000000

C++ benchmarks:

-QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qcxx-features
/F512000000 shlw32m.lib -link /FORCE:MULTIPLE

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks:

icl -Qvc9 -Qc99

C++ benchmarks:

icl -Qvc9

Peak Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
483.xalanbmk: -Qoption,cpp,--no_wchar_t_keyword

Peak Optimization Flags

C benchmarks:

400.perlbench: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qansi-alias -Qopt-prefetch
/F512000000 shlw32m.lib -link /FORCE:MULTIPLE

401.bzip2: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qopt-prefetch -Qansi-alias
/F512000000

403.gcc: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- /F512000000

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Peak Optimization Flags (Continued)

429.mcf: -QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch
/F512000000

445.gobmk: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O2 -Qprec-div- -Qansi-alias /F512000000

456.hmmcr: -QxSSE4.1 -Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias
/F512000000

458.sjeng: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qunroll14 /F512000000

462.libquantum: basepeak = yes

464.h264ref: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias /F512000000

C++ benchmarks:

471.omnetpp: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qansi-alias
-Qopt-ra-region-strategy=block /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE

473.astar: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qansi-alias
-Qopt-ra-region-strategy=routine /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-win32-revA.20090710.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-win32-revA.20090710.xml>



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