



SPEC[®] CFP2006 Result

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Intel Corporation

SPECfp[®]_rate2006 = 34.2

Intel DQ45CB motherboard (Intel Core 2 Duo E8500)

SPECfp_rate_base2006 = 32.5

CPU2006 license: 13

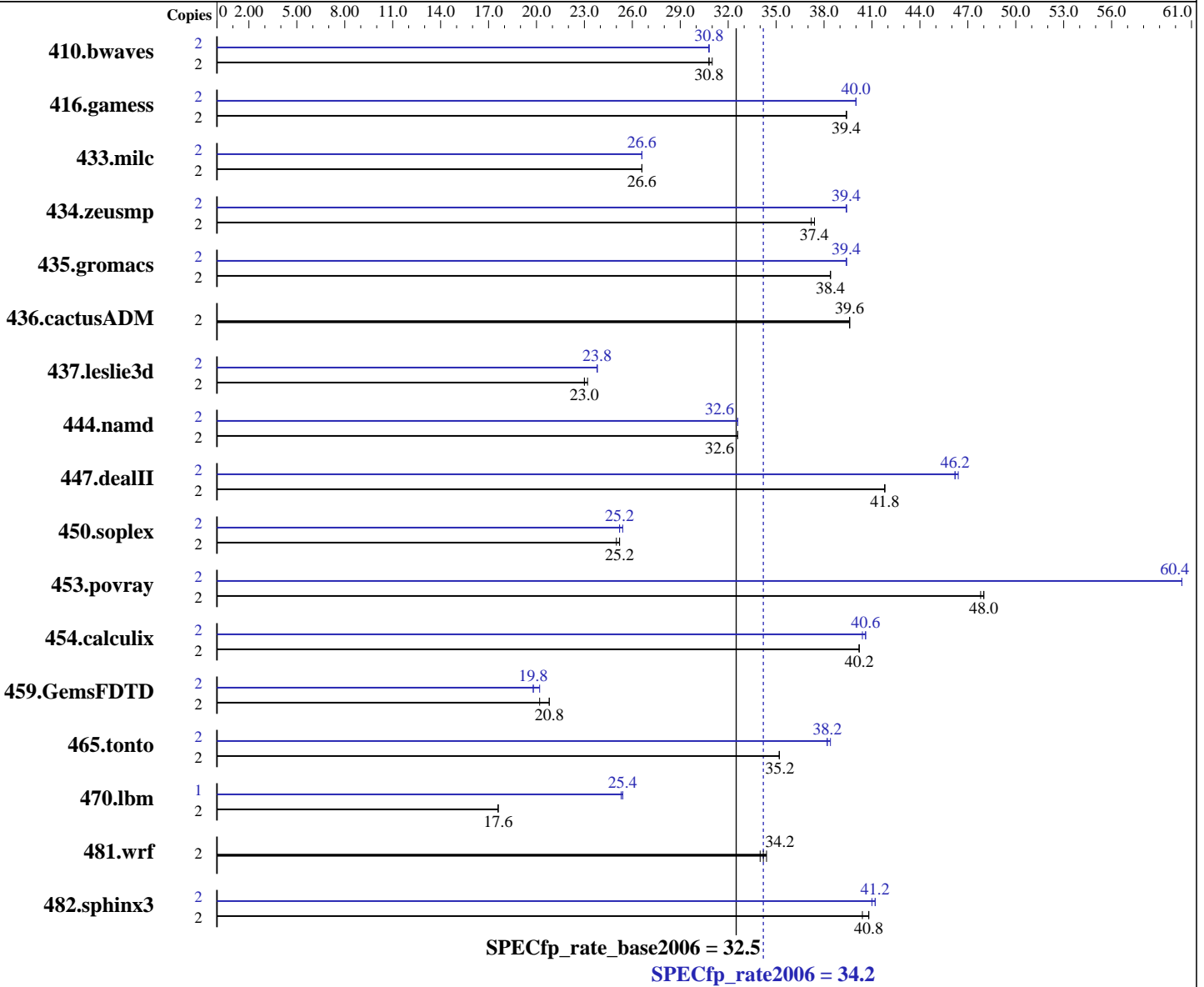
Test sponsor: Intel Corporation

Tested by: Intel Corporation

Test date: Apr-2009

Hardware Availability: May-2009

Software Availability: Nov-2008



Hardware

CPU Name: Intel Core 2 Duo E8500
 CPU Characteristics: 3166
 CPU MHz: Integrated
 FPU: Integrated
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 6 MB I+D on chip per chip

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Software

Operating System: Windows Vista Ultimate w/ SP1 (64-bit)
 Compiler: Intel C++ Compiler Professional 11.0 for IA32
 Build 20080930 Package ID: w_cproc_p_11.0.054
 Intel Visual Fortran Compiler Professional 11.0 for IA32
 Build 20080930 Package ID: w_cprof_p_11.0.054
 Microsoft Visual Studio 2008 (for libraries)
 Auto Parallel: No
 File System: NTFS

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L3 Cache: None
Other Cache: None
Memory: 4 GB (4x1GB DDR2-800 CL5)
Disk Subsystem: Seagate 320 GB SATA, 7200RPM
Other Hardware: None

System State: Default
Base Pointers: 32-bit
Peak Pointers: 32-bit
Other Software: SmartHeap Library Version 8.1 from <http://www.microquill.com/>

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	2	881	30.8	877	31.0	882	30.8	2	883	30.8	882	30.8	881	30.8
416.gamess	2	994	39.4	995	39.4	994	39.4	2	979	40.0	979	40.0	979	40.0
433.milc	2	689	26.6	689	26.6	689	26.6	2	689	26.6	689	26.6	689	26.6
434.zeusmp	2	490	37.2	487	37.4	487	37.4	2	463	39.4	463	39.4	462	39.4
435.gromacs	2	372	38.4	372	38.4	371	38.4	2	363	39.4	362	39.4	363	39.4
436.cactusADM	2	603	39.6	604	39.6	604	39.6	2	603	39.6	604	39.6	604	39.6
437.leslie3d	2	815	23.0	814	23.0	813	23.2	2	791	23.8	789	23.8	790	23.8
444.namd	2	494	32.6	493	32.6	492	32.6	2	493	32.6	493	32.6	493	32.6
447.dealII	2	548	41.8	547	41.8	548	41.8	2	495	46.2	495	46.2	492	46.4
450.soplex	2	661	25.2	664	25.2	666	25.0	2	660	25.2	662	25.2	659	25.4
453.povray	2	222	48.0	222	48.0	222	47.8	2	176	60.4	176	60.4	176	60.4
454.calculix	2	410	40.2	410	40.2	410	40.2	2	406	40.6	408	40.4	406	40.6
459.GemsFDTD	2	1052	20.2	1020	20.8	1023	20.8	2	1069	19.8	1045	20.2	1068	19.8
465.tonto	2	559	35.2	560	35.2	558	35.2	2	514	38.2	513	38.4	514	38.2
470.lbm	2	1556	17.6	1556	17.6	1556	17.6	1	541	25.4	543	25.3	542	25.4
481.wrf	2	657	34.0	649	34.4	654	34.2	2	657	34.0	649	34.4	654	34.2
482.sphinx3	2	965	40.4	957	40.8	958	40.8	2	950	41.0	944	41.2	946	41.2

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

General Notes

Tested systems can be used with Shin-G ATX case,
Antec Truepower Trio power supply TP3-650
Binaries were built on Windows Vista Ultimate (32-bit)

Base Compiler Invocation

C benchmarks:
icl -Qvc9 -Qc99

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Base Compiler Invocation (Continued)

C++ benchmarks:

icl -Qvc9

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icl -Qvc9 -Qc99 ifort

Base Portability Flags

436.cactusADM: -Qlowercase /assume:underscore
444.namd: -TP
447.dealII: -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
453.povray: -DSPEC_CPU_WINDOWS_ICL
454.calculix: -DSPEC_CPU_NOZMODIFIER -Qlowercase
481.wrf: -DSPEC_CPU_WINDOWS_ICL

Base Optimization Flags

C benchmarks:

-QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch /F1000000000

C++ benchmarks:

-QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qcxx-features
/F1000000000 shlw32m.lib -link /FORCE:MULTIPLE

Fortran benchmarks:

-QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch /F1000000000

Benchmarks using both Fortran and C:

-QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch /F1000000000

Peak Compiler Invocation

C benchmarks:

icl -Qvc9 -Qc99

C++ benchmarks:

icl -Qvc9

Fortran benchmarks:

ifort

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Peak Compiler Invocation (Continued)

Benchmarks using both Fortran and C:
icl -Qvc9 -Qc99 ifort

Peak Portability Flags

436.cactusADM: -Qlowercase /assume:underscore
444.namd: -TP
447.dealII: -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
453.povray: -DSPEC_CPU_WINDOWS_ICL
454.calculix: -DSPEC_CPU_NOZMODIFIER -Qlowercase
481.wrf: -DSPEC_CPU_WINDOWS_ICL

Peak Optimization Flags

C benchmarks:

433.milc: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Oa /F1000000000

470.lbm: -QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch
/F1000000000

482.sphinx3: -QxSSE4.1 -Qipo -O3 -Qprec-div- -Qunroll2 /F1000000000

C++ benchmarks:

444.namd: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Oa /F1000000000 shlw32m.lib
-link /FORCE:MULTIPLE

447.dealII: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias
-Qscalar-rep- /F1000000000 shlw32m.lib
-link /FORCE:MULTIPLE

450.soplex: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- /F1000000000 shlw32m.lib
-link /FORCE:MULTIPLE

453.povray: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qunroll4 -Qansi-alias /F1000000000
shlw32m.lib -link /FORCE:MULTIPLE

Fortran benchmarks:

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Peak Optimization Flags (Continued)

410.bwaves: -QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch /F1000000000

416.gamess: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll2 -Ob0 -Qansi-alias -Qscalar-rep- /F1000000000

434.zeusmp: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- /F1000000000

437.leslie3d: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qopt-prefetch /F1000000000

459.GemsFDTD: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll2 -Ob0 -Qopt-prefetch /F1000000000

465.tonto: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll4 -Qauto /F1000000000

Benchmarks using both Fortran and C:

435.gromacs: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qopt-prefetch /F1000000000

436.cactusADM: basepeak = yes

454.calculix: -QxSSE4.1 -Qipo -O3 -Qprec-div- /F1000000000

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-win32-revA.20090710.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-win32-revA.20090710.xml>

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For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.

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